Tentative

TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP3617

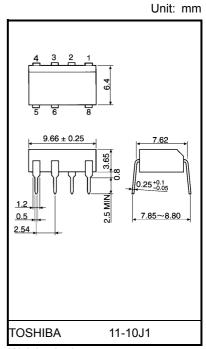
Triac Drivers
Programmable Controllers
AC-Output Modules
Solid-State Relays

The TOSHIBA TLP3616 consists of a photo-triac optically coupled to a gallium arsenide infrared-emitting diode in an 8-lead plastic DIP package.

Peak off-state voltage: 600 V (min.)
 Trigger LED current: 10 mA (max.)
 On-state current: 1.2 A_{rms} (max.)
 Isolation voltage: 2500 V_{rms} (min.)

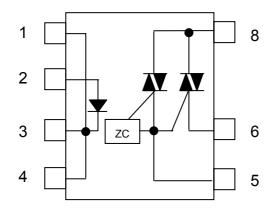
• Zero-crossing function

• UL recognized: UL1577, File No. E67349



Weight: 0.59 g

Pin Configuration (top view)



1: Cathode

2: Anode

3: Cathode

4: Cathode

5: Triac gate

6: Triac T1

8: Triac T2

Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit	
	Forward current	l _F	50	mA		
	Forward current derating (Ta ≥ 53°C)	ΔI _F / °C	-0.7	mA / °C		
LED	Peak forward current (100-µs pulse,	I _{FP}	1	Α		
	Reverse voltage	V _R	5	V		
	Junction temperature	Tj	125	°C		
	Off–state output terminal voltage	V_{DRM}	600	V		
	On–state RMS current	Ta = 25°C	I=(0.40)	1.2	Α	
_		Ta = 40°C	I _{T(RMS)}	1.0		
Detector	On–state current derating (Ta ≥ 40°C	ΔI _T / °C	-13	mA / °C		
Det	Peak current from snubber circuit (100-µs pulse, 120 pps)	I _{SP}	2	А		
	Peak nonrepetitive surge current (50	I _{TSM}	5	Α		
	Junction temperature	Tj	115	°C		
Stor	age temperature range	T _{stg}	-40~125	°C		
Operating temperature range			T _{opr}	-20~80	°C	
Lead soldering temperature (10 s)			T _{sol}	260	°C	
Isola	tion voltage (AC, 1 min., R.H. ≤ 60%)	BVS	2500	V _{rms}		

Note: Device considered a two–terminal device: Pins 1, 2, 3 and 4 are shorted together, and pins 5, 6 and 8 are shorted together.

2

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V_{AC}	_	_	240	V _{ac}
Forward current	I _F	15	20	25	mA
Peak current from snubber circuit	I _{SP}	_	_	1	Α
Operating temperature	T _{opr}	-20	_	80	°C

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	V _F	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse current	I _R	V _R = 5 V	-	1	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz	_	30	_	pF
Detector	Peak off-state current	I _{DRM}	V _{DRM} = 600 V, Ta = 110°C	_	-	100	μΑ
	Peak on-state voltage	V _{TM}	I _{TM} = 1.2 A	1	1	3.0	٧
	Critical rate of rise of off–state voltage	dv / dt	$V_{in} = 240 V_{rms}$ (Fig. 1)	ı	500	ı	V / µs
	Critical rate of rise of commutating voltage	dv / dt (c)	$V_{in} = 240 V_{rms}, I_T = 0.5 A_{rms}$ (Fig. 1)		5		V / µs

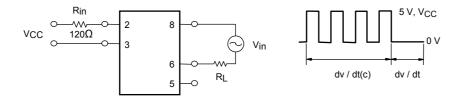
Coupled Electrical Characteristics (Ta = 25°C)

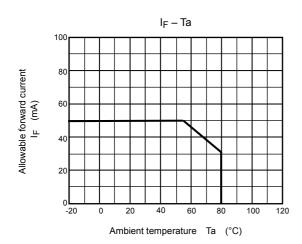
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I _{FT}	V _T = 3 V	_	_	10	mA
Inhibit voltage	V _{IH}	I _F = Rated I _{FT}	_	_	20	٧
Leakage in inhibited state	lін	I _F = Rated I _{FT} V _T = Rated V _{DRM}	_	200	_	μΑ

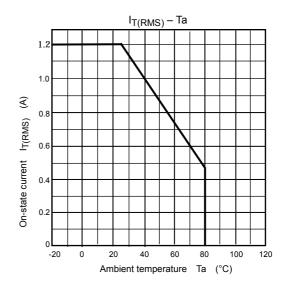
Isolation Characteristics (Ta = 25°C)

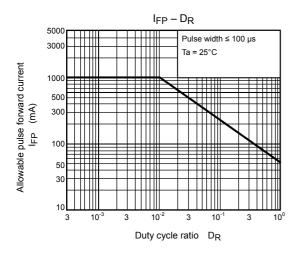
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	C _S	V _S = 0, f = 1 MHz	_	1.5	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≤ 60%	5×10 ¹⁰	10 ¹⁴	_	Ω
	BVS	AC, 1 minute	2500	_	_	Vrms
Isolation voltage		AC, 1 second, in oil	_	5000	_	VIIIIS
		DC, 1 minute, in oil	_	5000	_	V _{dc}

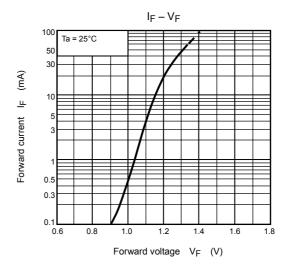
Fig. 1: dv / dt test circuit

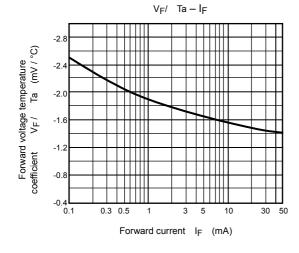


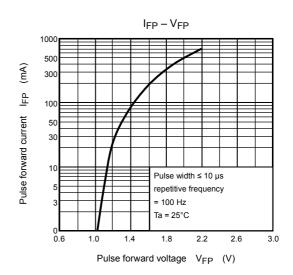


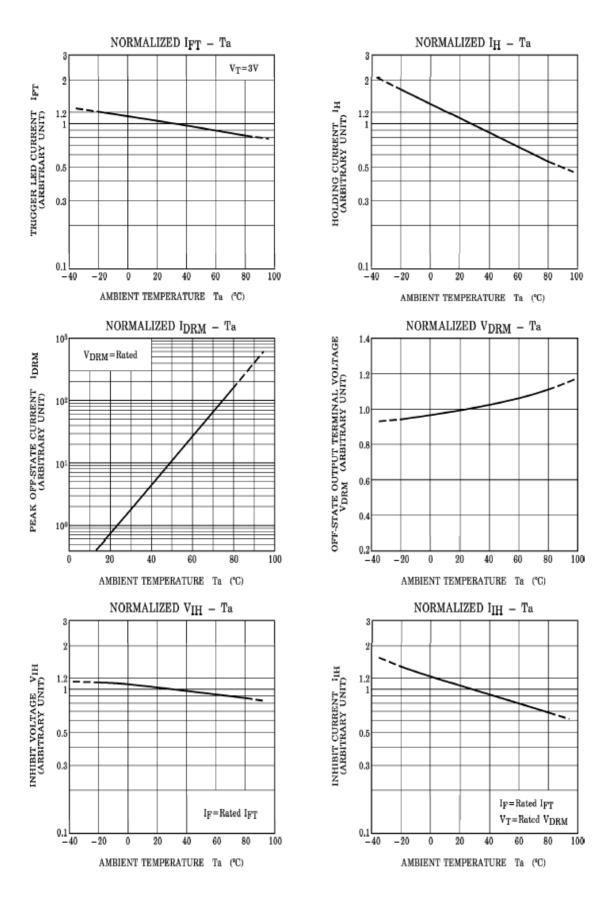












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