## FEATURES

- 2 Independent Asynchronous Buck Converters in SO-8
- Internally compensated
- Built in soft start
- Enable with Zero current shut down
- 400 KHz switching per output
- Interleaved oscillator to reduced supply ripple
- Simply re-configures to 2 phase single output
- Up to 4A output in 2 phase mode
- Independent hiccup current limit


## APPLICATIONS

- Audio Power Amplifiers
- Portable (Notebook) Computers
- Point of regulation for high performance electronics
- Consumer Electronics
- Desktop Computers
- LCD TVs and LCD monitors
- Distributed Power Systems
- Battery Chargers
- Pre-Regulator for Linear Regulators


## GENERAL DESCRIPTION

AMS4122 is a dual switching regulator capable to deliver up to 2A for each channel. Internally Compensated internal Soft Start makes AMS4122 one of the simplest and easiest switching regulators to use with the minimum external components. Each regulator has independent hiccup current limit. The Enable pin turns both devices on when a voltage of 2.5 V or higher it is applied to EN pin. Since both devices share one single package the thermal protection turns off both devices when the die temperature exceeds approximately $135^{\circ} \mathrm{C}$. Both regulators are adjustable using a 0.600 V reference for low output voltage settings. The oscillators are $180^{\circ}$ phase to each other to reduce the ripples on the input power supply and minimize the power dissipation on the package. The device can be easily configured for Two Phase single output switching regulator capable of 5A output load. By applying a 2.5 V or greater on feedback pin of the second switching regulator the device enters in Two Phase Mode. The first benefit of having a Two Phase device is reducing the output ripples by a factor of four as the single phase. The second benefit is higher output current capability at overall low cost. AMS4122 combined with AMS4123 (2A Switching Regulator + 1A LDO in SO-8) makes complete solution for LCD TV power requirements.

## ORDERING INFORMATION

| Package Type <br> SOIC EDP | TEMP. RANGE |
| :--- | :--- |
| AMS4122S | $-25^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ |

## PINCONNECTIONS

## TYPICAL APPLICATION



8L SOIC
SO Package (S)


Top View

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PIN DESCRIPTION

| Pin Number | NAME | DESCRIPTION |
| :---: | :--- | :--- |
| 1 | SW2 | This connects the inductor the Internal Switch2 |
| 2 | P2VIN | Input Power Pin for SW2 from unregulated power supply |
| 3 | GND | Ground Pin connected to PCB ground plane. This pin is also the ground for internal <br> voltage reference. |
| 4 | F/B2 | Feedback2. A resistor network of two resistors is used to set-up the output voltage <br> connected between Output2 to GND. The node between the two resistors is connected to <br> FFeedback2 pin. In Two Phase Mode F/B2 should be connected to a voltage 2.5V or <br> higher |
| 5 | F/B1 | Feedback1. A resistor network of two resistors is used to set-up the output voltage <br> connected between Output1 to GND. The node between the two resistors is connected to <br> Feedback1 pin. |
| 6 | EN | Enable. A voltage greater than 2.5V at this pin enables both devices operation. |
| 7 | P1+AVIN | Input Power Pin for SW1 and Analog Input Voltage that powers up the chip |
| 8 | SW1 | This connects the inductor the Internal Switch1 |

RECOMMANDED OPERATING CONDITIONS: The minimum input voltage should be 4.5 V , however the device will function as low as 4.0 V . The maximum operating voltage should be 20 V , the device will work at 22 V but the switching loss is higher. The best performance is at 1.5 A output load per channel; however it works well with 2 A load per channel. The minimum supply voltage is Vout +1 Vfor 1A load and Vout +1.1 V for 2 A load but must be higher than 4.2 V . There is an internal UVLO that will turn off the device if the input voltage is below 4.0 V . When the input voltage exceeds 12 V the output load current should be maintain below 1.5 A , due to switching loss the device will have lower performance.

## ABSOLUTE MAXIMUM RATINGS

| V $_{\text {IN }}$ | 4.0 V to 20 V | Junction Temperature | $+150^{\circ} \mathrm{C}$ |
| :--- | ---: | :--- | ---: |
| F/B | -0.3 to +12.0 V | Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| EN | -0.3 V to +20 V | Lead temperature | $260^{\circ} \mathrm{C}$ |

## ELECTRICAL CHARACTERISTICS

Electrical Characteristics at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ and $\mathrm{VIN}=12 \mathrm{~V}$ (unless otherwise noted).

| PARAMETER | TEST CONDITIONS |  | AMS4122 <br> Typ. |  | Max. |
| :--- | :--- | :---: | :---: | :---: | :---: | Units

Notes: 1) When a 1.3 V or greater is applied to F/B2, the device enters in Two Phase Mode of operation.
2) The oscillators for each switching regulator are shifted by $180^{\circ}$ of each-other to accommodate dual phase operation and reduce the ripples in the input power supply.
3) The power dissipation for SO-8 EDP package is limited to 2.5 W rated at $25^{\circ} \mathrm{C}$ ambient temperatures. The thermal resistance Junction to Case is $45^{\circ} \mathrm{C} / \mathrm{W}$. Total power dissipation for both switching regulators should be taken in consideration when calculating the output current for each regulator.
4) The load current is specified for two output configuration. The output load it is dependent of the output voltage. The total output power should be kept under $6 \mathrm{~W} /$ channel to avoid thermal shut-down.

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## TYPICAL PERFORMANCE WAVEFORM



AMS4122 DUAL OUTPUT Vin=12V
Low Output Current


DUAL OUTPUT Vin=7V
Full Load Output Current


AMS4122 DUAL OUTPUT Vin=12V
Full Load Output Current


DUAL PHASE
Full Load Output Current

## TYPICAL APPLICATION

## 2 Phase Single Output Application

$\mathrm{V}_{\text {OUT }}=\mathrm{V}_{\text {REF }}(1+\mathrm{R} 1 / \mathrm{R} 2)$


Dual Output Application
$\mathrm{V}_{\text {OUT1 }}=\mathrm{V}_{\text {REF }}(1+\mathrm{R} 1 / \mathrm{R} 2)$
$\mathrm{V}_{\text {OUT2 }}=\mathrm{V}_{\text {REF }}(1+\mathrm{R} 3 / \mathrm{R} 4)$


PACKAGE DIMENSIONS inches (millimeters) unless otherwise noted.

8 LEAD SOIC PLASTIC PACKAGE (S)


RECOMMENDED LAYOUT PATTERN

