

10X2 Strings WLED Boost Converter

DESCRIPTION

The EUP2538 is a constant current step-up converter specifically designed to drive white LEDs. The Step-up converter topology allows series connection of the white LEDs so the LED currents are identical for uniform brightness. The EUP2538 switches at 1MHz, allowing the use of tiny external components. The output capacitor can be as small as 0.47 μ F, saving space and cost versus alternative solutions. A low 0.3V feedback voltage minimizes power loss in the current setting resistor for better efficiency. The EUP2538 high-voltage output stage is perfect for driving mid-size and large panel displays containing up to ten white LEDs in series. The 1.2A current limit and 0.65 Ω $R_{DS(on)}$ allows the part using for up to 10 $\times$ 2 string WLED lighting. LED dimming can be done by using a DC voltage, a logic signal, or a pulse width modulation(PWM) signal. The enable input pin allows the device to be placed in shutdown mode with "zero" quiescent current.

The EUP2538 is available in low profile 8pin 3mm $\times$ 3mm TDFN package.

FEATURES

- 2.6V to 5.5V Input Range
- 38V Output with Over Voltage Protection
- Internal Soft-Start
- PWM Dimming Control
- Internal High Power 40V MOSFET Switch
- Fast 1MHz Switching Frequency
- Using Low-Profile Inductors and Capacitors
- Available in 3mm $\times$ 3mm TDFN-8 Package
- RoHS Compliant and 100% Lead (Pb)-Free

APPLICATIONS

- GPS Navigation Systems
- Portable Media Players
- Handheld Devices, Digital Camera
- Portable Game Machines

Typical Application Circuit

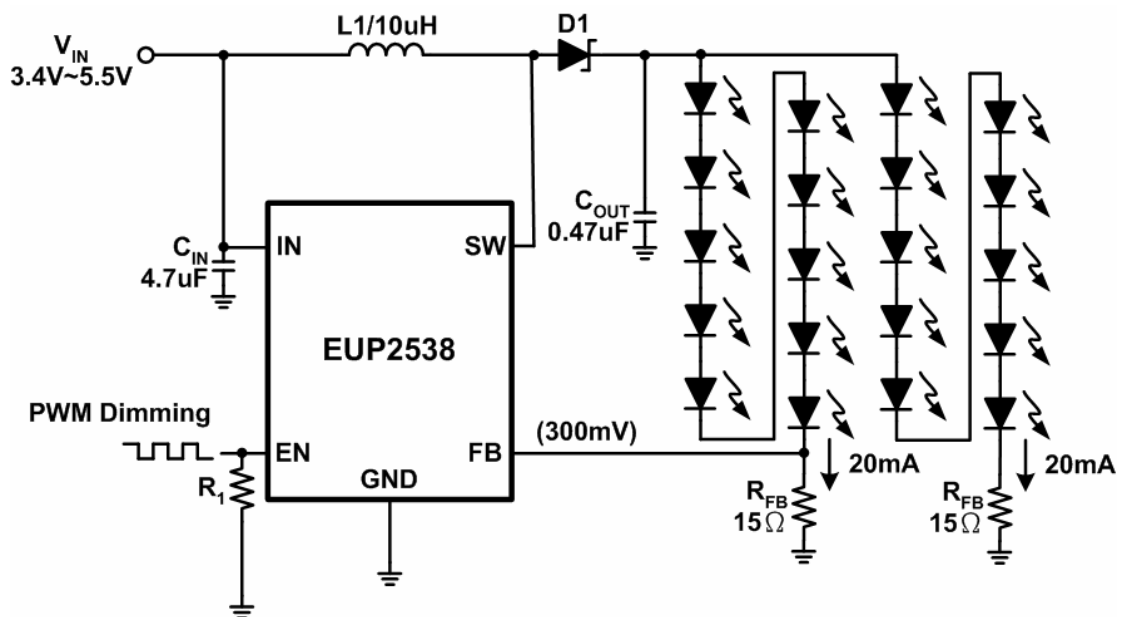
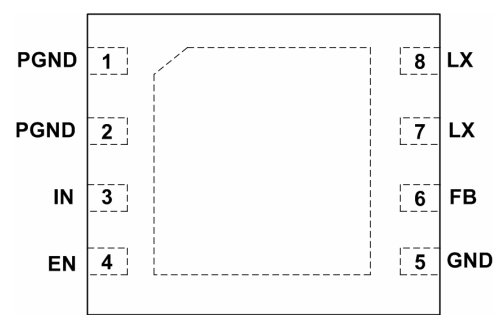


Figure 1. White LED Application

Pin Configurations

Package Type	Pin Configurations
TDFN-8	 Diagram illustrating the Pin Configurations for the TDFN-8 package. The package is shown with pins 1 through 8. The pins are labeled as follows: - Pin 1: PGND - Pin 2: PGND - Pin 3: IN - Pin 4: EN - Pin 5: GND - Pin 6: FB - Pin 7: LX - Pin 8: LX

Pin Description

PIN	TDFN-8	DESCRIPTION
PGND	1,2	Power Ground. Connect to GND and the exposed pad directly under the IC.
IN	3	Input Supply Voltage
EN	4	Chip Enable Pin. Connect to 1.4V or higher to enable device, 0.3V or less to disable device.
GND	5	Common Ground. Connect the pin to the ground plane.
FB	6	Feedback Pin. Reference voltage is 0.3V. Connect cathode of lowest LED and resistor here. Calculate resistor value according to the formula: $R_{FB}=0.3/I_{LED}$
LX	7,8	Switch Pin. This is the drain of the internal power switch. Connect inductor/diode here. Minimize trace area at this pin to reduce EMI.

Ordering Information

Order Number	Package Type	Marking	Operating Temperature Range
EUP2538JIR1	TDFN-8	XXXXXX P2538	-40 °C to +85°C

EUP2538 □ □ □ □

Lead Free Code
1: Lead Free 0: Lead

Packing
R: Tape & Reel

Operating temperature range
I: Industry Standard

Package Type
J: TDFN

Block Diagram

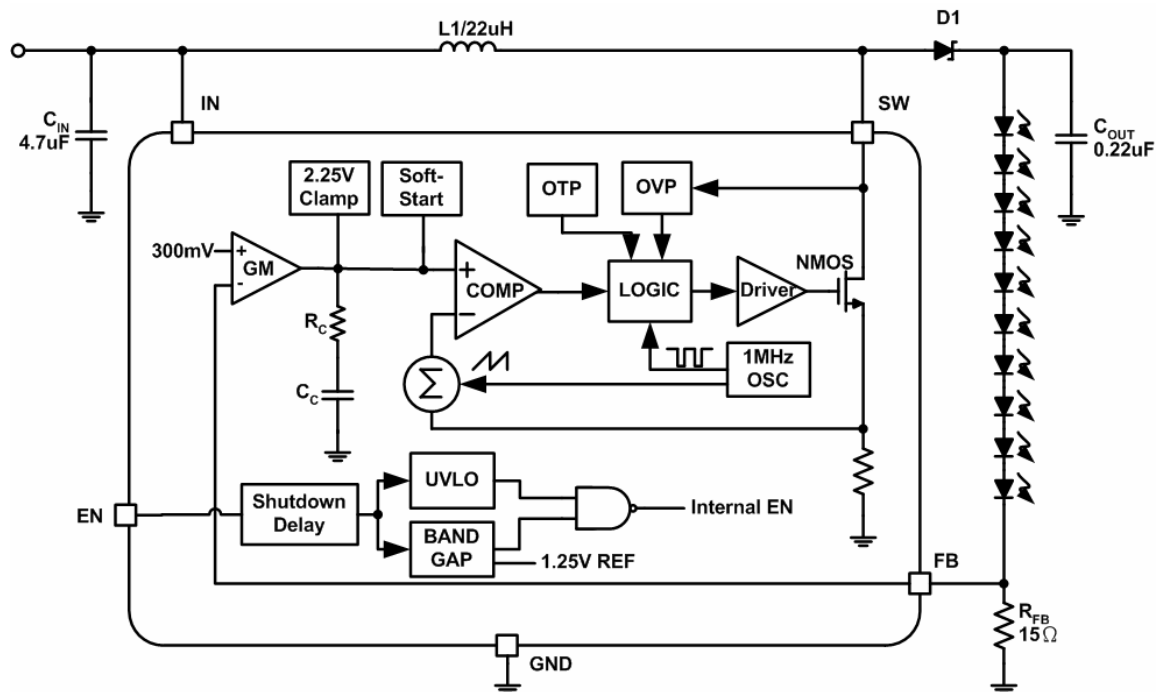


Figure 2.

Absolute Maximum Ratings (1)

■ Supply Voltage, V_{IN}	-----	-0.3V to 6V
■ SW	-----	-0.3V to 40V
■ The Other Pins	-----	-0.3V to 6V
■ Package Thermal Resistance		
TDFN-8, θ_{JA}	-----	39.1°C/W
■ Maximum Junction Temperature	-----	125°C
■ Lead Temperature (Soldering, 10sec.)	-----	260°C
■ Storage Temperature Range	-----	-65°C to +150°C

Recommend Operating Conditions (2)

■ Junction Temperature Range	-----	-40°C to +85°C
■ Supply Voltage, V_{IN}	-----	2.6V to 5.5V

Note (1): Stress beyond those listed under “Absolute Maximum Ratings” may damage the device.

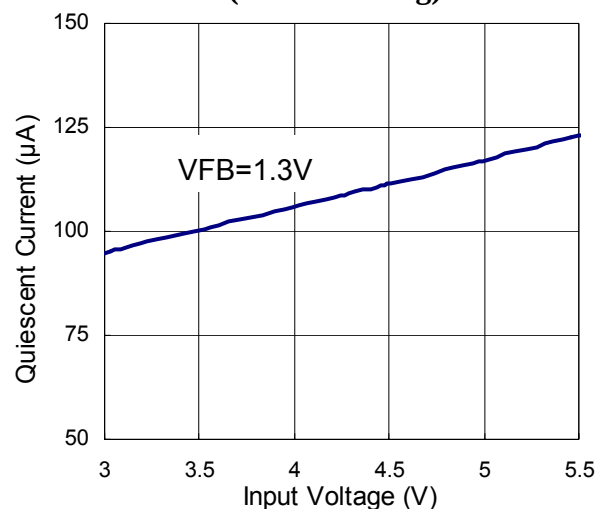
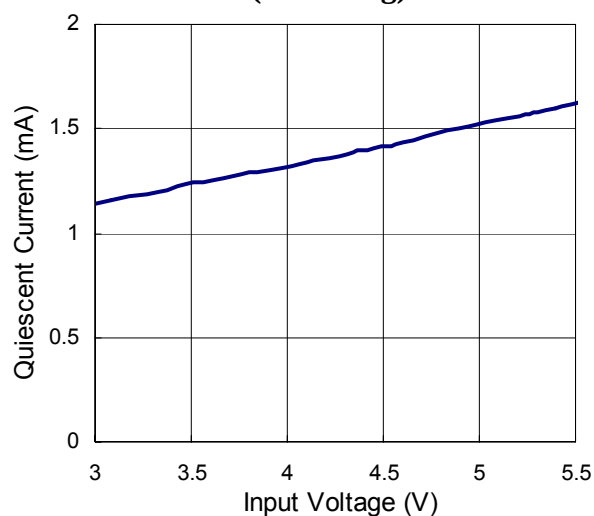
Note (2): The device is not guaranteed to function outside the recommended operating conditions.

Electrical Characteristics

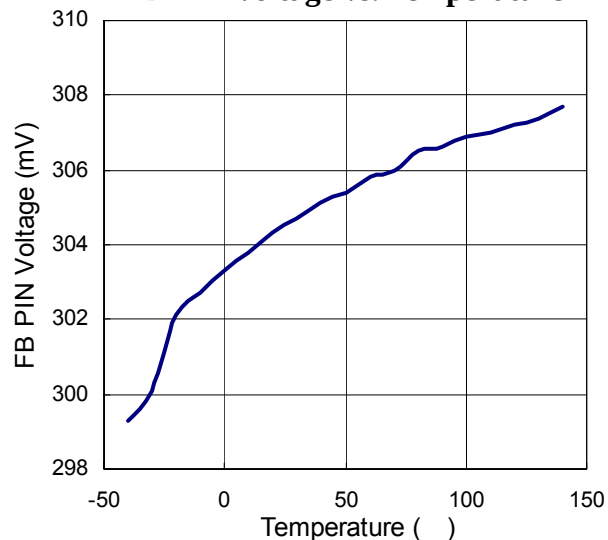
($V_{IN}=3.6V$, $V_{OUT}=34V$, $C_{OUT}=0.22\mu F$, $C_{IN}=2.2\mu F$, $L1=22\mu H$, $R_{FB}=15\Omega$, $T_A=-40^\circ C$ to $+85^\circ C$. Unless otherwise noted.
Typical values are at $T_A=+25^\circ C$)

Symbol	Parameter	Conditions	EUP2538			Unit
			Min	Typ	Max.	
UVLO	Under Voltage Lock Out	Rising	2.2	2.4	2.6	V
	Maximum Output Voltage	No Switching			40	V
I_{CC1}	Supply Current	$V_{CC}=5.5V$, Continuous Switching		1.7	2.6	mA
I_{CC2}	Quiescent Current	$V_{CC}=5.5V$, $FB=1.3V$, No Switching		130	170	μA
I_{CC3}	Shutdown current	$V_{CC}=5.5V$, $V_{EN}<0.4V$		0.1	1	μA
Oscillator						
Fosc	Operation Frequency		0.8	1	1.3	MHz
Dmax	Maximum Duty Cycle			92		%
Reference Voltage						
V_{FB}	Feedback Voltage		285	300	315	mV
MOSFET						
$R_{ds(on)}$	On resistance of MOSFET			0.65	1.3	Ω
ILX	Current Limit		0.85	1.2	1.6	A
Control and Protection						
V_{EN1}	Shutdown Voltage		0.4	0.7		V
V_{EN2}	Enable Voltage			0.7	1.2	V
I_{EN}	EN Pin Pull Low Current			0.1	1	μA
OVP	OVP Threshold			38		V

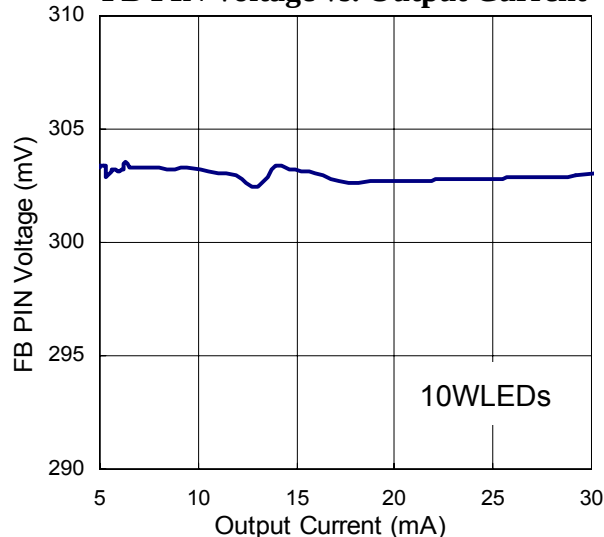
Typical Operating Characteristic

Quiescent Current vs. VIN
(Not Switching)Quiescent Current vs. VIN
(Switching)

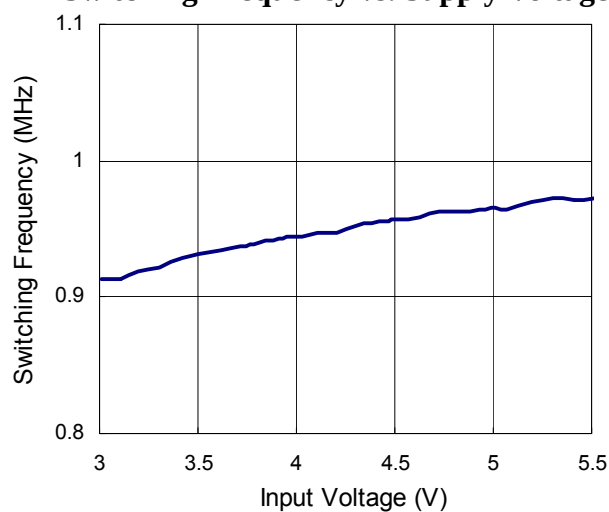
FB PIN Voltage vs. Temperature



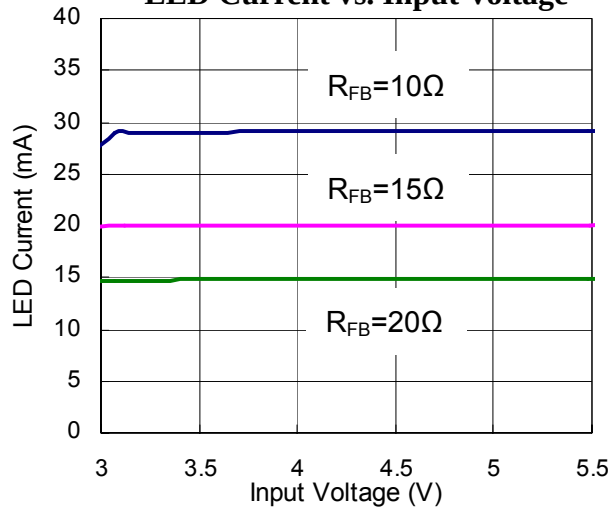
FB PIN Voltage vs. Output Current



Switching Frequency vs. Supply Voltage

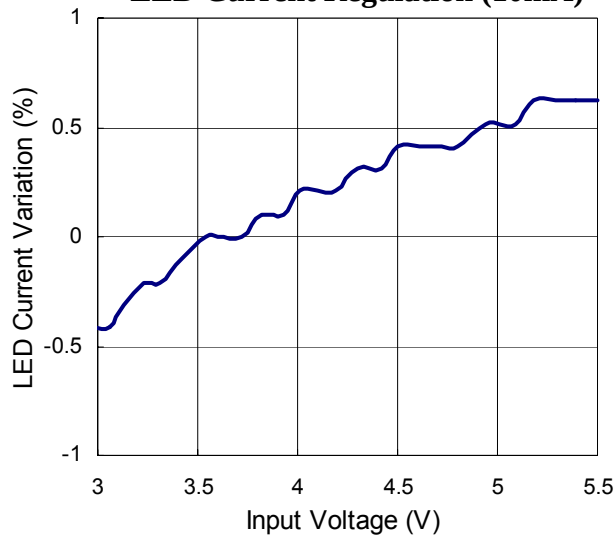


LED Current vs. Input Voltage

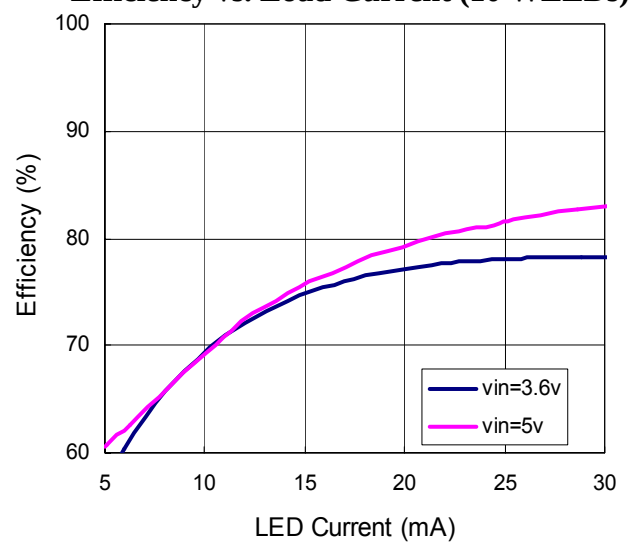


Typical Operating Characteristics (continued)

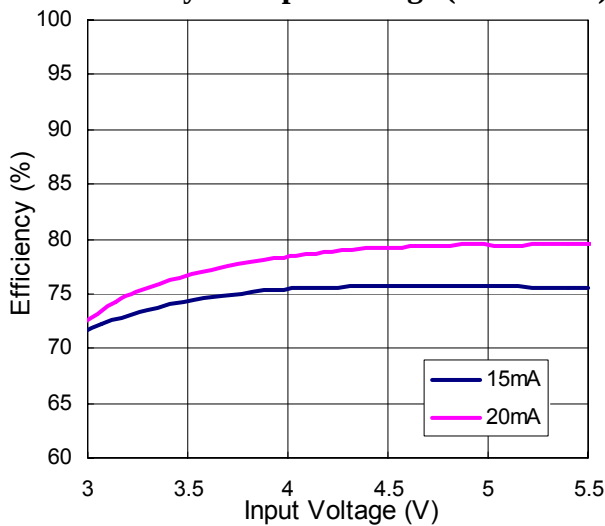
LED Current Regulation (10mA)



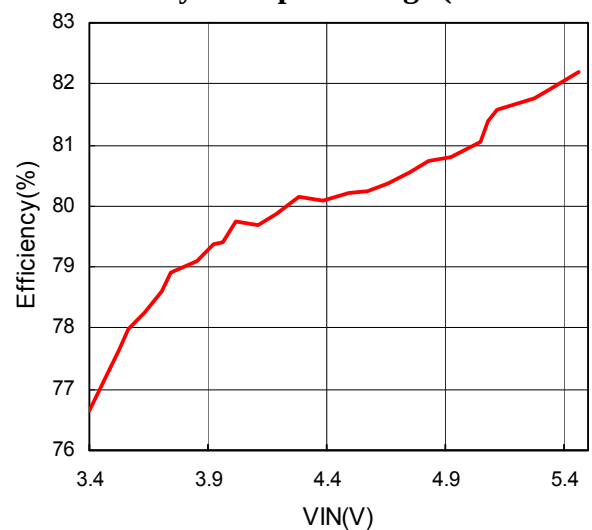
Efficiency vs. Load Current (10 WLEDs)



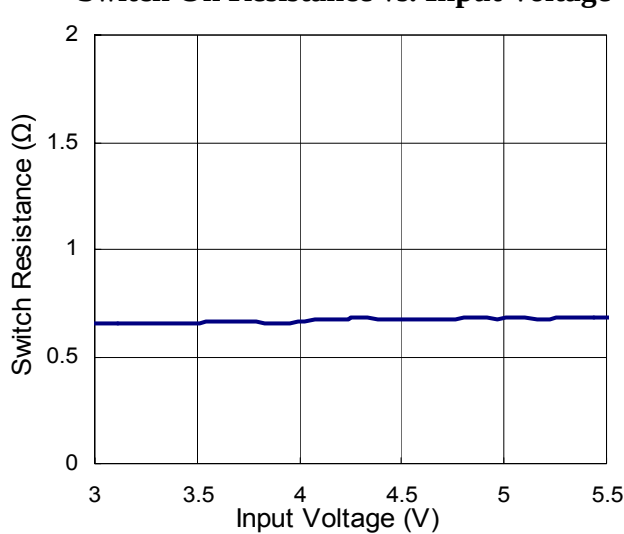
Efficiency vs. Input Voltage (10WLEDs)



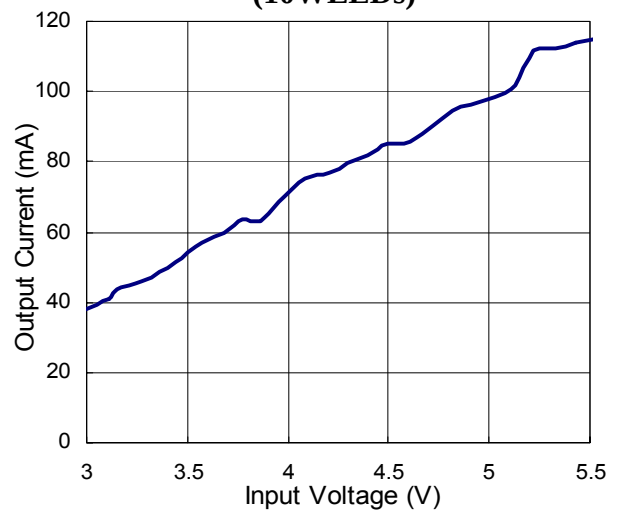
Efficiency vs. Input Voltage (20WLEDs)



Switch On Resistance vs. Input Voltage

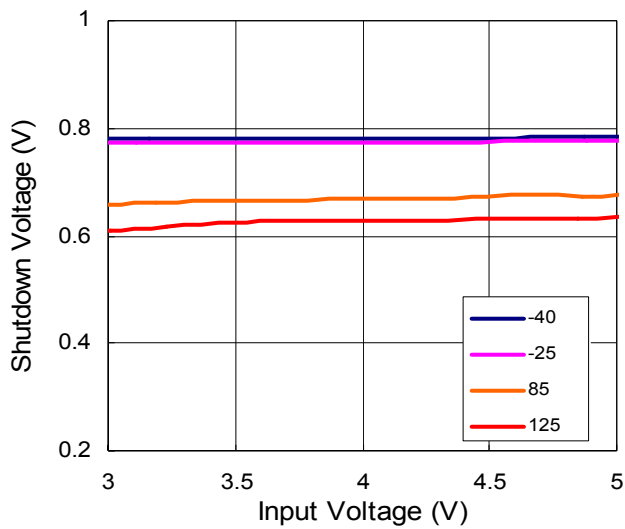


Maximum Output Current vs. Input Voltage (10WLEDs)

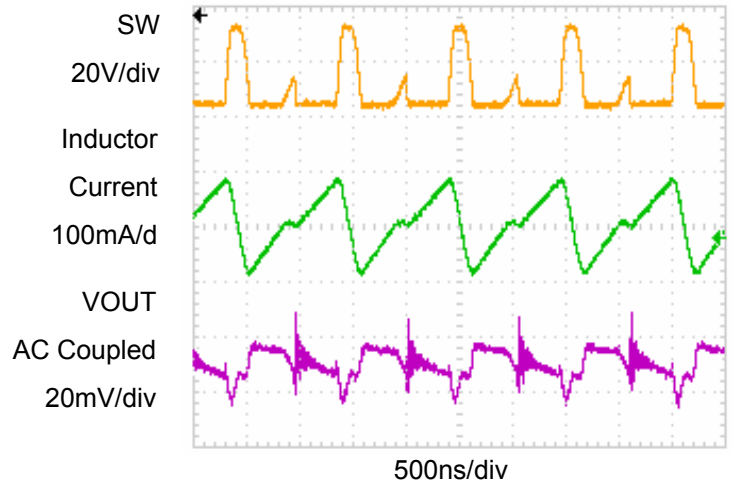


Typical Operating Characteristics (continued)

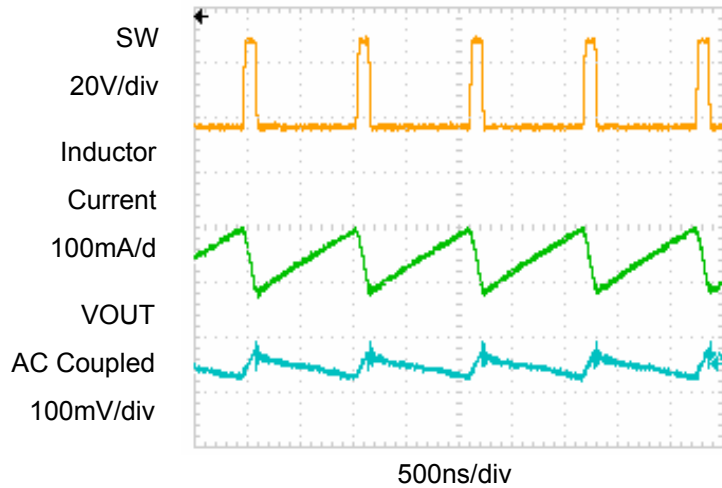
Shutdown Voltage vs. Input Voltage



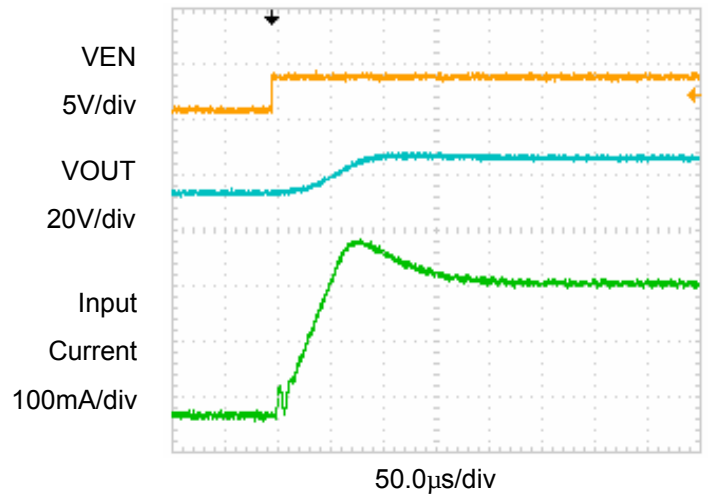
Light Load Switching Waveform VIN=5V, ILED=1.5mA, L=22μH



Typical Switching Waveforms



Power-up With 10 LEDs at 20mA



Application Information

LED Current Control

The EUP2538 regulates the LED current by setting the current sense resistor (R_{FB}) connecting to feedback and ground. The internal feedback reference voltage is 0.3V. The LED current can be set from following equation easily.

$$I_{LED} = \frac{0.3}{R_{FB}} \quad (1)$$

In order to have an accurate LED current, precision resistors are preferred (1% is recommended). The table for R_{FB} selection is shown below.

R _{FB} Resistor Value selection	
LED Current (mA)	R _{FB} (Ω)
5	60
10	30
15	20
20	15
25	12
30	10

Dimming Control

a. Using a PWM Signal to EN Pin

For controlling the LED brightness, the EUP2538 can perform the dimming control by applying a PWM signal to EN pin, and the PWM signal frequency range is from 100Hz to 100KHz. The average LED current is proportional to the PWM signal duty cycle. The magnitude of the PWM signal should be higher than the maximum enable voltage of EN pin, in order to let the dimming control perform correctly.

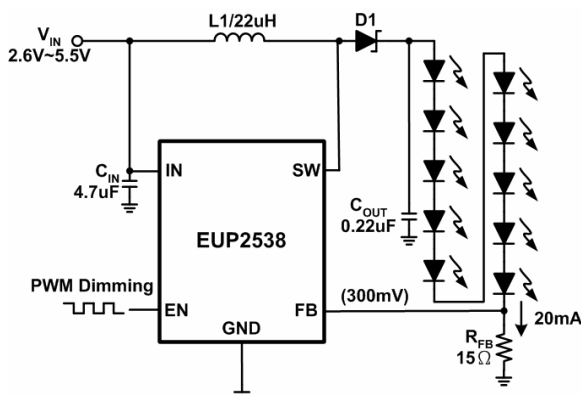


Figure 3. Direct PWM Dimming Control

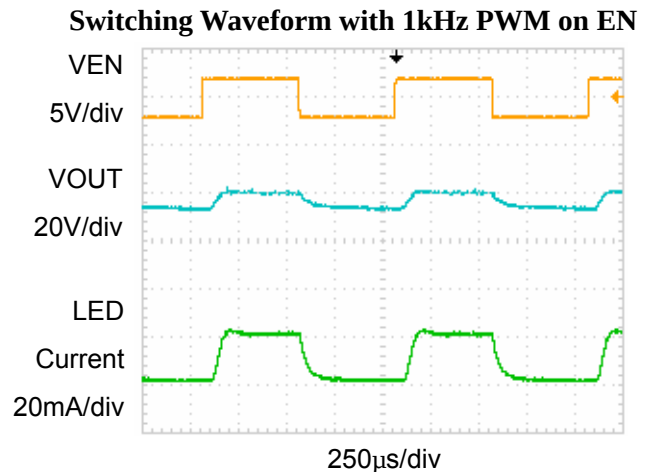


Figure 4.

b. Using a DC Voltage

Using a variable DC voltage to adjust the brightness is a popular method in some applications. The dimming control using a DC voltage circuit is shown in Figure 5. According to the Superposition Theorem, as the DC voltage increases, the voltage contributed to V_{FB} increases and the voltage drop on R_2 decreases, i.e. the LED current decreases. For example, if the V_{DC} range is from 0V to 2.8V, the selection of resistors in Figure 5 sets dimming control of LED current from 20mA to 0mA.

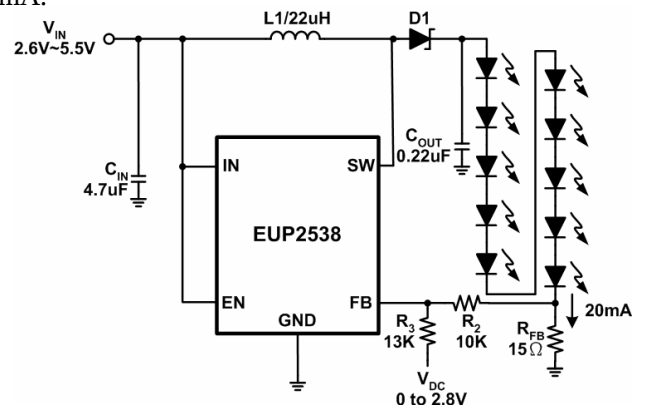


Figure 5. DC Voltage Dimming Control

c. Using a Filtered PWM Signal

Another common application is using a filtered PWM signal as an adjustable DC voltage for LED dimming control. A filtered PWM signal acts as the DC voltage to regulate the output current. The recommended application circuit is shown in the Figure 6. In this circuit, the output ripple depends on the frequency of PWM signal. For smaller output voltage ripple (<100mV), the recommended frequency of 2.8V PWM signal should be above 2kHz. To fix the frequency of PWM signal and change the duty cycle of PWM signal can get different output current. According to the application circuit of Figure 6, output current is from 20.5mA to 5.5mA by adjusting the PWM duty cycle from 10% to 90%.

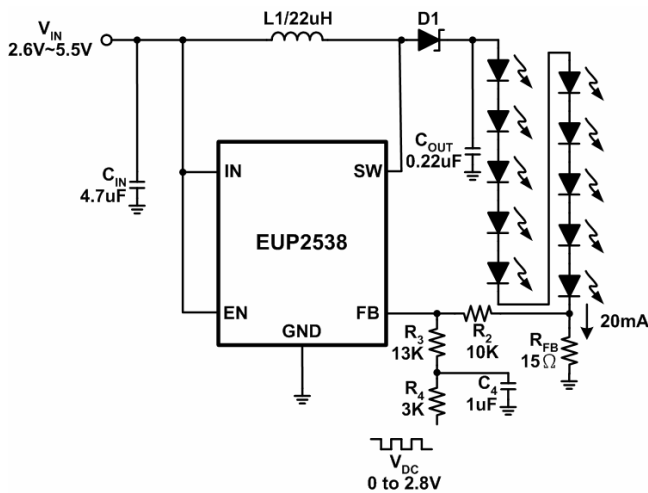


Figure 6. Filtered PWM Dimming Control

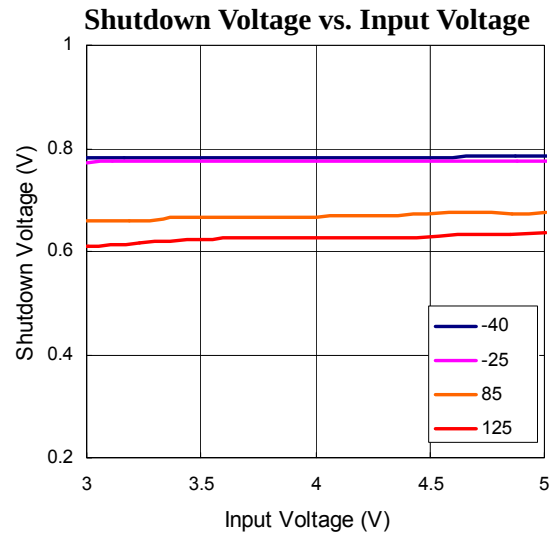


Figure 9.

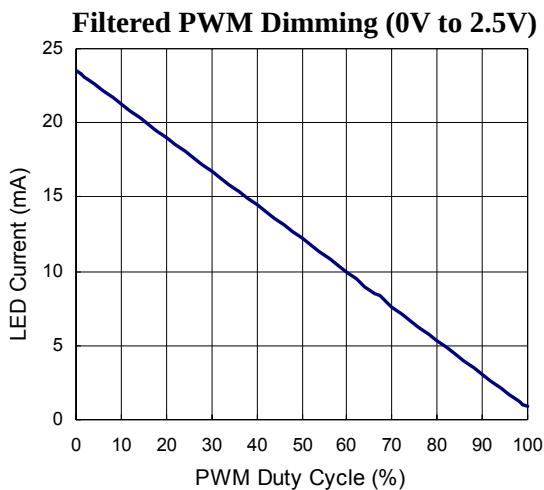


Figure 7.

Open Load Shutdown

In the event of an “Open LED” fault condition, the EUP2538 will continue to boost the output voltage with maximum power until the output voltage reaches approximately 38V. Once the output exceeds this level, the device will cease operation until the EN pin is cycled off and on.

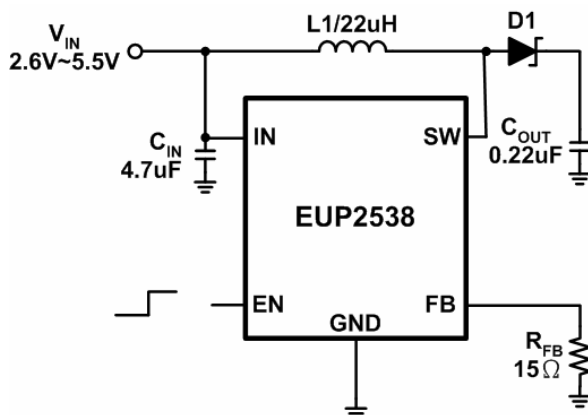


Figure 8. Open LED Protection

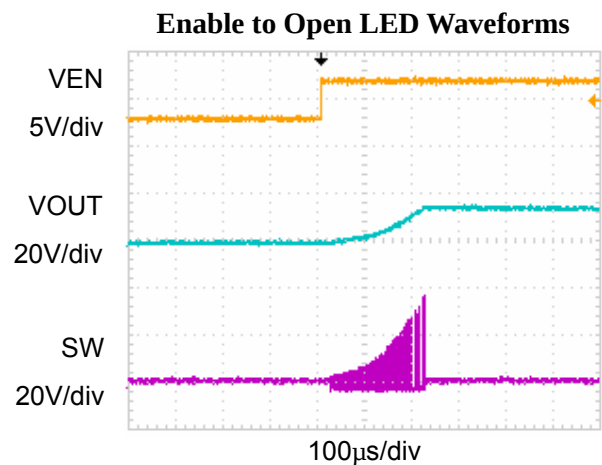


Figure 10.

Thermal Shutdown

Thermal overload protection circuitry has been included to prevent the device from operating at unsafe junction temperatures above 150°C. In the event of a thermal overload condition the device will automatically shutdown and wait till the junction temperatures cools to 130°C before normal operation is resumed.

Capacitors Selection

A 4.7μF to 10μF ceramic input capacitor (C_{IN}) and a 0.22μF to 4.7μF ceramic output capacitor (C_{OUT}) are sufficient for most applications. During Direct PWM Dimming control, a larger output capacitor will significantly reduce audio noise induced by output capacitor, and a smaller will enlarge the audio noise, a 2.2μF C_{OUT} is recommended. Under normal condition, a 4.7μF input capacitor is sufficient. For applications with higher output power, a larger input capacitor of 10μF may be appropriate. X5R and X7R capacitor types are ideal due to their stability across temperature range.

Inductor Selection

The recommended value of inductor for 2 to 10 WLEDs applications are 4.7 μ H to 47 μ H. Small size and better efficiency are the major concerns for portable device, such as EUP2538 used for mobile phone. The inductor should have low core loss at 1MHz and low DCR for better efficiency. To avoid inductor saturation current rating should be considered.

Schottky Diode Selection

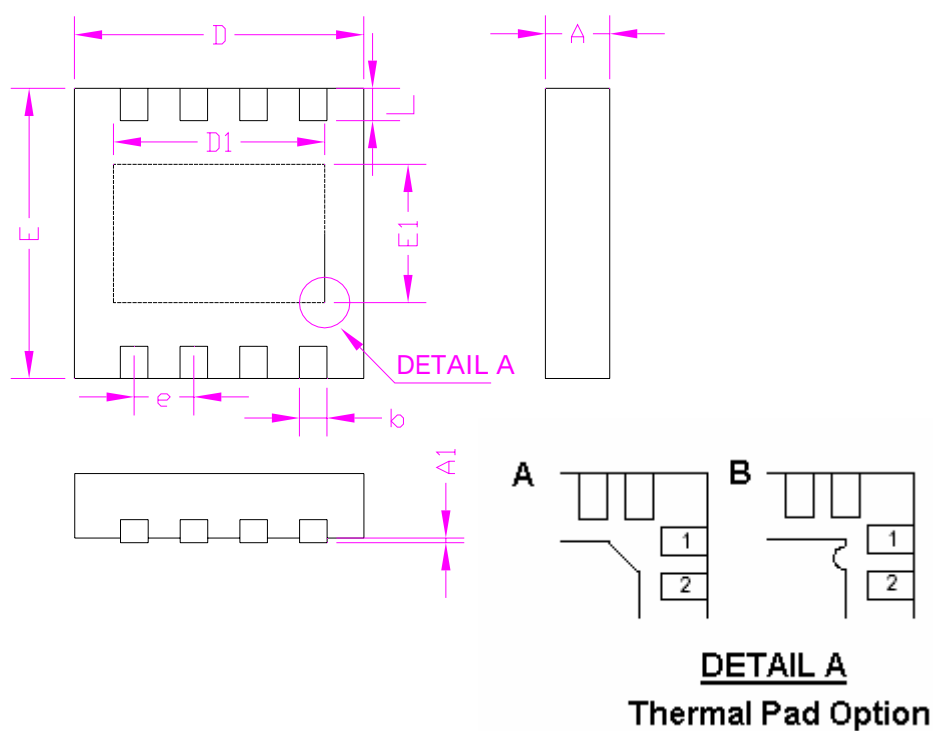
The current rating of the Schottky diode must exceed the peak current flowing through it. The Schottky diode performance is rated in terms of its forward voltage at a given current. In order to achieve the best efficiency, this forward voltage should be as low as possible. The response time is also critical since the driver is operating at 1MHz.

Board Layout

Careful PC board layout is required due to fast switching. All components must be placed as close to the device as possible. Keep the path between the inductor L1, diode D1, and output capacitor C_{OUT} extremely short for minimal noise and ringing. The feedback components such as the sense resistor R_{FB} must be kept close to the FB pin to prevent noise injection on the FB pin trace. The ground return of C_{IN} and C_{OUT} should be tied close to the GND pin. See the EUP2538 demo board layout for reference.

Packaging Information

TDFN-8



SYMBOLS	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	0.80	0.028	0.031
A1	0.00	0.05	0.000	0.002
b	0.20	0.40	0.008	0.016
D	2.90	3.10	0.114	0.122
D1	2.30		0.090	
E	2.90	3.10	0.114	0.122
E1	1.50		0.059	
e	0.65		0.026	
L	0.25	0.45	0.010	0.018