# RF Power MOSFET Transistor 120W, 2-175MHz, 28V



- N-Channel enhancement mode device
- DMOS structure

Parameter

Drain-Source Voltage

Gate-Source Voltage

**Drain-Source Current** 

Junction Temperature

Storage Temperature

Thermal Resistance

F (MHz)

30

50

100

TYPICAL DEVICE IMPEDANCE

Power Dissipation

Lower capacitances for broadband operation

ABSOLUTE MAXIMUM RATINGS AT 25° C

Symbol

VDS

V<sub>GS</sub>

 $I_{DS}$ 

 $\mathbf{P}_{\mathsf{D}}$ 

ТJ

TSTG

 $\theta_{JC}$ 

Z<sub>IN</sub> (Ω)

3.0 - j12.5

1.5 - j8.5

1.0 - j6.0

V<sub>DD</sub> = 28V, I<sub>DQ</sub> = 600mA, P<sub>OUT</sub> = 120 W

Rating

65

20

12

250

200

-55 to +150

0.7

 $Z_{LOAD}(\Omega)$ 

8.0 + j6.0

7.0 +j6.5

6.5 + j5.0

Units

V

V

А

W

°C

°C

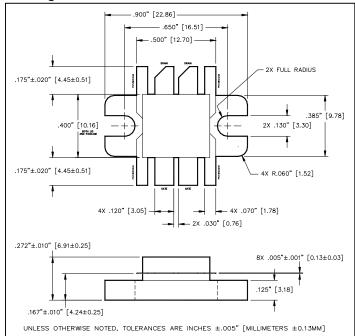
°C/W

- High saturated output power
- Lower noise figure than bipolar devices



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### Package Outline



 $Z_{\ensuremath{\text{IN}}}$  is the series equivalent input impedance of the device from gate to source.

 $Z_{\mbox{\scriptsize LOAD}}$  is the optimum series equivalent load impedance as measured from drain to ground.

Parameter	Symbol	Min	Max	Units	Test Conditions
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	65	-	V	V <sub>GS</sub> = 0.0 V , I <sub>DS</sub> = 3.0 mA
Drain-Source Leakage Current	I <sub>DSS</sub>	-	6.0	mA	$V_{GS} = 28.0 \text{ V}$ , $V_{GS} = 0.0 \text{ V}$
Gate-Source Leakage Current	I <sub>GSS</sub>	-	6.0	μA	$V_{GS} = 20.0 \text{ V}$ , $V_{DS} = 0.0 \text{ V}$
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2.0	6.0	V	V <sub>DS</sub> = 10.0 V , I <sub>DS</sub> = 600.0 mA
Forward Transconductance	G <sub>M</sub>	3.0	-	S	$V_{\text{DS}}$ = 10.0 V , $I_{\text{DS}}$ = 6000.0 mA , $\Delta$ $V_{\text{GS}}$ = 1.0V, 80 $\mu s$ Pulse
Input Capacitance	C <sub>ISS</sub>	-	270	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Output Capacitance	C <sub>OSS</sub>	-	240	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Reverse Capacitance	C <sub>RSS</sub>	-	48	pF	V <sub>DS</sub> = 28.0 V , F = 1.0 MHz
Power Gain	G <sub>P</sub>	13	-	dB	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz
Drain Efficiency	ŋ <sub>D</sub>	60	-	%	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz
Return Loss	RL	10	-	%	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz
Load Mismatch Tolerance	VSWR-T	-	30:1	-	$V_{DD}$ = 28.0 V, $I_{DQ}$ = 600 mA, $P_{OUT}$ = 120.0 W F =175 MHz

#### 1

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80

60

40

20

2

0.1

100MH

0.2 0.3

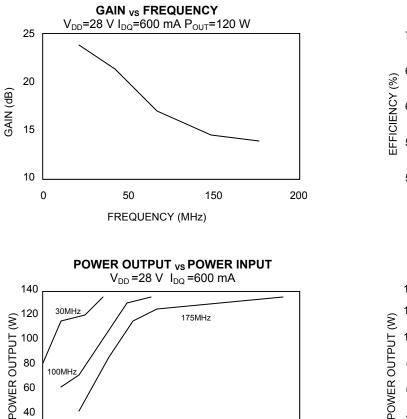
1 2 3 4 5 6 7 8 9

POWER INPUT (W)

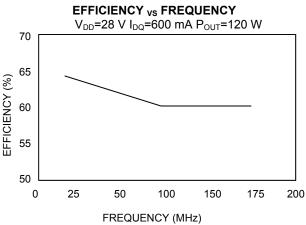
## **RF Power MOSFET Transistor** 120W, 2-175MHz, 28V

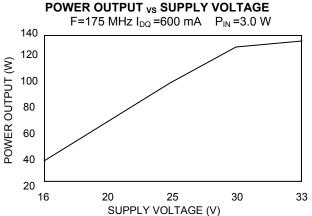


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**Typical Broadband Performance Curves** 



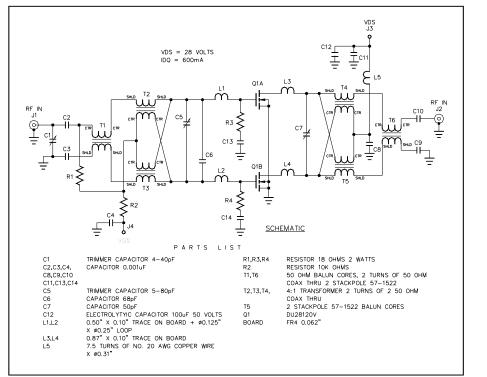


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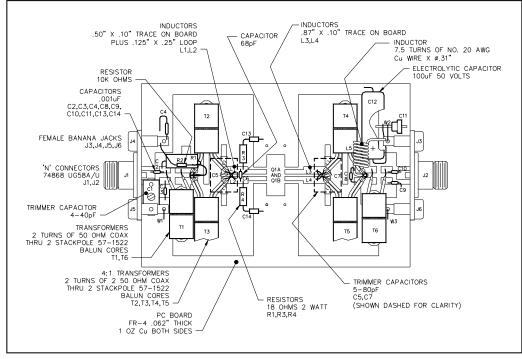
# DU28120V

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### TEST FIXTURE SCHEMATIC



### **TEST FIXTURE ASSEMBLY**









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