

### Features

- Attenuation 0.5-dB Steps to 15.5 dB
- Temperature Stability:  $\pm 0.1$  dB from  $-55^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$  Typical
- Fast Switching Speed: 3 nS Typical to 90%
- Ultra Low DC Power Consumption

### Description

M/A-COM's MADA2030G is a 5-bit, 0.5-dB step GaAs MMIC digital attenuator. The MADA2030G is ideally suited for use where high accuracy, fast switching, very low power consumption and low intermodulation products are required at a low cost.

Typical applications include radio and cellular equipment, wireless LANS, GPS equipment and other gain/level control circuits.

The MADA2030G is fabricated with a monolithic GaAs MMIC using a mature 1-micron process. The process features full chip passivation for increased performance and reliability.

### Ordering Information

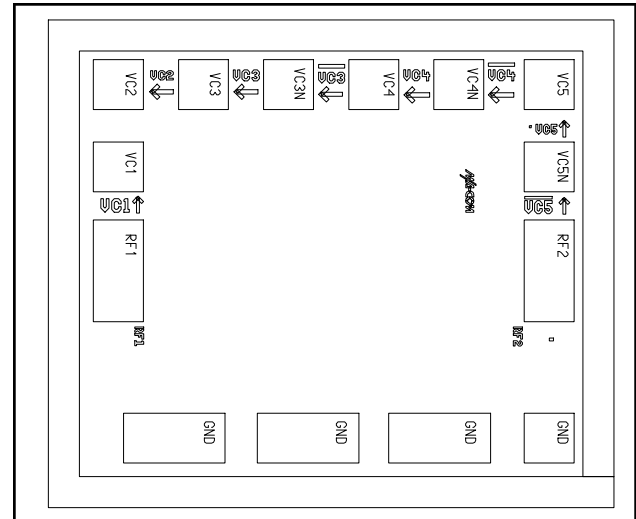
| Part Number | Package |
|-------------|---------|
| MADA2030G   | DIE     |

### Absolute Maximum Ratings <sup>1,2</sup>

| Parameter                      | Absolute Maximum |
|--------------------------------|------------------|
| Input Power<br>500 MHz - 2 GHz | +34 dBm          |
| Control Voltage                | -8.5 VDC         |
| Operating Temperature          | +175°C           |
| Storage Temperature            | -65°C to +175°C  |

1. Exceeding any one or combination of these limits may cause permanent damage to this device.
2. M/A-COM does not recommend sustained operation near these survivability limits.

### Die



### Bond Pad Dimensions

| Bond Pad                               | Dimension Inches (mm)                      |
|--|--|
| RF <sub>IN</sub> , RF <sub>OUT</sub>   | 0.004 x 0.008 (0.100 x 0.200)              |
| VC1, VC2, VC3, VC3, VC4, VC4, VC5, VC5 | 0.004 x 0.004 (0.100 x 0.100)              |
| GND1, GND2, GND3                       | 0.008 x 0.004 (0.200 x 0.100)              |
| GND4                                   | 0.004 x 0.004 (0.100 x 0.100)              |
| DIE Size                               | 0.045 x 0.039 x 0.010 (1.14 x 0.99 x 0.25) |

### Electrical Specifications: $T_A = 25^\circ\text{C}$ , $Z_0 = 50 \Omega$

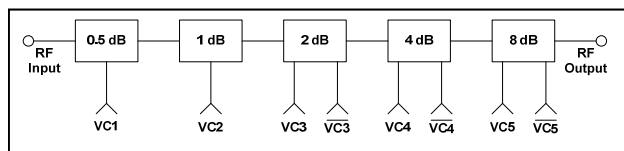
| Parameter                                 | Test Conditions   | Units  | Min. | Typ.    | Max.  |
|---|---|--|------|---------|-------|
| Reference Insertion Loss                  | DC - 1.0 GHz  | dB   | —    | —       | 1.9   |
|   | DC - 2.0 GHz  | dB   | —    | —       | 2.3   |
| Attenuation Accuracy                      | DC - 2.0 GHz  | $\pm (0.3 \text{ dB} \pm 3\% \text{ of attenuation setting in dB})$ dB |      |         |       |
| VSWR (Worst Case)                         | DC - 2.0 GHz  | Ratio  | —    | —       | 1.6:1 |
| Phase Balance                             | (For any bit or combinations of bits per unit)                        |  |      |         |       |
|   | 500 MHz   | °  | —    | +1 / -2 | —     |
|   | 1.0 GHz   | °  | —    | +2 / -3 | —     |
|   | 2.0 GHz   | °  | —    | +4 / -6 | —     |
| Switching Characteristics                 | 50% Control to 90% RF, 50% Control to 10% RF<br>Unfiltered Transients | nS   | —    | 3       | —     |
|   |   | mV   | —    | 7       | —     |
| Input Power for<br>1 dB Compression       | 100 MHz   | dBm  | —    | 24      | —     |
|   | Above 500 MHz   | dBm  | —    | 27      | —     |
| IP2                                       | for two-tone input power up to +5 dBm                                 |  |      |         |       |
|   | 100 MHz   | dBm  | —    | 45      | —     |
|   | Above 500 MHz   | dBm  | —    | 68      | —     |
| IP3                                       | for two-tone input power up to +5 dBm                                 |  |      |         |       |
|   | 100 MHz   | dBm  | —    | 40      | —     |
|   | Above 500 MHz   | dBm  | —    | 45      | —     |
| Control Voltages<br>(Complementary Logic) | $V_{IN}$ Low, 0 to -0.2 V   | $\mu\text{A}$  | —    | —       | 5     |
|   | $V_{IN}$ Hi, -5 V   | $\mu\text{A}$  | —    | 75      | —     |
|   | $V_{IN}$ Hi, -8 V   | $\mu\text{A}$  | —    | —       | 250   |

### Truth Table<sup>3</sup>

| VC1 | VC2 | VC3 | $\overline{\text{VC3}}$ | VC4 | $\overline{\text{VC4}}$ | VC5 | $\overline{\text{VC5}}$ | Attenuation Setting |
|-----|-----|-----|-------------------------|-----|-------------------------|-----|-------------------------|---------------------|
| 1   | 1   | 1   | 0                       | 1   | 0                       | 1   | 0                       | Reference           |
| 0   | 1   | 1   | 0                       | 1   | 0                       | 1   | 0                       | 0.5 dB              |
| 1   | 0   | 1   | 0                       | 1   | 0                       | 1   | 0                       | 1 dB                |
| 1   | 1   | 0   | 1                       | 1   | 0                       | 1   | 0                       | 2 dB                |
| 1   | 1   | 1   | 0                       | 0   | 1                       | 1   | 0                       | 4 dB                |
| 1   | 1   | 1   | 0                       | 1   | 0                       | 0   | 1                       | 8 dB                |
| 0   | 0   | 0   | 1                       | 0   | 1                       | 0   | 1                       | 15.5 dB             |

3. 0 =  $V_{IN}$  Low, 0 to -0.2 V, 1 =  $V_{IN}$  Hi, -5 V to -8 V

### Schematic



### Handling Procedures

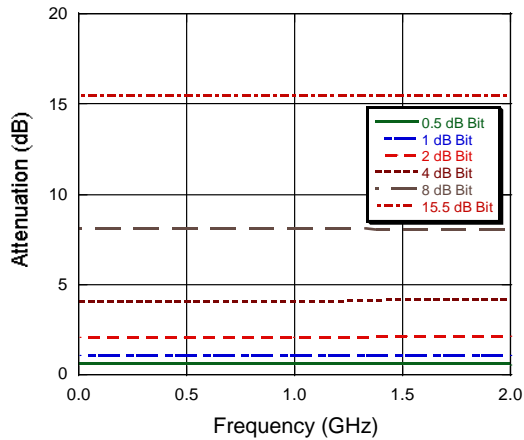
Please observe the following precautions to avoid damage:

### Static Sensitivity

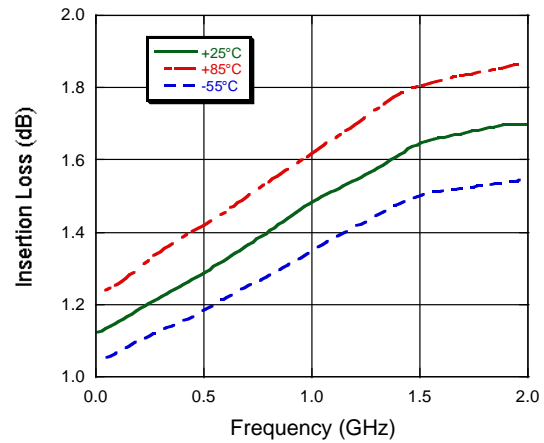
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

## Typical Performance Curves

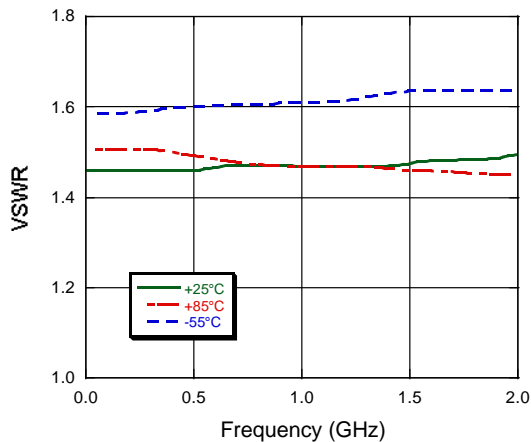
**Attenuation**



**Insertion Loss**



**Worst Case VSWR**



## Handling Procedures

Permanent damage to the MADA2030G may occur if the following precautions are not adhered to:

- A. Cleanliness - The MADA2030G should be handled in a clean environment. DO NOT attempt to clean after the MADA2030G is installed.
- B. Static Sensitivity - All die handling equipment and personnel should be DC grounded.
- C. Transients - Avoid instrument and power supply transients while bias is applied to the MADA2030G. Use shielded signal and bias cables to minimize inductive pick-up.
- D. Bias - Apply voltage to either complementary control ports only when the other is grounded. Neither port should be allowed to "float".
- E. General Handling - It is recommended that the MADA2030G chip be handled along the long side of the die with a sharp pair of bent tweezers. DO NOT touch the surface of the chip with fingers or tweezers.

## Mounting

The MADA2030G is back-metallized with Pd/Ni/Au (100/1,000/10,000Å) metallization. It can be die-mounted using Au/Sn eutectic preforms or a thermally conductive epoxy. The package surface should be clean and flat before attachment.

### Eutectic Die Attach:

- A. An 80/20 Au/Sn preform is recommended with a work surface temperature of approximately 255°C and a tool temperature of 265°C. When hot 90/10 nitrogen/hydrogen gas is applied, tool tip temperature should be approximately 290°C.
- B. DO NOT expose the MADA2030G to a temperature greater than 320°C for more than 20 seconds. No more than 3 seconds of scrubbing should be required for attachment.

### Epoxy Die Attach:

- B Apply a minimum amount of epoxy and place the MADA2030G into position. A thin epoxy fillet should be visible around the perimeter of the die.
- C Cure epoxy per manufacturer's recommended schedule.
- D Electrically conductive epoxy may be used but is not required.

## Wire Bonding

- A. Ball or wedge bond with 1.0 mil diameter pure gold wire. Thermosonic wire bonding with a nominal stage temperature of 150°C and a ball bonding force of 40 to 50 grams or wedge bonding force of 18 to 22 grams is recommended. Ultrasonic energy and time should be adjusted to the minimum levels to achieve reliable wirebonds.
- B. Wirebonds should be started on the chip and terminated on the package. GND bonds should be as short as possible; at least three and no more than four bond wires from ground pads to package are recommended.