## Features

- CDMA Handset Switching Applications
- Balanced (symmetrical) RF Ports
- Low Cross Modulation
- Low Insertion Loss: 0.55 dB @ 1 GHz
- High Isolation: 27 dB @ 1 GHz
- High Power: P0.1dB = 36 dBm @ 1 GHz
- 0.5 micron GaAs pHEMT Process
- Lead-Free 2 mm 12-Lead STQFN package with 0.5 mm lead pitch
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and $260^{\circ} \mathrm{C}$ Reflow Compatible


## Description

The MASW-009482 is an industry leading GaAs pHEMT MMIC single pole three throw (SP3T) switch in a lead-free 2 mm 12-lead STQFN package with 0.5 mm lead pitch. The MASW-009482 is uniquely configured to enable switching from a common antenna port to CDMA Cellular, PCS, or AWS ports. It is also ideal for other applications where a compact, high performance, symmetrical SP3T switch is required.

The design is symmetric and has been fully optimized for excellent cross modulation performance in all three paths while still maintaining excellent insertion loss and isolation. The symmetrical design allows the user to assign CDMA Cellular, PCS and AWS to ports RF1, RF2 or RF3 as required to optimize the layout.

The MASW-009482 is fabricated using a 0.5 micron gate length GaAs pHEMT process. The process features full passivation for performance and reliability.

## Ordering Information ${ }^{1,2}$

| Part Number | Package |
| :---: | :---: |
| MASW-009482-TR3000 | 3000 piece reel |
| MASW-009482-001SMB | Sample Board <br> $0.05-4.0 \mathrm{GHz}$ Tuning |

[^0]
## Functional Schematic



## Pin Configuration ${ }^{3}$

| Pin No. | Function | Description |
| :---: | :---: | :---: |
| 1 | V3 | Control 3 |
| 2 | RF3 | RF Port 3 |
| 3 | GND | Ground |
| 4 | GND | Ground |
| 5 | RF2 | RF Port 2 |
| 6 | V2 | Control 2 |
| 7 | GND | Ground |
| 8 | RF1 | RF Port 1 |
| 9 | V1 | Control 1 |
| 10 | GND | Ground |
| 11 | RFC | RF Common |
| 12 | GND | Ground |
| 13 | GND (paddle) | Ground |

3. All package ground pins ( $\mathrm{P} 3,4,7,10,12$ ) and paddle ground are no connection (N/C) electrically to the internal die. M/ACOM recommends connecting all ground connections to PCB ground to ensure a good thermal path.
[^1]- India Tel: +91.80.43537383 - China Tel: +86.21.2407.1588
Visit www.macomtech.com for additional data sheets and product information.

Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{Z}_{0}=50 \Omega, \mathrm{~V}_{\mathrm{C}}=0 \mathrm{~V} / 2.7 \mathrm{~V}, 1000 \mathrm{pF}$ Capacitors ${ }^{4}$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss <br> (All Paths) | 1.0 GHz 2.0 GHz 2.5 GHz | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | - | $\begin{aligned} & 0.55 \\ & 0.75 \\ & 0.75 \end{aligned}$ | $\begin{aligned} & 0.7 \\ & 0.9 \\ & 0.9 \end{aligned}$ |
| Isolation | $\begin{aligned} & 1.0 \mathrm{GHz} \\ & 2.0 \mathrm{GHz} \\ & 2.5 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & 23 \\ & 17 \\ & 16 \end{aligned}$ | $\begin{aligned} & 27 \\ & 21 \\ & 20 \end{aligned}$ | - |
| Return Loss (All RF ports) | DC - 2.5 GHz | dB | - | 20 | - |
| Input IP3 | Two Tone, $+23 \mathrm{dBm} /$ tone, 5 MHz Spacing, 1 GHz Two Tone, $+23 \mathrm{dBm} /$ tone, 5 MHz Spacing, 2 GHz | $\begin{aligned} & \mathrm{dBm} \\ & \mathrm{dBm} \end{aligned}$ | — | $\begin{aligned} & 65 \\ & 64 \end{aligned}$ | - |
| Cross Modulation | For Cell Band: Two-tone signal input: $\begin{gathered} \mathrm{T}_{\mathrm{x}} 1=+22 \mathrm{dBm} @ 820 \mathrm{MHz}, \mathrm{~T}_{\mathrm{x}} 2=+22 \mathrm{dBm} @ 821 \mathrm{MHz}, \\ \mathrm{R}_{\mathrm{x}} \text { interferer }=-23 \mathrm{dBm} @ 865 \mathrm{MHz}^{5} \end{gathered}$ | dBm | - | -108 | - |
|  | For PCS Band: Two-tone signal input: $\begin{gathered} \mathrm{T}_{\mathrm{x}} 1=+18 \mathrm{dBm} @ 1880 \mathrm{MHz}, \mathrm{~T}_{\mathrm{x}} 2=+18 \mathrm{dBm} @ 1881 \mathrm{MHz}, \\ \mathrm{R}_{\mathrm{x}} \text { interferer }=-23 \mathrm{dBm} @ 1960 \mathrm{MHz}^{5} \end{gathered}$ | dBm | - | -109 | - |
| $2^{\text {nd }}$ Harmonic | $\begin{aligned} & \text { Fin }=1 \mathrm{GHz}, \text { Pin }=+26 \mathrm{dBm} \\ & \text { Fin }=2 \mathrm{GHz} \text {, Pin }=+26 \mathrm{dBm} \end{aligned}$ | dBc | - | $\begin{aligned} & 82 \\ & 83 \end{aligned}$ | - |
| $3^{\text {rd }}$ Harmonic | $\begin{aligned} & \text { Fin }=1 \mathrm{GHz}, \text { Pin }=+26 \mathrm{dBm} \\ & \text { Fin }=2 \mathrm{GHz}, \text { Pin }=+26 \mathrm{dBm} \end{aligned}$ | dBc | - | $\begin{aligned} & 84 \\ & 74 \end{aligned}$ | - |
| Input P0.1dB | $\mathrm{V}_{\mathrm{C}}=0 \mathrm{~V} / 2.7 \mathrm{~V}, 1 \mathrm{GHz}$ | dBm | - | 36 | - |
| Trise, Tfall | 10\% to 90\% RF, $90 \%$ to $10 \% \mathrm{RF}, 900 \mathrm{MHz}$ | ns | - | 45 | - |
| Ton, Toff | $50 \%$ control to $90 \%$ RF, and $50 \%$ control to $10 \%$ RF, 900 MHz | ns | - | 70 | - |
| Transients | In Band | mV | - | 40 | - |
| Control Current | $\mathrm{V}_{\mathrm{C}}=0 \mathrm{~V} / 2.7 \mathrm{~V}$ | $\mu \mathrm{A}$ | - | 6 | 25 |

4. External DC blocking capacitors are required on all RF ports. Typical performance specifications are with 1000 pF blocking and decoupling capacitors / as shown on the application schematic .
5. $\mathrm{R}_{\mathrm{x}}$ Interferer power set to -10 dBm during test to improve dynamic range of measurement system. Typical performance with -23 dBm interferer is determined by using a linear relationship between interferer power level and cross modulation products.

## Absolute Maximum Ratings ${ }^{6,7}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power | +36 dBm |
| $(0.5-4.0 \mathrm{GHz}, 2.6 \mathrm{~V}$ Control $)$ | $\pm 8.5$ volts |
| Control Voltage | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Operating Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Storage Temperature |  |

6. Exceeding any one or combination of these limits may cause permanent damage to this device.
7. M/A-COM Technology does not recommend sustained operation near these survivability limits.

## Truth Table ${ }^{8,9}$

| V1 | V2 | V3 | ANT-RF1 | ANT-RF2 | ANT-RF3 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | 0 | On | Off | Off |
| 0 | 1 | 0 | Off | On | Off |
| 0 | 0 | 1 | Off | Off | On |

8. Differential voltage, V (state 1 ) -V (state 0 ) must be 2.6 V minimum and must not exceed 8.5 V .
9. Positive Control: $1=+2.6 \mathrm{~V}$ to $+8.0 \mathrm{~V}, 0=0 \mathrm{~V}+/-0.2 \mathrm{~V}$ Negative Control: $1=0 \mathrm{~V}+/-0.2 \mathrm{~V}, 0=-2.6 \mathrm{~V}$ to -8.0 V

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GaAs Broadband High Power SP3T Switch
DC-4.0 GHz
Typical Performance Curves:
$\mathrm{Z}_{0}=50 \Omega, 1000 \mathrm{pF}$ Blocking and decoupling caps, $\mathrm{V}_{\mathrm{CTL}}=0 /+2.7 \mathrm{~V}_{\mathrm{DC}}$

## Insertion Loss



Input Return Loss


Input P0.1dB Compression


Isolation


Input IP2 and IP3


## Harmonics



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GaAs Broadband High Power SP3T Switch DC-4.0 GHz

Typical Performance Curves vs. Control Voltage: $\mathrm{Z}_{0}=50 \Omega, 1000 \mathrm{pF}$ Blocking and decoupling caps

Input IP3 vs. Control Voltage


2nd Harmonic vs. Control Voltage


Input IP2 vs. Control Voltage


3rd Harmonic vs. Control Voltage


Input P0.1dB vs. Control Voltage


GaAs Broadband High Power SP3T Switch DC-4.0 GHz

Application Schematic ${ }^{10,11}$

10. The exposed pad centered on the package bottom must be connected to ground to ensure a good thermal path.
11. All blocking and decoupling capacitors $=1000 \mathrm{pF}$

## Qualification

Qualified to M/A-COM specification REL-201, Process Flow -2.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

## Lead-Free 2mm STQFN-12LD-0.5 mm Pitch ${ }^{\dagger}$


${ }^{\dagger}$ Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 1 requirements.
Plating is $100 \%$ matte tin over copper.

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## Applications Section: Tri-Mode Mobile Phone Performance

## Typical Electrical Performance: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{Z}_{0}=50 \Omega, \mathrm{~V}_{\mathrm{C}}=\mathbf{0 V} / 2.6 \mathrm{~V}, 1000 \mathrm{pF}$ Capacitors ${ }^{1}$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss (All Paths) | $\begin{gathered} 824-894 \mathrm{MHz} \text { (CELL) } \\ 1710-1755 \mathrm{MHz} \text { (AWS) } \\ 1850-1990 \mathrm{MHz} \text { (PCS) } \end{gathered}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | — | $\begin{aligned} & 0.55 \\ & 0.65 \\ & 0.75 \end{aligned}$ | — |
| Isolation | $\begin{gathered} 824-894 \mathrm{MHz} \text { (CELL) } \\ 1710-1755 \mathrm{MHz} \text { (AWS) } \\ 1850-1990 \mathrm{MHz} \text { (PCS) } \end{gathered}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | - | $\begin{aligned} & 27 \\ & 23 \\ & 22 \end{aligned}$ | — |
| Return Loss (All RF ports) | DC - 2.5 GHz | dB | - | 20 | - |
| Input IP3 | Two Tone, +23 dBm/tone, 1 MHz Spacing, 894 MHz Two Tone, +23 dBm/tone, 1 MHz Spacing, 1755 MHz Two Tone, +23 dBm/tone, 1 MHz Spacing, 1990 MHz | dBm dBm dBm | - | $\begin{aligned} & 68 \\ & 65 \\ & 67 \end{aligned}$ | - |
| Cross Modulation | For Cell Band: Two-tone signal input: $\begin{gathered} \mathrm{T}_{\mathrm{x}} 1=+22 \mathrm{dBm} @ 820 \mathrm{MHz}, \mathrm{~T}_{\mathrm{x}} 2=+22 \mathrm{dBm} @ 821 \mathrm{MHz}, \\ \mathrm{R}_{\mathrm{x}} \text { interferer }=-23 \mathrm{dBm} @ 865 \mathrm{MHz}^{2} \end{gathered}$ | dBm | - | -105 | - |
|  | For PCS Band: Two-tone signal input: $\begin{gathered} \mathrm{T}_{\mathrm{x}} 1=+18 \mathrm{dBm} @ 1880 \mathrm{MHz}, \mathrm{~T}_{\mathrm{x}} 2=+18 \mathrm{dBm} @ 1881 \mathrm{MHz}, \\ \mathrm{R}_{\mathrm{x}} \text { interferer }=-23 \mathrm{dBm} @ 1960 \mathrm{MHz}^{2} \end{gathered}$ | dBm | - | -103 | - |
| $2^{\text {nd }}$ Harmonic | $\begin{aligned} & \text { Fin }=894 \mathrm{MHz}, \text { Pin }=+25.5 \mathrm{dBm} \\ & \text { Fin }=1755 \mathrm{MHz}, \text { Pin }=+25.0 \mathrm{dBm} \\ & \text { Fin }=1990 \mathrm{MHz}, \text { Pin }=+24.0 \mathrm{dBm} \end{aligned}$ | dBc dBc dBc | - | $\begin{aligned} & 81 \\ & 81 \\ & 85 \end{aligned}$ | - |
| $3{ }^{\text {rd }}$ Harmonic | $\begin{aligned} & \text { Fin }=894 \mathrm{MHz}, \text { Pin }=+25.5 \mathrm{dBm} \\ & \text { Fin }=1755 \mathrm{MHz}, \text { Pin }=+25.0 \mathrm{dBm} \\ & \text { Fin }=1990 \mathrm{MHz}, \text { Pin }=+24.0 \mathrm{dBm} \end{aligned}$ | dBc dBc dBc | - | $\begin{aligned} & 82 \\ & 81 \\ & 91 \end{aligned}$ | - |
| Input P0.1dB | $\begin{gathered} \text { Fin }=894 \mathrm{MHz} \\ \text { Fin }=1755 \mathrm{MHz} \\ \text { Fin }=1990 \mathrm{MHz} \end{gathered}$ | dBm dBm dBm | - | $\begin{aligned} & 34.5 \\ & 36.0 \\ & 35.5 \end{aligned}$ | - |
| Trise, Tfall | 10\% to 90\% RF, $90 \%$ to $10 \% \mathrm{RF}, 900 \mathrm{MHz}$ | ns | - | 45 | - |
| Ton, Toff | $50 \%$ control to $90 \%$ RF, and $50 \%$ control to $10 \%$ RF, 900 MHz | ns | - | 70 | - |
| Transients | In Band | mV | - | 40 | - |
| Control Current | $\mathrm{V}_{\mathrm{C}}=0 \mathrm{~V} / 2.6 \mathrm{~V}$ | $\mu \mathrm{A}$ | - | 6 | - |

1. External DC blocking capacitors are required on all RF ports. Typical performance specifications are with 1000 pF blocking and decoupling capacitors / as shown on the application schematic
2. $\mathrm{R}_{\mathrm{x}}$ Interferer power set to -10 dBm during test to improve dynamic range of measurement system. Typical performance with -23 dBm interferer is determined by using a linear relationship between interferer power level and cross modulation products.

GaAs Broadband High Power SP3T Switch DC-4.0 GHz

## Applications Section - Tri-Mode Mobile Phone Performance

Typical Performance Curves: $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}, \mathrm{Z}_{0}=50 \Omega, \mathrm{~V}_{\mathrm{C}}=0 \mathrm{~V} / 2.6 \mathrm{~V}, 1000 \mathrm{pF}$ Capacitors

Insertion Loss


Input Return Loss


Input P0.1dB Compression


Isolation


Input IP2 and IP3


Harmonics


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[^0]:    1. Reference Application Note M513 for reel size information.
    2. All sample boards include 5 loose parts.
[^1]:    * Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

