

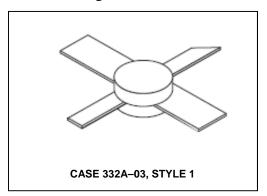
# Microwave Pulse Power Silicon NPN Transistor 4.0W (peak), 960–1215MHz

M/A-COM Products Released - Rev. 07.07

Designed for Class B and C common base amplifier applications in short and long pulse TACAN, IFF, DME, and radar transmitters.

- Guaranteed performance @ 1090 MHz, 35 Vdc
   Output power = 4.0 W Peak
   Minimum gain = 10 dB
- 100% Tested for load mismatch at all phase angles with 10:1 VSWR
- Industry standard package
- Nitride passivated
- Gold metallized, emitter ballasted for long life and resistance to metal migration
- Internal input matching for broadband operation

### **Product Image**



#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	20	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	50	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	3.5	Vdc
Collector Current — Continuous	Ic	250	mAdc
Total Device Dissipation @ T <sub>C</sub> = 25°C (1) Derate above 25°C	P <sub>D</sub>	7.0 40	Watts mW/°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (2)	R <sub>eJC</sub>	25	°C/W

## **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	+			•	•
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 5.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)</sub> CEO	20	_	_	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 5.0 mAdc, V <sub>BE</sub> = 0)	V <sub>(BR)</sub> CES	50	_	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 5.0 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)</sub> CBO	50	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 1.0 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	3.5	_	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 35 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	_	_	0.5	mAdc
ON CHARACTERISTICS					
DC Current Gain	h <sub>FE</sub>	10	_	100	_

(I<sub>C</sub> = 75 mAdc, V<sub>CE</sub> = 5.0 Vdc)

NOTES:

(continued)

- 1. These devices are designed for RF operation. The total device dissipation rating applies only when the device is operated as RF amplifiers.
- 2. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.

ADVANCED: Data Sheets contain information regarding a product M/A-COM Technology Solutions is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed.

PRELIMINARY: Data Sheets contain information regarding a product M/A-COM Technology Solutions has under development. Performance is based on engineering tests. Specifications are

typical. Mechanical outline has been fixed. Engineering samples Commitment to produce in volume is not quartitied.

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- Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300
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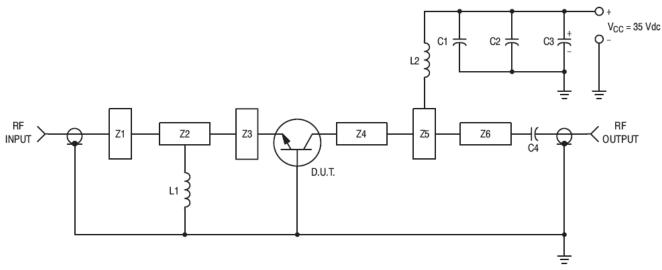


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## **ELECTRICAL CHARACTERISTICS** — **continued** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS		•			
Output Capacitance (V <sub>CB</sub> = 35 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	3.3	5.0	pF
FUNCTIONAL TESTS (Pulse Width = 10 μs, Duty Cycle = 1.0%)					
Common–Base Amplifier Power Gain (V <sub>CC</sub> = 35 Vdc, P <sub>out</sub> = 4.0 W pk, f = 1090 MHz)	G <sub>PB</sub>	10	11	_	dB
Collector Efficiency (V <sub>CC</sub> = 35 Vdc, P <sub>out</sub> = 4.0 W pk, f = 1090 MHz)	η	40	45	_	dB
Load Mismatch (V <sub>CC</sub> = 35 Vdc, P <sub>out</sub> = 4.0 W pk, f = 1090 MHz, VSWR = 10:1 All Phase Angles)	Ψ	No Degradation in Power Output			



C1 — 0.1 μF 
C2, C4 — 220 pF Chip Capacitor 
C3 — 20 μF, 50 V Electrolytic 
L1, L2 — 3 Turns #18 AWG, 1/8″ ID 
Z1–Z6 Distributed Microstrip Elements, See Photomaster 
Board Material — 0.031″ Thick Glass Teflon

Figure 1. 1090 MHz Test Circuit

<sup>•</sup> Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300

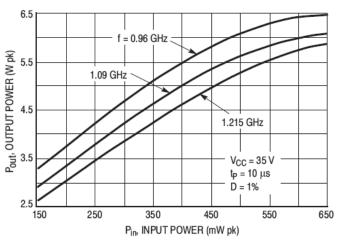
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#### TYPICAL CHARACTERISTICS



8 V<sub>CC</sub> = 35 V t<sub>p</sub> = 10 µs D = 1% P<sub>in</sub> = 650 mW pk 400 mW pk 960 1090 1215 f, FREQUENCY (MHz)

Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Frequency

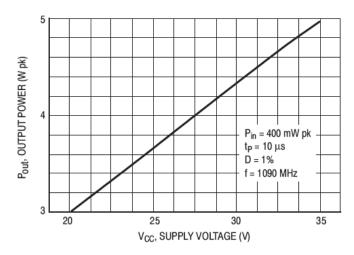


Figure 4. Output Power versus Supply Voltage

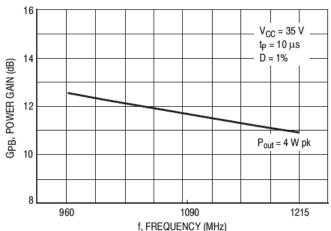


Figure 5. Power Gain versus Frequency

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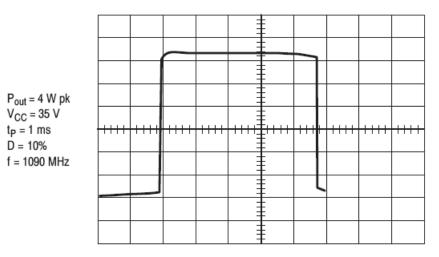


Figure 7. Typical Long Pulse Performance

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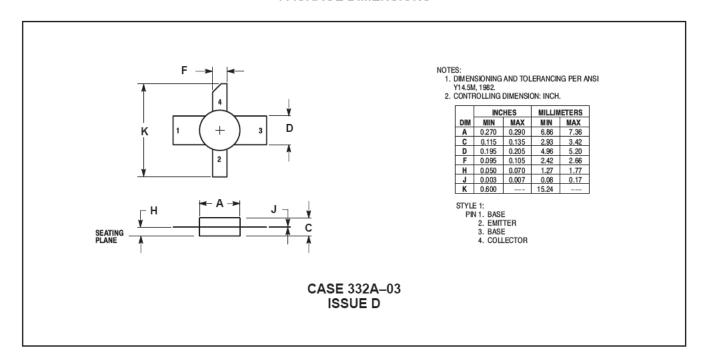
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#### PACKAGE DIMENSIONS



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