

Radar Pulsed Power Transistor 20W, 3.1-3.4 GHz, 300µs Pulse, 10% Duty

M/A-COM Products Released, 10 Jul 07

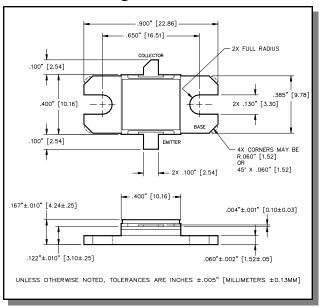
Features

- · NPN silicon microwave power transistors
- · Common base configuration
- Broadband Class C operation
- High efficiency inter-digitized geometry
- Diffused emitter ballasting resistors
- · Gold metallization system
- Internal input and output impedance matching
- Hermetic metal/ceramic package
- RoHS compliant

Absolute Maximum Ratings at 25°C

Parameter	Symbol	Rating	Units
Collector-Emitter Voltage	V _{CES}	65	V
Emitter-Base Voltage	V_{EBO}	3.0	V
Collector Current (Peak)	Ic	2.4	Α
Power Dissipation @ +25°C	P _{TOT}	146	W
Storage Temperature	T _{STG}	-65 to +200	°C
Junction Temperature	T_J	200	°C

Outline Drawing



Electrical Specifications: T_C = 25 ± 5°C (Room Ambient)

Parameter	Test Conditions	Frequency	Symbol	Min	Max	Units
Collector-Emitter Breakdown Voltage	I _C = 10mA		BV _{CES}	65	-	V
Collector-Emitter Leakage Current	V _{CE} = 40V		I _{CES}	-	1.5	mA
Thermal Resistance	Vcc = 36V, Pout = 20W	F = 3.1, 3.25, 3.4 GHz	R _{TH(JC)}	-	1.2	°C/W
Input Power	Vcc = 36V, Pout = 20W	F = 3.1, 3.25, 3.4 GHz	P _{IN}	-	3.56	W
Power Gain	Vcc = 36V, Pout = 20W	F = 3.1, 3.25, 3.4 GHz	G₽	7.5	-	dB
Collector Efficiency	Vcc = 36V, Pout = 20W	F = 3.1, 3.25, 3.4 GHz	ης	35	-	%
Input Return Loss	Vcc = 36V, Pout = 20W	F = 3.1, 3.25, 3.4 GHz	RL	-	-6	dB
Load Mismatch Tolerance	Vcc = 36V, Pout = 20W	F = 3.1, 3.25, 3.4 GHz	VSWR-T	-	2:1	-

Commitment to produce in volume is not qua

[•] North America Tel: 800.366.2266 / Fax: 978.366.2266

[•] Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300

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Visit www.macomtech.com for additional data sheets and product information.

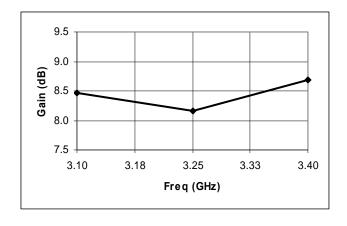


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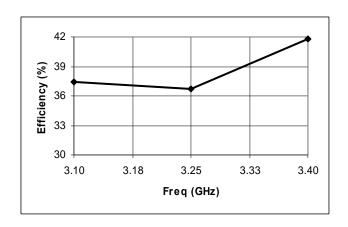
Typical RF Performance

Freq. (GHz)	Pin (W)	Pout (W)	Gain (dB)	Ic (A)	Eff (%)	RL (dB)	VSWR-T (2:1)
3.10	2.85	20.0	8.47	1.49	37.4	-9.6	-
3.25	3.05	20.0	8.17	1.52	36.7	-10.7	Р
3.40	2.71	20.0	8.68	1.33	41.8	-15.8	-

Gain vs. Frequency



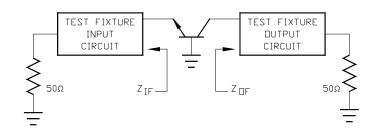
Collector Efficiency vs. Frequency



RF Test Fixture Impedance

typical. Mechanical outline has been fixed. Engineering samples Commitment to produce in volume is not guaranteed.

F (GHz)	Z _{IF} (Ω)	$Z_{OF}(\Omega)$
3.10	16.0 + j5.0	19.0 + j3.0
3.25	14.5 + j2.0	15.5 - j2.0
3.40	11.5 + j0.0	10.0 - j3.5



Solutions has under development. Performance is based on engineering tests. Specifications are

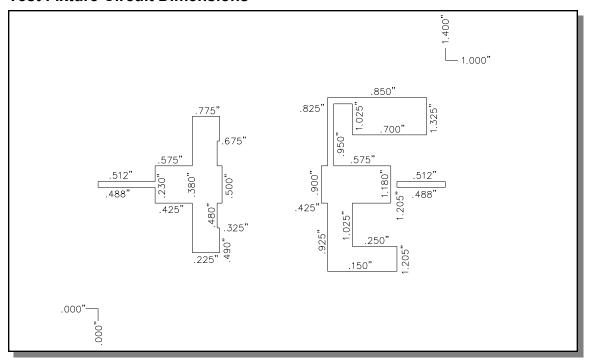
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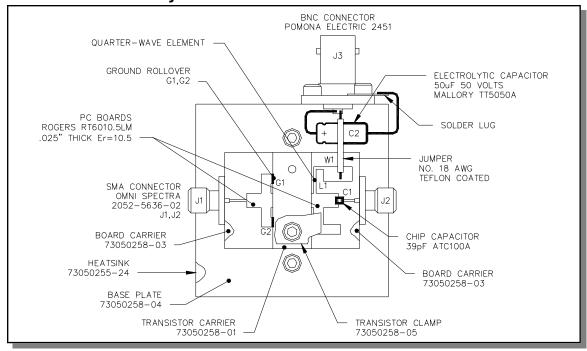
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Test Fixture Circuit Dimensions



Test Fixture Assembly

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