

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

N-channel Silicon Juncton FET

2SK596S — Electret Condenser Microphone Applications

Features

- · Low output noise voltage: VNO=-110dB max (VCC=4.5V, RL=1kΩ, Cin=15pF, VIN=0V, A curve)
- · Especially suited for use in condenser microphone for audio equipments and telephones
- · Excellent transient characteristic
- · Adoption of FBET process

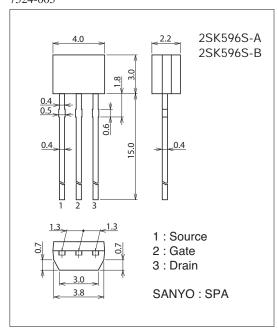
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Gate-to-Drain Voltage	V _{GDO}		-20	V
Gate Current	IG		10	mA
Drain Current	ID		1	mA
Allowable Power Dissipation	PD		100	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit : mm (typ) 7524-005



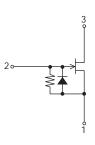
Product & Package Information

Package : SPA
JEITA, JEDEC : SC-72
Minimum Packing Quantity : 500 pcs./bag

Marking



Electrical Connection



Electrical Characteristics at Ta=25°C

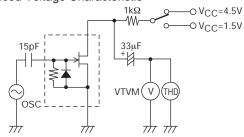
Parameter	Symbol	Conditions		Ra	Ratings		Unit	
Parameter	Symbol	Conditions	Rank	min	typ	max	Unit	
Gate-to-Drain Breakdown Voltage	V(BR)GDO	IG=-100μA		-20			V	
Cutoff Voltage	V _{GS} (off)	V _{DS} =5V, I _D =1μA			-0.4	-1.5	V	
Drain Current	lpcc*	V _{DS} =5V, V _{GS} =0V	A 100			170		
Drain Current	IDSS*	VDS=5V, VGS=0V	В	150		240	μΑ	
Forward Transfer Admittance	yfs	V _{DS} =5V, V _{GS} =0V, f=1kHz		0.4	0.8		mS	
Input Capacitance	Ciss	V _{DS} =5V, V _{GS} =0V, f=1MHz			4.1		pF	
Reverse Transfer Capacitance	Crss	V _{DS} =5V, V _{GS} =0V, f=1MHz			0.88		pF	
[Ta=25°C, V_{CC} =4.5V, R_L =1 k_{Ω} , Cin=15pF	[Ta=25°C, V _{CC} =4.5V, R _L =1kΩ, Cin=15pF, See specified Test Circuit.]							
Voltage Gain	Gv	V _{IN} =10mV, f=1kHz		-5.0		dB		
	Gγ	VIN=10IIIV, I=1KHZ	В		-3.8		V μA mS pF	
Reduced Voltage Characteristic	AC) D	V _{IN} =10mV, f=1kHz, V _{CC} =4.5V → 1.5V		-0.84	-1.8	dB		
	∆Gyy	V_{\parallel} = 10111V, 1=1KHZ, V_{\parallel} CC=4.5V \rightarrow 1.5V	В		-0.90	-2.0	ub ub	
Frequency Characteristic	∆Gvf	f=1kHz → 110Hz				-1.0	dB	
Total Harmonic Distortion	THD	Via 20mV f 1kHz	20m)/ f 1kHz		0/			
	וחט	V _{IN} =30mV, f=1kHz	В		1.6		70	
Output Noise Voltage	V _{NO}	V _{IN} =0V, A curve				-110	dB	

* : The 2SK596S is classified by IDSS as follows : (unit : $\mu A)$

Rank	A	В
IDSS	100 to 170	150 to 240

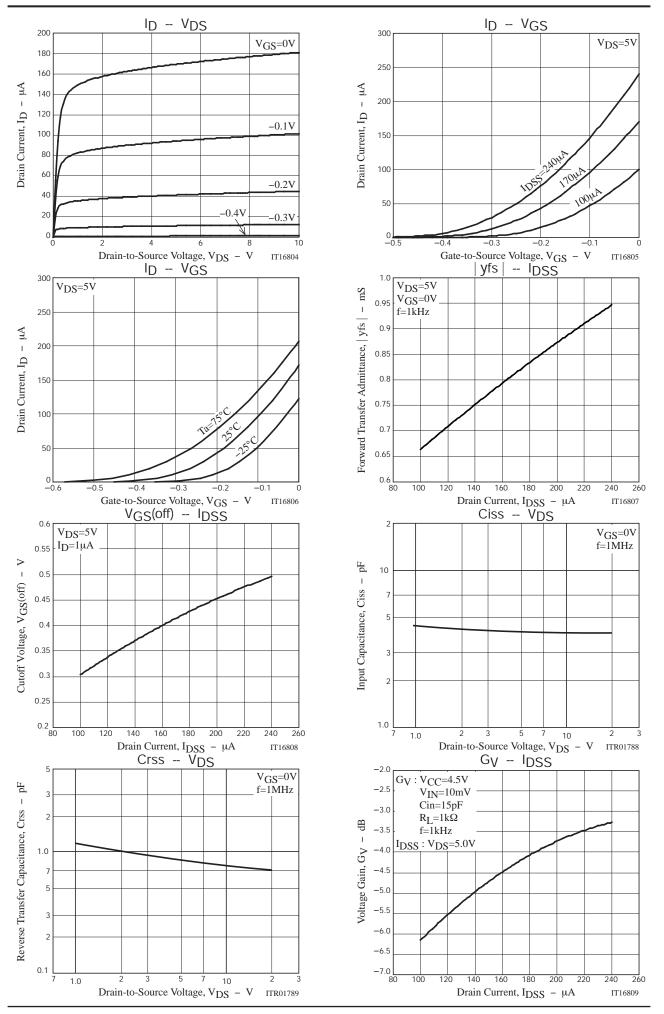
Test Circuit

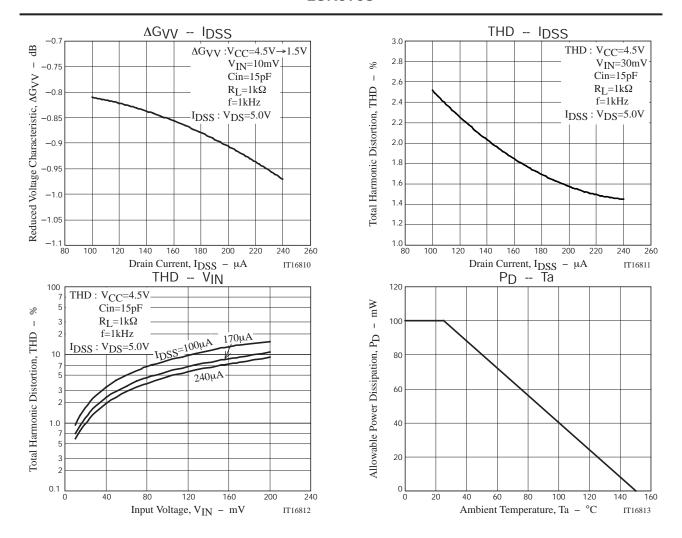
Voltage Gain Frequency Characteristic Harmonic Distortion Reduced Voltage Characteristic



Ordering Information

•				
Device	Package	Shipping	memo	
2SK596S-A	SPA	500pcs./bag	Pb Free	
2SK596S-B	SPA	500pcs./bag	PD Flee	





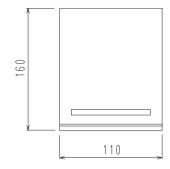
Bag Packing Specification 2SK596S-A, 2SK596S-B

1. Packing Format

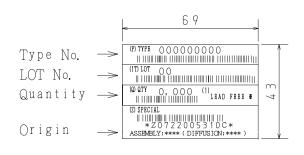
Package Name	Maximum Number of devices contained (pcs)					
	Bag	Inner	ВОХ	Outer BOX		
~ ¬ .		B-1	B-1/2	A-1	A-2	
SPA	SPA 500	20,000	10,000	100,000	60,000	
	•	Packing format (Dimensions:mm (external))				
		Inner	ВОХ	Outer	ВОХ	
		B-1		A-1	A-2	
		445×225×55	445×225×55	470×250×300	470×250×190	

2. Bag dimensions

(unit:mm)

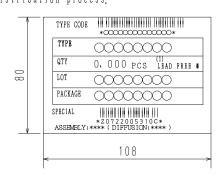


3. Bag label, Inner box label (unit:mm)



4. Outer box label

It is a label at the time of factory shipments. The form of a label may change in physical distribution process.



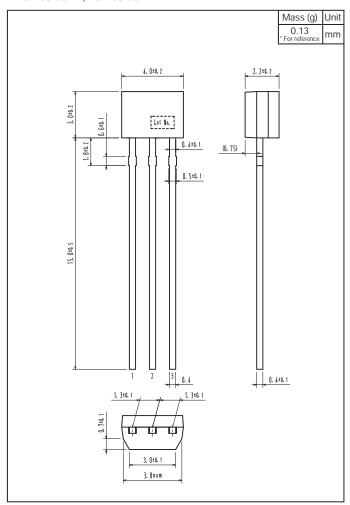
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label		JEITA Phase
LEAD FREE	}	JEITA Phase 3A
LEAD FREE	4	JEITA Phase 3

Outline Drawing

2SK596S-A, 2SK596S-B



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