BDW42 and BDW47 are Preferred Devices

Darlington Complementary Silicon Power Transistors

This series of plastic, medium-power silicon NPN and PNP Darlington transistors are designed for general purpose and low speed switching applications.

Features

- High DC Current Gain $h_{FE} = 2500$ (typ) @ $I_C = 5.0$ Adc.
- Collector Emitter Sustaining Voltage @ 30 mAdc:

 $V_{CEO(sus)} = 80 \text{ Vdc (min)} - BDW46$ 100 Vdc (min) - BDW42/BDW47

• Low Collector Emitter Saturation Voltage

 $V_{CE(sat)} = 2.0 \text{ Vdc (max)} @ I_C = 5.0 \text{ Adc}$ $3.0 \text{ Vdc (max)} @ I_C = 10.0 \text{ Adc}$

- Monolithic Construction with Built-In Base Emitter Shunt resistors
- TO-220AB Compact Package
- Pb-Free Packages Are Available*

MAXIMUM RATINGS

| Rating | S mi of Value | | Jn | |
|--|-----------------------------------|-------------|-----------|--|
| Collector-Emitter V trig trig to L D V46 | CE | Jo | Vdc | |
| BDW42, BDW47 | | 100 | | |
| Collector-Base Voltage BDW46 BDW42, BDW47 | V _{CB} | 80 100 | Vdc | |
| Emitter-Base Voltage | V _{EB} | 5.0 | Vdc | |
| Collector Current | Ic | 15 | Adc | |
| Base Current | I _B | 0.5 | Adc | |
| Total Device Dissipation @ T _C = 25°C Derate above 25°C | P _D | 85 0.68 | W W/°C | |
| Operating and Storage Junction Temperature Range | T _J , T _{stg} | -55 to +150 | °C | |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|--------------------------------------|-----------------|------|------|
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 1.47 | °C/W |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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15 AMP DARLINGTON COMPLEMENTARY SILICON **POWER TRANSISTORS** 80-100 VOLT, 85 WATT



BDWxx = **Device Code**

x = 42, 46, or 47

Assembly Location

Υ Year WW Work Week Pb-Free Package

ORDERING INFORMATION

| Device | Package | Shipping |
|--------|-----------------------|---------------|
| BDW42 | TO-220AB | 50 Units/Rail |
| BDW42G | TO-220AB (Pb-Free) | 50 Units/Rail |
| BDW46 | TO-220AB | 50 Units/Rail |
| BDW46G | TO-220AB (Pb-Free) | 50 Units/Rail |
| BDW47 | TO-220AB | 50 Units/Rail |
| BDW47G | TO-220AB (Pb-Free) | 50 Units/Rail |

Preferred devices are ON Semiconductor recommended choices for future use and best overall value

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

| Characteristic | | Symbol | Min | Max | Unit |
|--|-----------------------------|-----------------------|------|--|------|
| OFF CHARACTERISTICS | | | • | • | • |
| Collector Emitter Sustaining Voltage (Note 1) | | V _{CEO(sus)} | | | Vdc |
| $(I_C = 30 \text{ mAdc}, I_B = 0)$ | BDW46 | - () | 80 | - | |
| | BDW42/BDW47 | | 100 | - | |
| Collector Cutoff Current | | I _{CEO} | | | mAdc |
| $(V_{CE} = 40 \text{ Vdc}, I_B = 0)$ | BDW46 | | - | 2.0 | |
| $(V_{CE} = 50 \text{ Vdc}, I_B = 0)$ | BDW42/BDW47 | | - | 2.0 | |
| Collector Cutoff Current | | I _{CBO} | | | mAdc |
| $(V_{CB} = 80 \text{ Vdc}, I_{E} = 0)$ | BDW46 | | _ | 1.0 | |
| $(V_{CB} = 100 \text{ Vdc}, I_{E} = 0)$ | BDW42/BDW47 | | - | 1.0 | |
| Emitter Cutoff Current | | I _{EBO} | - | 2.0 | mAdc |
| $(V_{BE} = 5.0 \text{ Vdc}, I_{C} = 0)$ | | | | | |
| ON CHARACTERISTICS (Note 1) | | | | | |
| DC Current Gain | | h _{FE} | | | |
| $(I_C = 5.0 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc})$ | | | 1000 | - | |
| $(I_C = 10 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc})$ | | | 250 | - | |
| Collector-Emitter Saturation Voltage | | V _{CE(sat)} | | | Vdc |
| $(I_C = 5.0 \text{ Adc}, I_B = 10 \text{ mAdc})$ | | (, | - | 2.0 | |
| $(I_C = 10 \text{ Adc}, I_B = 50 \text{ mAdc})$ | | | - | 3.0 | |
| Base-Emitter On Voltage | | V _{BE(on)} | - | 3.0 | Vdc |
| $(I_C = 10 \text{ Adc}, V_{CE} = 4.0 \text{ Vdc})$ | | (- / | | | |
| SECOND BREAKDOWN (Note 2) | | | | | |
| Second Breakdown Collector | | I _{S/b} | | | Adc |
| Current with Base Forward Biased | | | | | |
| BDW42 | $V_{CE} = 28.4 \text{ Vdc}$ | | 3.0 | - | |
| | $V_{CE} = 40 \text{ Vdc}$ | | 1.2 | | |
| BDW46/BDW47 | $V_{0E} = 22.5 \text{ Vdc}$ | O 100 | 3.8 | \ | |
| \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ | V _{CE} = 36 V JC | | 1.2 | <u>\ </u> | |
| DYNAMIC CHARACTERISTICS | 10.0 | | | 1 | |
| Magnitude of common emitter small signal short circuit curre | nt transfer ratio | f_T | 4.0 | - | MHz |
| $(I_C = 3.0 \text{ Adc}, V_{CE} = 3.0 \text{ Vdc}, f = 1.0 \text{ MHz})$ | | | | | |
| Output Capacitance | | C _{ob} | | | pF |
| $(V_{CB} = 10 \text{ Vdc}, I_{E} = 0, f = 0.1 \text{ MHz})$ | BDW42 | | - | 200 | |
| | BDW46/BDW47 | | - | 300 | |
| Small-Signal Current Gain | | h _{fe} | 300 | - | |
| $(I_C = 3.0 \text{ Adc}, V_{CE} = 3.0 \text{ Vdc}, f = 1.0 \text{ kHz})$ | | | | | |

^{1.} Pulse Test: Pulse Width = 300 μ s, Duty Cycle = 2.0%. 2. Pulse Test non repetitive: Pulse Width = 250 ms.

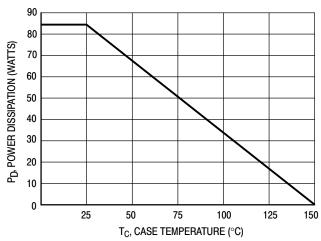


Figure 1. Power Temperature Derating Curve

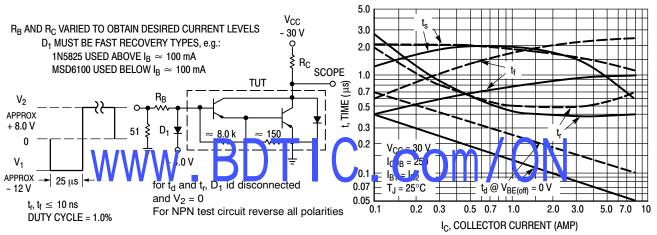


Figure 2. Switching Times Test Circuit

Figure 3. Switching Times

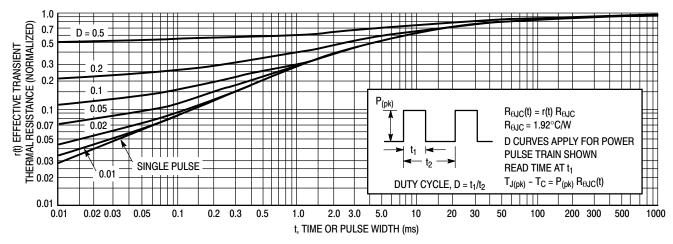


Figure 4. Thermal Response

ACTIVE-REGION SAFE OPERATING AREA

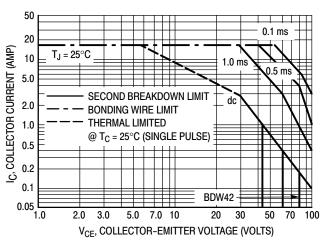


Figure 5. BDW42

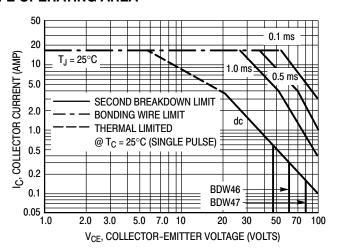


Figure 6. BDW46 and BDW47

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 5 and 6 is based on $T_{J(pk)} = 200^{\circ}C$; T_{C} is variable depending on the property of the second transition of the power parameters are two powers.

Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} \leq 200^{\circ}\text{C.}\ T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. *Linear extrapolation

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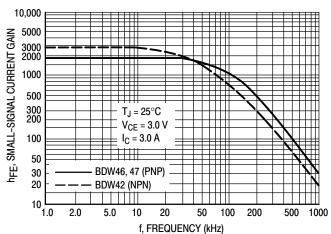


Figure 7. Small-Signal Current Gain

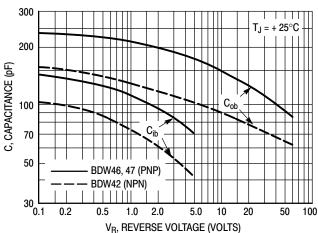


Figure 8. Capacitance

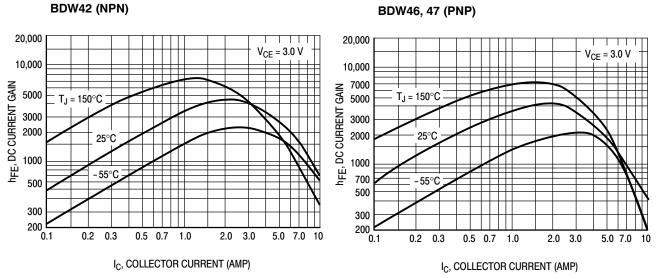


Figure 9. DC Current Gain

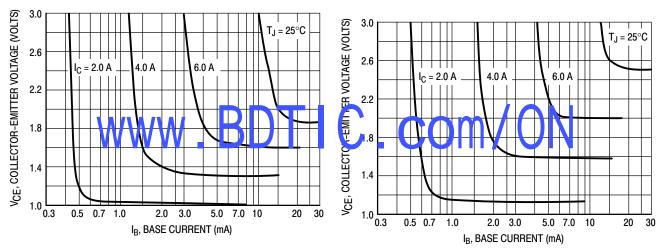


Figure 10. Collector Saturation Region

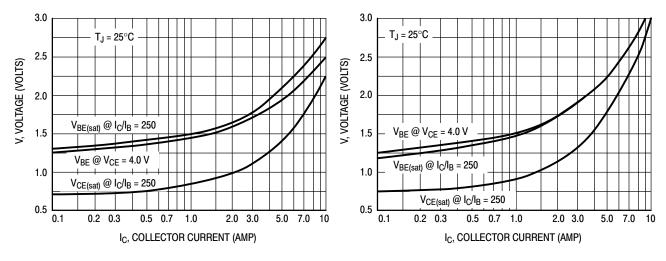


Figure 11. "On" Voltages

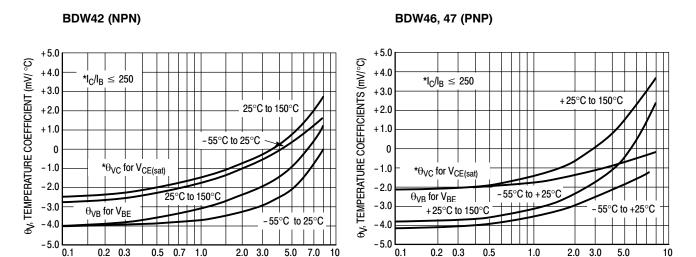


Figure 12. Temperature Coefficients

IC, COLLECTOR CURRENT (AMP)

IC, COLLECTOR CURRENT (AMP)

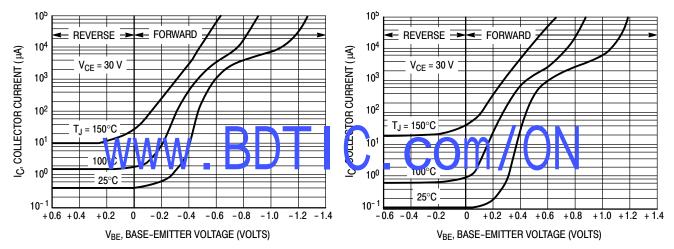


Figure 13. Collector Cut-Off Region

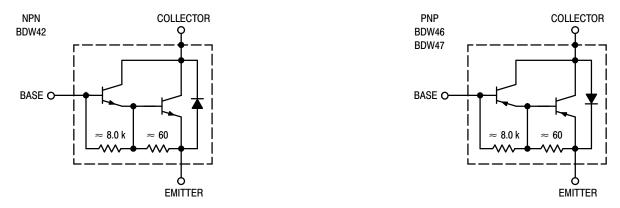
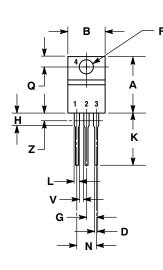
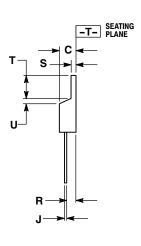


Figure 14. Darlington Schematic

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 ISSUE AE





NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
- DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

| | INCHES | | MILLIMETERS | |
|-----|--------|-------|-------------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.570 | 0.620 | 14.48 | 15.75 |
| В | 0.380 | 0.405 | 9.66 | 10.28 |
| С | 0.160 | 0.190 | 4.07 | 4.82 |
| D | 0.025 | 0.035 | 0.64 | 0.88 |
| F | 0.142 | 0.161 | 3.61 | 4.09 |
| G | 0.095 | 0.105 | 2.42 | 2.66 |
| Н | 0.110 | 0.155 | 2.80 | 3.93 |
| J | 0.014 | 0.025 | 0.36 | 0.64 |
| K | 0.500 | 0.562 | 12.70 | 14.27 |
| L | 0.045 | 0.060 | 1.15 | 1.52 |
| N | 0.190 | 0.210 | 4.83 | 5.33 |
| Q | 0.100 | 0.120 | 2.54 | 3.04 |
| R | 0.080 | 0.110 | 2.04 | 2.79 |
| S | 0.045 | 0.055 | 1.15 | 1.39 |
| T | 0.235 | 0.255 | 5.97 | 6.47 |
| U | 0.000 | 0.050 | 0.00 | 1.27 |
| ٧ | 0.045 | | 1.15 | |
| Z | | 0.080 | | 2.04 |

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