

# MCR8N

Preferred Device

## Silicon Controlled Rectifiers

### Reverse Blocking Thyristors

Designed primarily for half-wave ac control applications, such as motor controls, heating controls, and power supplies; or wherever half-wave, silicon gate-controlled devices are needed.

#### Features

- Blocking Voltage of 600 thru 800 Volts
- On-State Current Rating of 8 Amperes RMS at 80°C
- High Surge Current Capability – 80 Amperes
- Rugged, Economical TO–220AB Package
- Glass Passivated Junctions for Reliability and Uniformity
- Minimum and Maximum Values of IGT, VGT and IH Specified for Ease of Design
- High Immunity to dv/dt – 100 V/μsec Minimum at 125°C
- Pb–Free Packages are Available\*

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

| Rating   | Symbol                                 | Value      | Unit               |
|--|--|------------|--------------------|
| Peak Repetitive Off-State Voltage (Note 1)<br>(T <sub>J</sub> = –40 to 125°C, Sine Wave,<br>50 to 60 Hz, Gate Open) MCR8M<br>MCR8N | V <sub>DRM</sub> ,<br>V <sub>RRM</sub> | 600<br>800 | V                  |
| On-State RMS Current<br>(180° Conduction Angles; T <sub>C</sub> = 80°C)  | I <sub>T(RMS)</sub>                    | 8.0        | A                  |
| Peak Non-Repetitive Surge Current<br>(One Full Cycle, 60 Hz, T <sub>C</sub> = 125°C)   | I <sub>TSM</sub>                       | 80         | A                  |
| Circuit Fusing Consideration (t = 8.33 ms)   | I <sup>2</sup> t                       | 26.5       | A <sup>2</sup> sec |
| Forward Peak Gate Power<br>(Pulse Width ≤ 1.0 μs, T <sub>C</sub> = 80°C)   | P <sub>GM</sub>                        | 5.0        | W                  |
| Forward Average Gate Power<br>(t = 8.3 ms, T <sub>C</sub> = 80°C)  | P <sub>G(AV)</sub>                     | 0.5        | W                  |
| Forward Peak Gate Current<br>(Pulse Width ≤ 1.0 μs, T <sub>C</sub> = 80°C)   | I <sub>GM</sub>                        | 2.0        | A                  |
| Operating Junction Temperature Range   | T <sub>J</sub>                         | –40 to 125 | °C                 |
| Storage Temperature Range  | T <sub>stg</sub>                       | –40 to 150 | °C                 |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. V<sub>DRM</sub> and V<sub>RRM</sub> for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



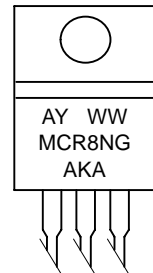
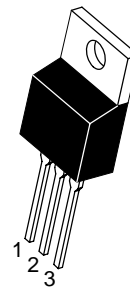
ON Semiconductor®

<http://onsemi.com>

SCRs  
8 AMPERES RMS  
600 thru 800 VOLTS



#### MARKING DIAGRAM



TO–220AB  
CASE 221A–09  
STYLE 3

A = Assembly Location  
Y = Year  
WW = Work Week  
G = Pb–Free Package  
AKA = Diode Polarity

#### PIN ASSIGNMENT

|   |         |
|---|---------|
| 1 | Cathode |
| 2 | Anode   |
| 3 | Gate    |
| 4 | Anode   |

#### ORDERING INFORMATION

| Device | Package               | Shipping        |
|--------|-----------------------|-----------------|
| MCR8N  | TO–220AB              | 50 Units / Rail |
| MCR8NG | TO–220AB<br>(Pb–Free) | 50 Units / Rail |

Preferred devices are recommended choices for future use and best overall value.

# MCR8N

## THERMAL CHARACTERISTICS

| Characteristic  | Symbol                             | Value       | Unit          |
|---|------------------------------------|-------------|---------------|
| Thermal Resistance<br>Junction-to-Case<br>Junction-to-Ambient                 | $R_{\theta JC}$<br>$R_{\theta JA}$ | 2.2<br>62.5 | $^{\circ}C/W$ |
| Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Seconds | $T_L$                              | 260         | $^{\circ}C$   |

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}C$ unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|----------------|--------|-----|-----|-----|------|
|----------------|--------|-----|-----|-----|------|

### OFF CHARACTERISTICS

|   |                          |   |   |             |                      |
|---|--------------------------|---|---|-------------|----------------------|
| Peak Repetitive Forward or Reverse Blocking Current<br>( $V_D = \text{Rated } V_{DRM}$ and $V_{RRM}$ ; Gate Open) | $I_{DRM}$ ,<br>$I_{RRM}$ | - | - | 0.01<br>2.0 | mA                   |
|   |                          |   |   |             | $T_J = 25^{\circ}C$  |
|   |                          |   |   |             | $T_J = 125^{\circ}C$ |

### ON CHARACTERISTICS

|   |          |     |      |     |                      |
|---|----------|-----|------|-----|----------------------|
| Peak Forward On-State Voltage (Note )<br>( $I_{TM} = 16 A$ )                  | $V_{TM}$ | -   | -    | 1.8 | V                    |
| Gate Trigger Current (Continuous dc)<br>( $V_D = 12 V$ ; $R_L = 100 \Omega$ ) | $I_{GT}$ | 2.0 | 7.0  | 15  | mA                   |
| Holding Current<br>( $V_D = 12 V$ , Gate Open, Initiating Current = 200 mA)   | $I_H$    | 4.0 | 17   | 30  | mA                   |
| Latch Current<br>( $V_D = 12 V$ , $I_G = 15 mA$ )                             | $I_L$    | 6.0 | 20   | 40  | mA                   |
| Gate Trigger Voltage (Continuous dc)<br>( $V_D = 12 V$ ; $100 \Omega$ )       | $V_{GT}$ | 0.5 | 0.65 | 1.0 | V                    |
|   |          |     |      |     | $T_J = 25^{\circ}C$  |
| Gate Non-Trigger Voltage<br>( $V_D = 12 V$ ; $R_L = 100 \Omega$ )             | $V_{GD}$ | 0.2 | -    | -   | V                    |
|   |          |     |      |     | $T_J = 125^{\circ}C$ |

### DYNAMIC CHARACTERISTICS

|   |       |     |     |    |            |
|---|-------|-----|-----|----|------------|
| Critical Rate of Rise of Off-State Voltage<br>( $V_D = \text{Rated } V_{DRM}$ , Exponential Waveform, Gate Open, $T_J = 125^{\circ}C$ ) | dv/dt | 100 | 250 | -  | V/ $\mu s$ |
| Critical Rate of Rise of On-State Current<br>IPK = 50 A, Pw = 40 $\mu s$ , diG/dt = 1 A/ $\mu s$ , Igt = 50 mA                          | di/dt | -   | -   | 50 | A/ $\mu s$ |

2. Indicates Pulse Test: Pulse Width  $\leq 2.0$  ms, Duty Cycle  $\leq 2\%$ .

# MCR8N

## Voltage Current Characteristic of SCR

| Symbol    | Parameter                                 |
|-----------|---|
| $V_{DRM}$ | Peak Repetitive Off State Forward Voltage |
| $I_{DRM}$ | Peak Forward Blocking Current             |
| $V_{RRM}$ | Peak Repetitive Off State Reverse Voltage |
| $I_{RRM}$ | Peak Reverse Blocking Current             |
| $V_{TM}$  | Peak On State Voltage                     |
| $I_H$     | Holding Current                           |

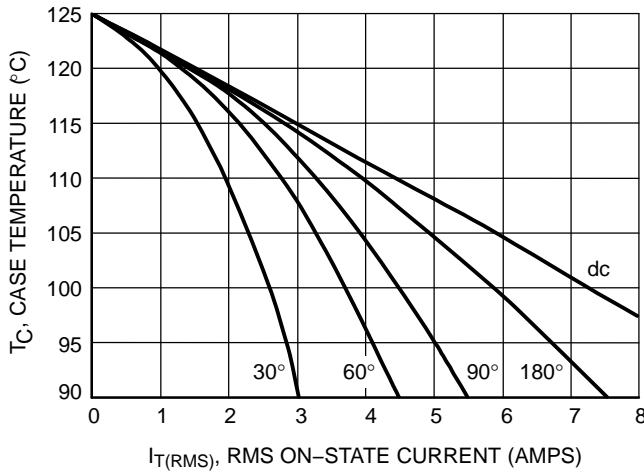
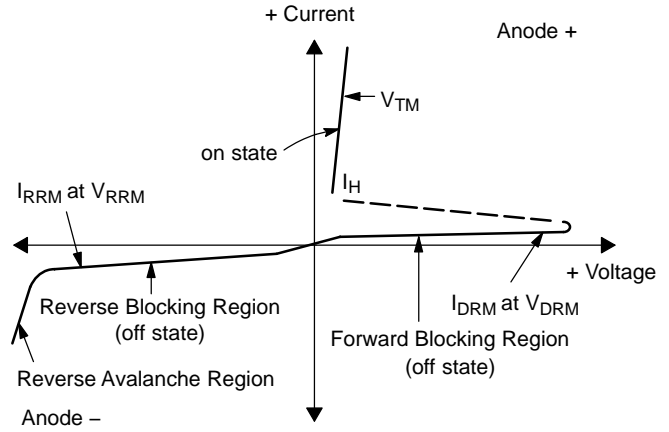


Figure 1. Typical RMS Current Derating

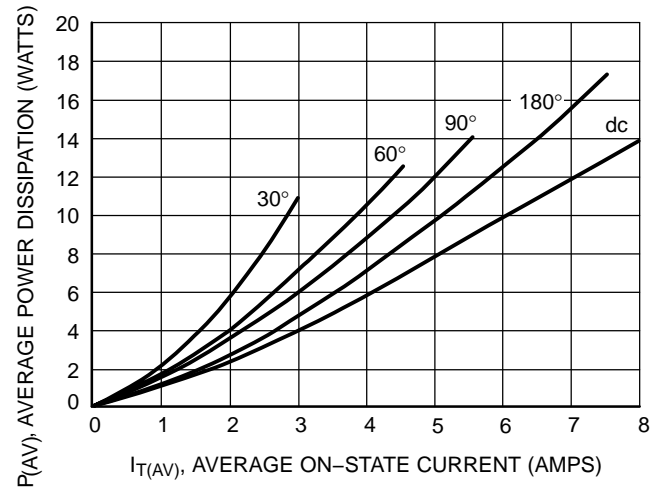


Figure 2. On-State Power Dissipation

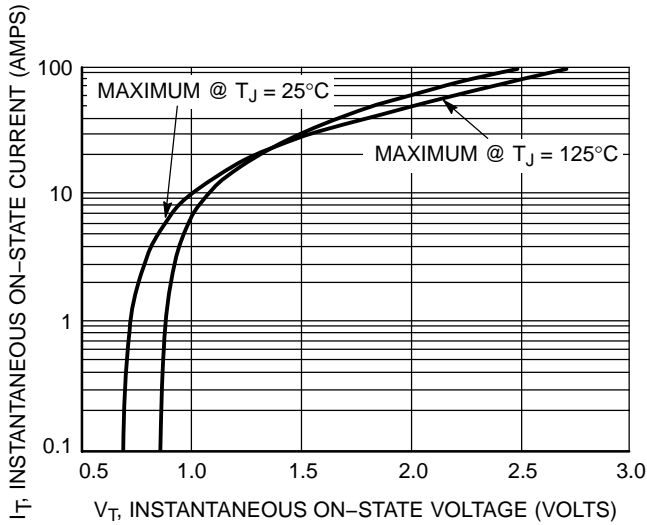


Figure 3. Typical On-State Characteristics

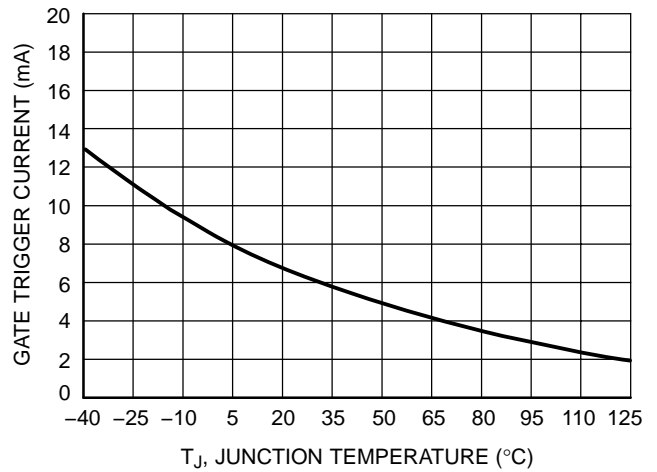


Figure 4. Typical Gate Trigger Current versus Junction Temperature

# MCR8N

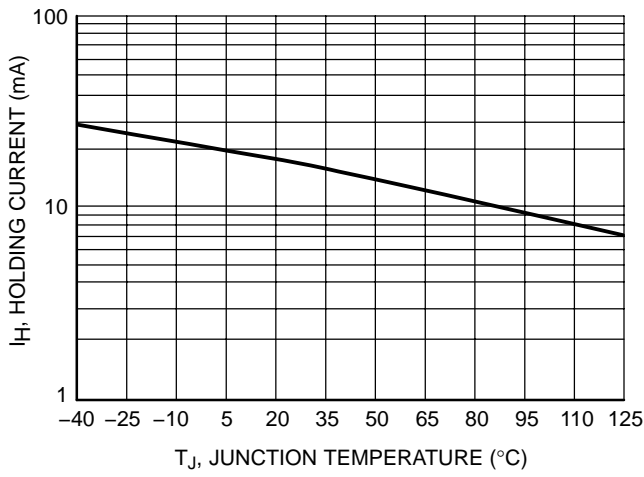


Figure 5. Typical Holding Current versus Junction Temperature

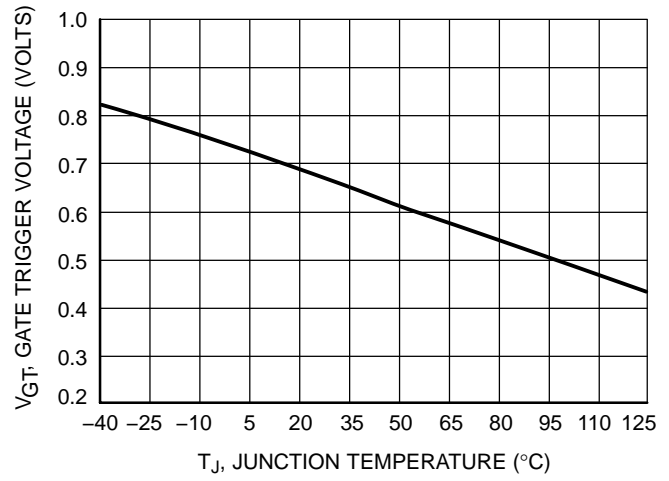


Figure 6. Typical Gate Trigger Voltage versus Junction Temperature

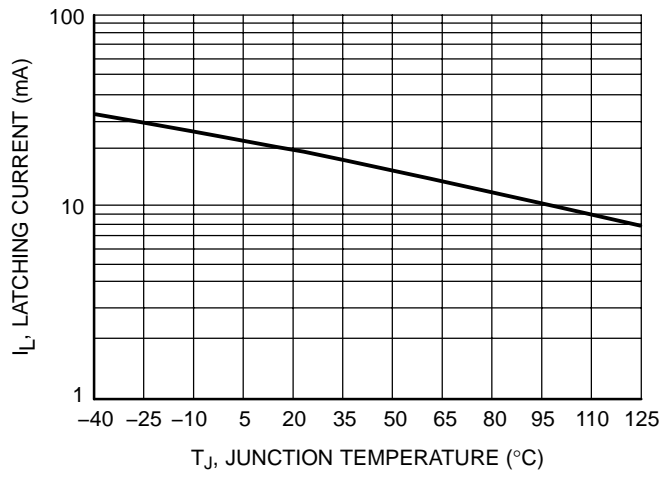
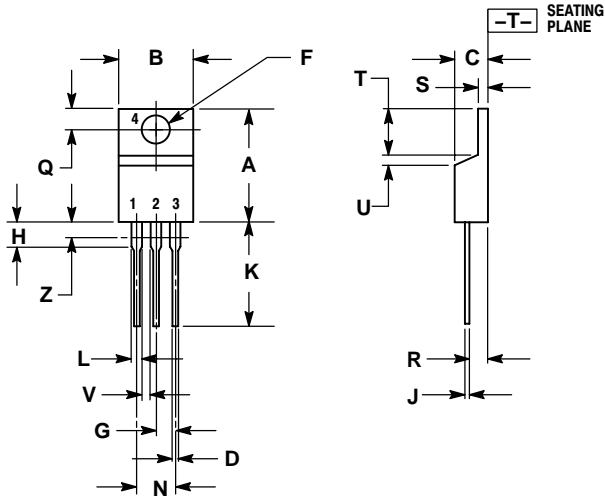


Figure 7. Typical Latching Current versus Junction Temperature

# MCR8N

## PACKAGE DIMENSIONS

TO-220AB  
CASE 221A-09  
ISSUE AA



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

| DIM | INCHES |       | MILLIMETERS |       |
|-----|--------|-------|-------------|-------|
|     | MIN    | MAX   | MIN         | MAX   |
| A   | 0.570  | 0.620 | 14.48       | 15.75 |
| B   | 0.380  | 0.405 | 9.66        | 10.28 |
| C   | 0.160  | 0.190 | 4.07        | 4.82  |
| D   | 0.025  | 0.035 | 0.64        | 0.88  |
| F   | 0.142  | 0.147 | 3.61        | 3.73  |
| G   | 0.095  | 0.105 | 2.42        | 2.66  |
| H   | 0.110  | 0.155 | 2.80        | 3.93  |
| J   | 0.018  | 0.025 | 0.46        | 0.64  |
| K   | 0.500  | 0.562 | 12.70       | 14.27 |
| L   | 0.045  | 0.060 | 1.15        | 1.52  |
| N   | 0.190  | 0.210 | 4.83        | 5.33  |
| Q   | 0.100  | 0.120 | 2.54        | 3.04  |
| R   | 0.080  | 0.110 | 2.04        | 2.79  |
| S   | 0.045  | 0.055 | 1.15        | 1.39  |
| T   | 0.235  | 0.255 | 5.97        | 6.47  |
| U   | 0.000  | 0.050 | 0.00        | 1.27  |
| V   | 0.045  | ---   | 1.15        | ---   |
| Z   | ---    | 0.080 | ---         | 2.04  |

**STYLE 3:**

- PIN 1. CATHODE
2. ANODE
3. GATE
4. ANODE

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

**PUBLICATION ORDERING INFORMATION**

**LITERATURE FULFILLMENT:**  
Literature Distribution Center for ON Semiconductor  
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA  
**Phone:** 480-829-7710 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 480-829-7709 or 800-344-3867 Toll Free USA/Canada  
**Email:** orderlit@onsemi.com

**N. American Technical Support:** 800-282-9855 Toll Free USA/Canada  
**Japan:** ON Semiconductor, Japan Customer Focus Center  
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051  
**Phone:** 81-3-5773-3850

**ON Semiconductor Website:** <http://onsemi.com>  
**Order Literature:** <http://www.onsemi.com/litorder>  
For additional information, please contact your local Sales Representative.