

NDD02N40, NDT02N40

N-Channel Power MOSFET 400 V, 5.5 Ω

Features

- 100% Avalanche Tested
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

ABSOLUTE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	NDD	NDT	Unit
Drain-to-Source Voltage	V _{DSS}	400		V
Gate-to-Source Voltage	V _{GS}	±20		V
Continuous Drain Current R _{θJC} Steady State, T _C = 25°C (Note 1)	I _D	1.7	0.4	A
Continuous Drain Current R _{θJC} Steady State, T _C = 100°C (Note 1)	I _D	1.1	0.27	A
Power Dissipation – R _{θJC} Steady State, T _C = 25°C	P _D	39	2.0	W
Pulsed Drain Current	I _{DM}	6.9	1.7	A
Continuous Source Current (Body Diode)	I _S	1.7	0.4	A
Single Pulse Drain-to-Source Avalanche Energy, I _D = 1 A	EAS	120		mJ
Maximum Temperature for Soldering Leads	T _L	260		°C
Operating Junction and Storage Temperature	T _J , T _{STG}	-55 to +150		°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Limited by maximum junction temperature
2. I_S = 1.7 A, di/dt ≤ 100 A/μs, V_{DD} ≤ BV_{DSS}, T_J = +150°C

THERMAL RESISTANCE

Parameter	Symbol	Value	Unit
Junction-to-Case (Drain)	R _{θJC}	3.2	°C/W
Junction-to-Ambient Steady State	R _{θJA}		°C/W
NDD02N40 (Note 4)		39	
NDD02N40-1 (Note 3)		96	
NDT02N40 (Note 4)		62	
NDT02N40 (Note 5)		151	

3. Insertion mounted
4. Surface mounted on FR4 board using 1" sq. pad size (Cu area = 1.127" sq. [2 oz] including traces)
5. Surface-mounted on FR4 board using minimum recommended pad size (Cu area = 0.026" sq. [2 oz]).

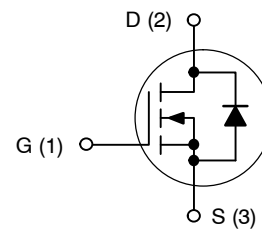


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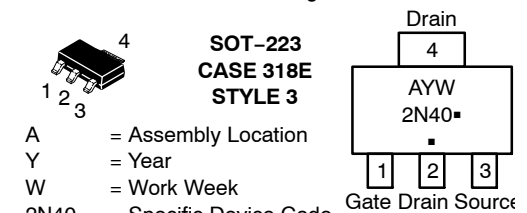
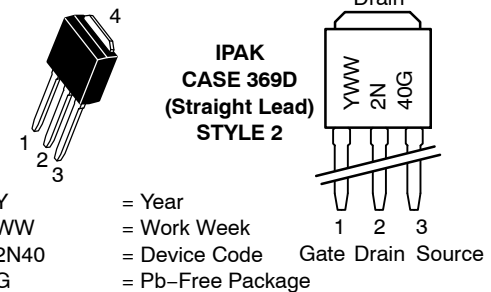
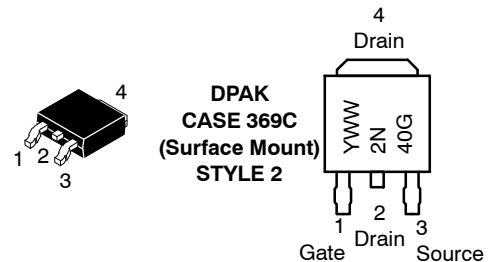
<http://onsemi.com>

V _{(BR)DSS}	R _{DS(ON)} MAX
400 V	5.5 Ω @ 10 V

N-Channel MOSFET



MARKING DIAGRAMS



Y = Year
WW = Work Week
2N40 = Device Code
G = Pb-Free Package

A = Assembly Location
Y = Year
W = Work Week
2N40 = Specific Device Code
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

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ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA	400			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	Reference to 25°C, I _D = 1 mA		460		mV/°C
Drain-to-Source Leakage Current	I _{DSS}	V _{DS} = 400 V, V _{GS} = 0 V	T _J = 25°C		1	μA
			T _J = 125°C		50	
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = ±20 V			±10	μA

ON CHARACTERISTICS (Note 6)

Gate Threshold Voltage	V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250 μA	0.8	1.6	2	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J	Reference to 25°C, I _D = 50 μA		4.6		mV/°C
Static Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 0.22 A		4.5	5.5	Ω
Forward Transconductance	g _{FS}	V _{DS} = 15 V, I _D = 0.22 A		1.1		S

DYNAMIC CHARACTERISTICS

Input Capacitance (Note 7)	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		121		pF
Output Capacitance (Note 7)	C _{oss}			16		
Reverse Transfer Capacitance (Note 7)	C _{rss}			3		
Total Gate Charge (Note 7)	Q _g	V _{DS} = 200 V, I _D = 1.7 A, V _{GS} = 10 V		5.5		nC
Gate-to-Source Charge (Note 7)	Q _{gs}			0.8		
Gate-to-Drain ("Miller") Charge (Note 7)	Q _{gd}			1.0		
Plateau Voltage	V _{GP}			3.1		
Gate Resistance	R _g			8.7		Ω

RESISTIVE SWITCHING CHARACTERISTICS (Note 8)

Turn-on Delay Time	t _{d(on)}	V _{DD} = 200 V, I _D = 1.7 A, V _{GS} = 10 V, R _G = 0 Ω		5		ns
Rise Time	t _r			7		
Turn-off Delay Time	t _{d(off)}			14		
Fall Time	t _f			4		

SOURCE-DRAIN DIODE CHARACTERISTICS

Diode Forward Voltage	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V	T _J = 25°C		0.9	1.6	V
			T _J = 100°C		0.8		
Reverse Recovery Time	t _{rr}	V _{GS} = 0 V, V _{DD} = 30 V, I _S = 1.7 A, d _i /d _t = 100 A/μs		146		ns	
Charge Time	t _a			37			
Discharge Time	t _b			109			
Reverse Recovery Charge	Q _{rr}			260			nC

6. Pulse Width ≤ 380 μs, Duty Cycle ≤ 2%.

7. Guaranteed by design.

8. Switching characteristics are independent of operating junction temperatures.

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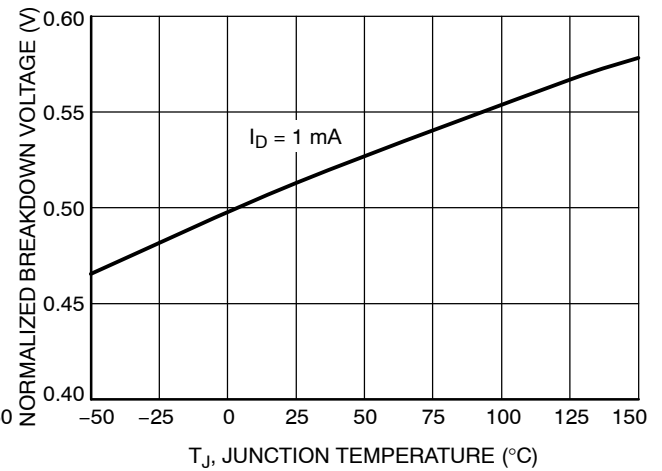
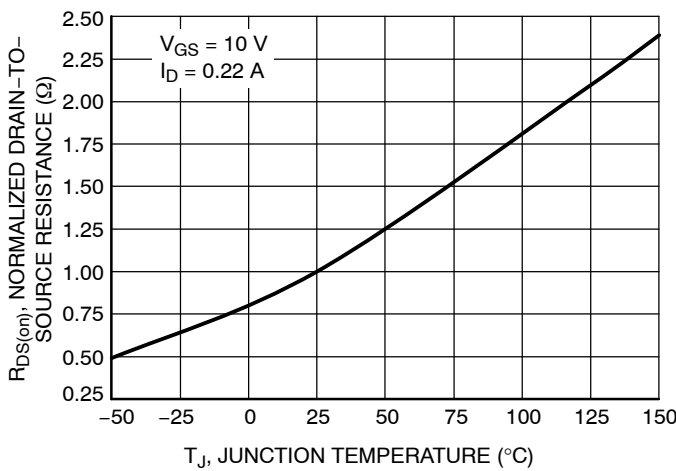
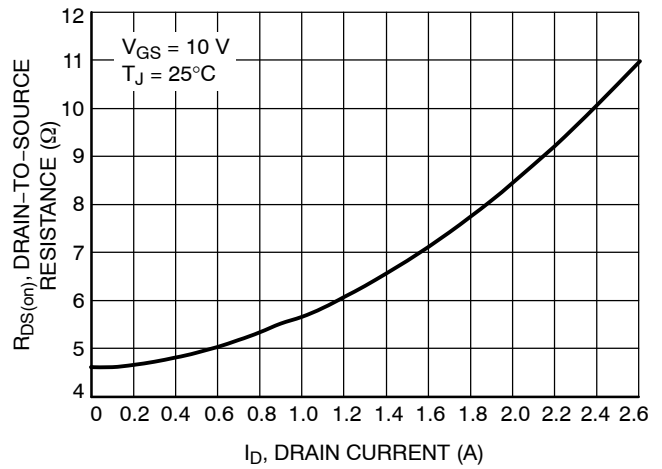
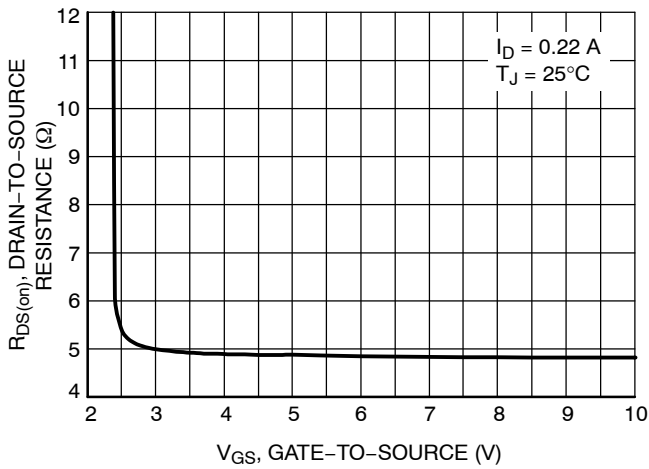
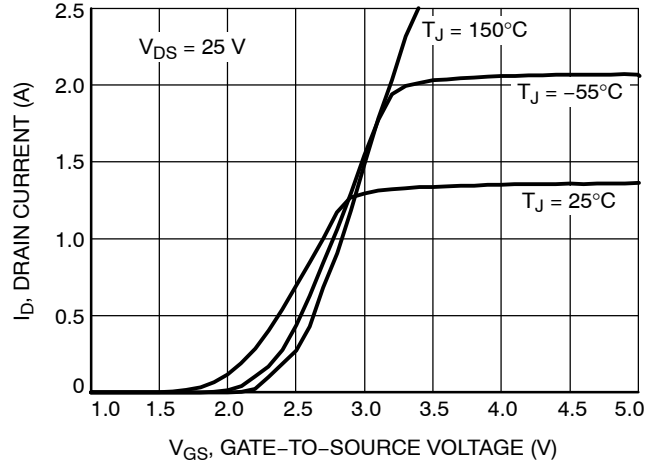
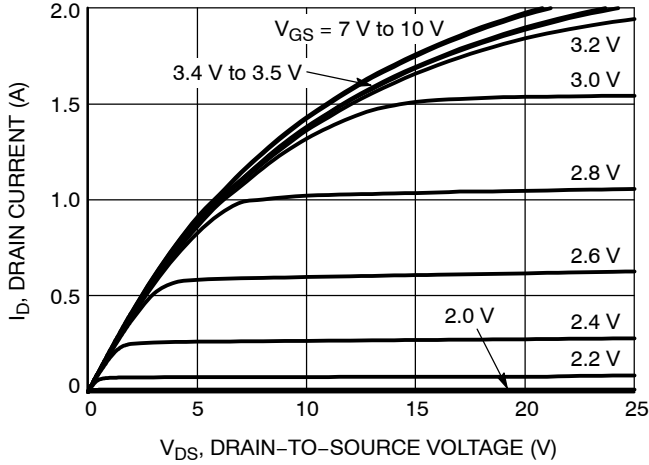
ORDERING INFORMATION

Device	Package	Shipping†
NDD02N40-1G	IPAK (Pb-Free, Halogen Free)	75 Units / Rail
NDD02N40T4G	DPAK (Pb-Free, Halogen Free)	2500 / Tape & Reel
NDT02N40T1G	SOT-223 (Pb-Free, Halogen Free)	1000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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TYPICAL CHARACTERISTICS



NDD02N40, NDT02N40

TYPICAL CHARACTERISTICS

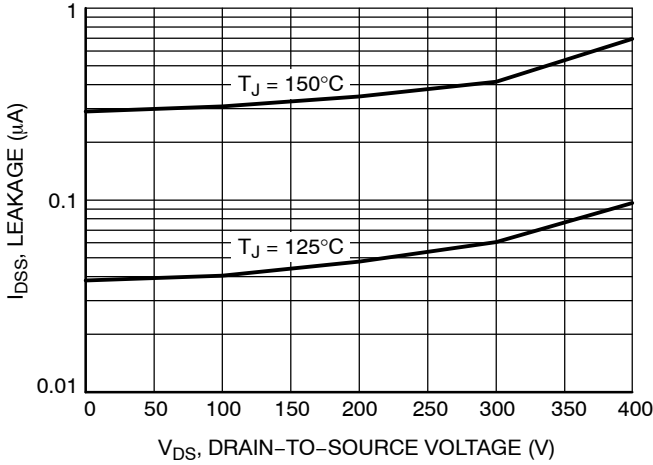


Figure 7. Drain-to-Source Leakage Current vs. Voltage

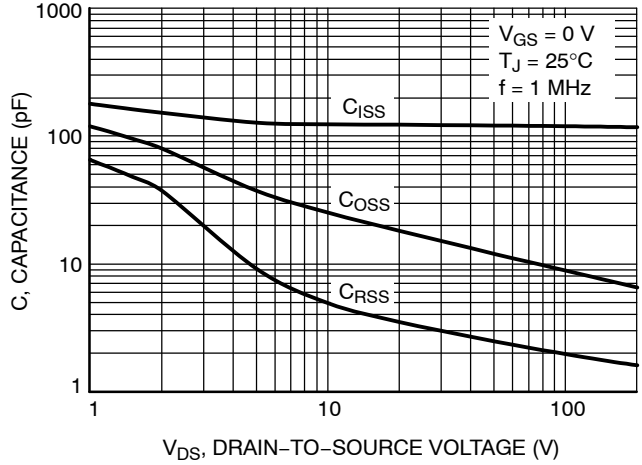


Figure 8. Capacitance Variation

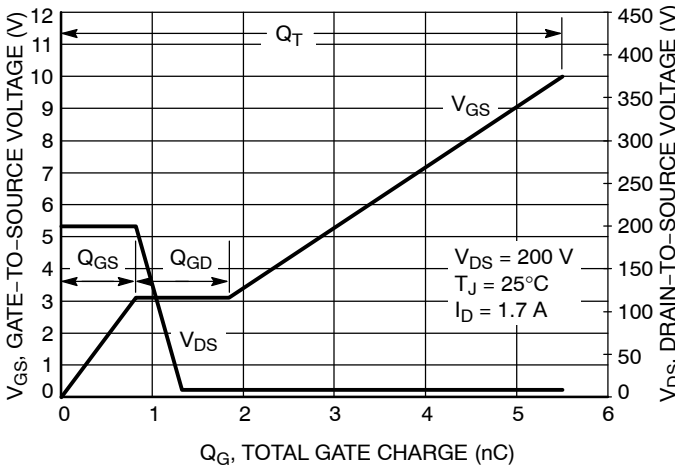


Figure 9. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

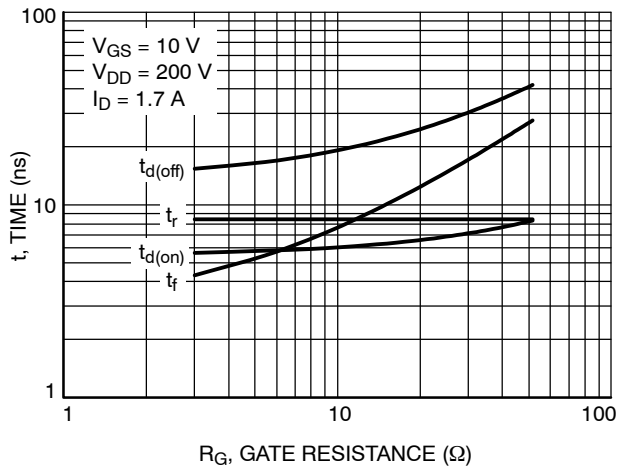


Figure 10. Resistive Switching Time Variation vs. Gate Resistance

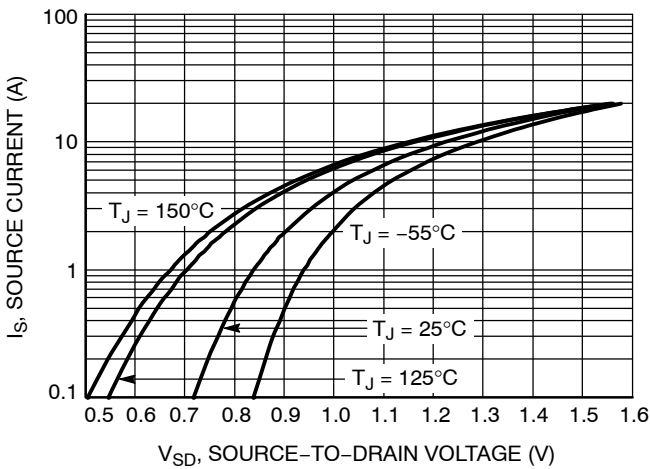


Figure 11. Diode Forward Voltage vs. Current

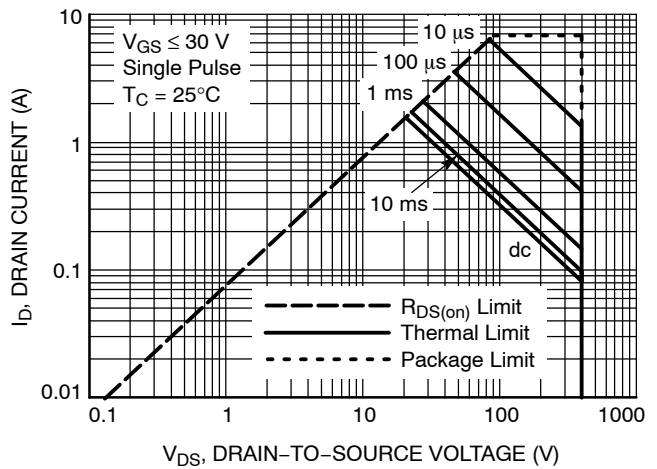


Figure 12. Maximum Rated Forward Biased Safe Operating Area for NDD02N40

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TYPICAL CHARACTERISTICS

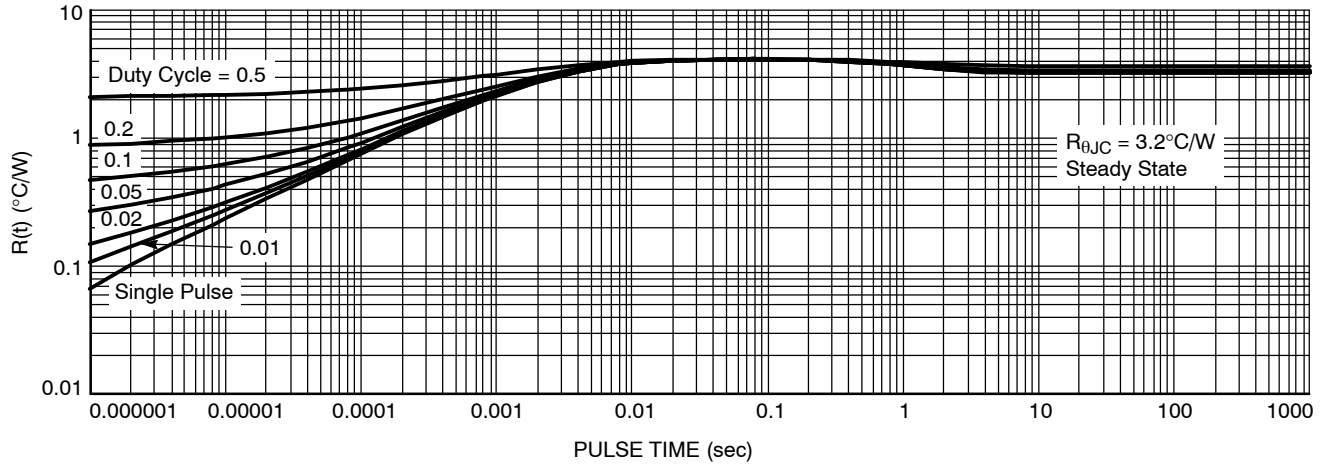
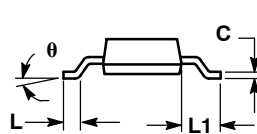
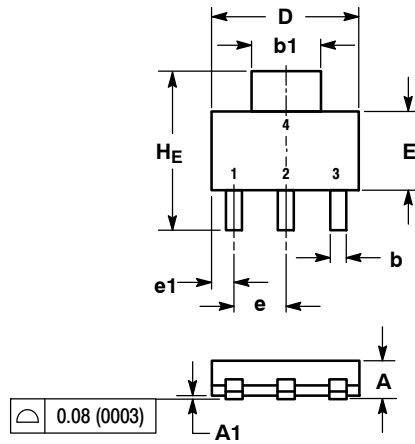


Figure 13. Thermal Impedance

NDD02N40, NDT02N40

PACKAGE DIMENSIONS

SOT-223 (TO-261)
CASE 318E-04
ISSUE N

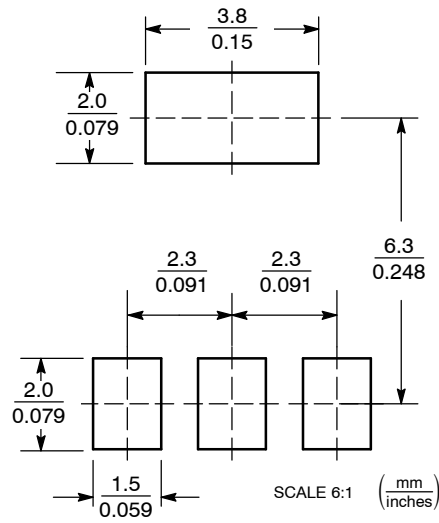


NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCH.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.50	1.63	1.75	0.060	0.064	0.068
A1	0.02	0.06	0.10	0.001	0.002	0.004
b	0.60	0.75	0.89	0.024	0.030	0.035
b1	2.90	3.06	3.20	0.115	0.121	0.126
c	0.24	0.29	0.35	0.009	0.012	0.014
D	6.30	6.50	6.70	0.249	0.256	0.263
E	3.30	3.50	3.70	0.130	0.138	0.145
e	2.20	2.30	2.40	0.087	0.091	0.094
e1	0.85	0.94	1.05	0.033	0.037	0.041
L	0.20	---	---	0.008	---	---
L1	1.50	1.75	2.00	0.060	0.069	0.078
HE	6.70	7.00	7.30	0.264	0.276	0.287
θ	0°	-	10°	0°	-	10°

STYLE 3:
PIN 1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

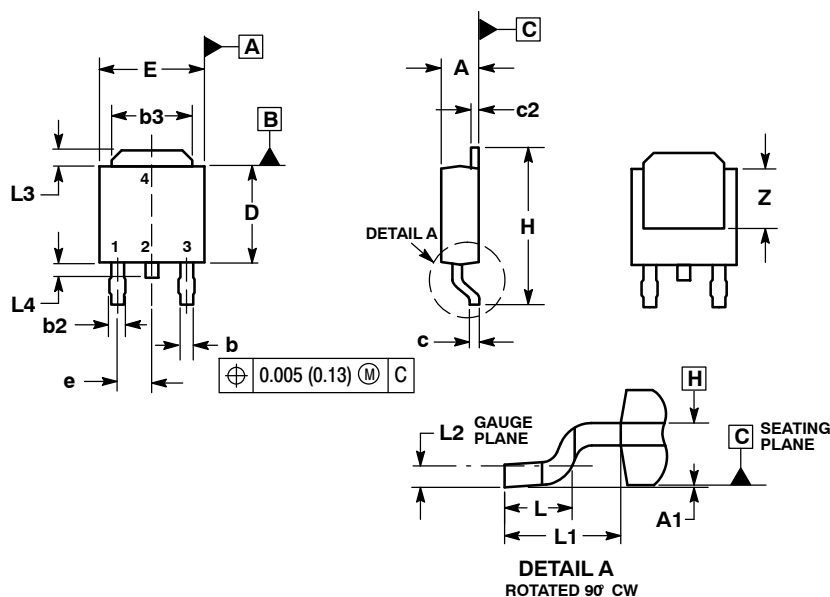
SOLDERING FOOTPRINT



NDD02N40, NDT02N40

PACKAGE DIMENSIONS

DPAK (SINGLE GAUGE) CASE 369C ISSUE D



NOTES:

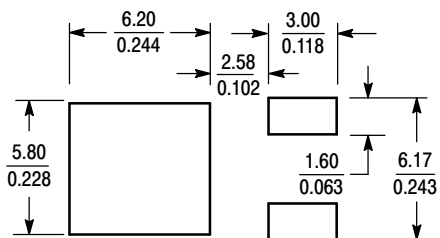
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS b3, L3 and Z.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
6. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.030	0.045	0.76	1.14
b3	0.180	0.215	4.57	5.46
c	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
E	0.250	0.265	6.35	6.73
e	0.090 BSC		2.29 BSC	
H	0.370	0.410	9.40	10.41
L	0.055	0.070	1.40	1.78
L1	0.108 REF		2.74 REF	
L2	0.020 BSC		0.51 BSC	
L3	0.035	0.050	0.89	1.27
L4	---	0.040	---	1.01
Z	0.155	---	3.93	---

STYLE 2:

- PIN 1. GATE
- DRAIN
- SOURCE
- DRAIN

SOLDERING FOOTPRINT*



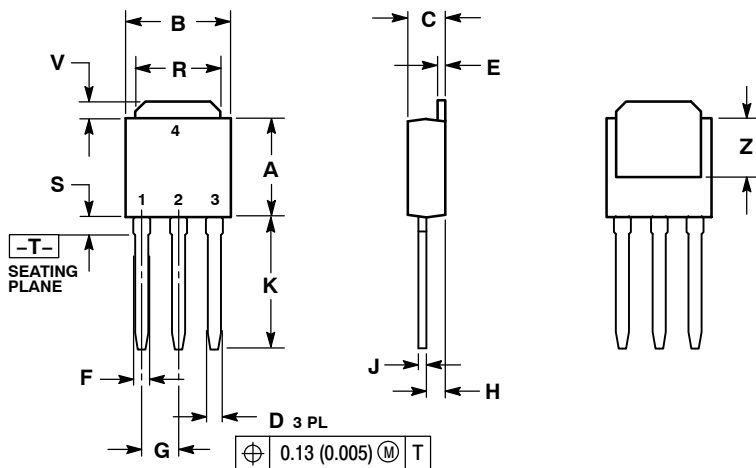
SCALE 3:1 $\left(\frac{\text{mm}}{\text{inches}}\right)$

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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PACKAGE DIMENSIONS


IPAK CASE 369D ISSUE C



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.235	0.245	5.97	6.35
B	0.250	0.265	6.35	6.73
C	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
E	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.090 BSC		2.29 BSC	
H	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.350	0.380	8.89	9.65
R	0.180	0.215	4.45	5.45
S	0.025	0.040	0.63	1.01
V	0.035	0.050	0.89	1.27
Z	0.155	---	3.93	---

- STYLE 2:
PIN 1. GATE
2. DRAIN
3. SOURCE
4. DRAIN

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