

#### **DATA SHEET**

# **SKY77340 PA Module for Quad-Band GSM / EDGE**

## **Applications**

· Quad-band cellular handsets:

#### **GMSK Modulation**

- Class 4 GSM850/900
- Class 1 DCS1800/PCS1900
- Class 12 GPRS multi-slot operation

#### **EDGE** modulation

- Class E2 GSM850/900
- Class E2 DCS1800/ PCS1900

#### **Features**

- · High efficiency:
  - GSM850 54%
  - GSM900 53%
  - DCS 52%
  - PCS 52%
- Input/Output matching  $50~\Omega$  internal (with DC blocking)
- 16-pin MCM
- · Small outline
  - 6 x 8 mm
- Low profile
  - 1.2 mm
- · Gold-plated, lead-free contacts
- Low APC current
  - 10 uA

Skyworks offers lead (Pb)-free "environmentally friendly" packaging that is RoHS compliant (European Parliament for the Restriction of Hazardous Substances).

## Description

The SKY77340 Power Amplifier Module (PAM) is designed in a compact form factor for quad-band cellular handsets comprising GSM850/900, DCS1800, PCS1900, supporting GMSK and linear EDGE modulation. Class 12 General Packet Radio Service (GPRS) multi-slot operation is also supported.

The module consists of a GSM850/900 PA block and a DCS1800/PCS1900 PA block, impedancematching circuitry for 50  $\Omega$  input and output impedances, and a Multi-function Power Amplifier Control (MFC) block. A custom CMOS integrated circuit provides the internal MFC function and interface circuitry.

Two separate Heterojunction Bipolar Transistor (HBT) PA blocks are fabricated onto InGaP die; one supports the GSM850/900 bands, the other supports the DCS1800 and PCS1900 bands. Both PA blocks share common power supply pins to distribute current. The InGaP die, the silicon die, and the passive components are mounted on a multi-layer laminate substrate. The assembly is encapsulated with plastic overmold.

RF input and output ports are internally matched to 50  $\Omega$  to reduce the number of external components. Extremely low leakage current (2.5 µA, typical) maximizes handset standby time. Band select (BS) circuitry selects GSM transmit frequency band (logic 0) and DCS/PCS transmit frequency band (logic 1), MODE circuitry selects GMSK modulation (logic 0) or EDGE modulation (logic 1), VRAMP controls the output power for GMSK modulation and provides bias optimization for EDGE modulation depending on the state of MODE control.

The integrated multi-function control (MFC) provides envelope amplitude control in GMSK mode, reducing sensitivity to input drive, temperature, power supply, and process variation. In EDGE mode, the MFC configures the PA for fixed gain, and provides the ability to optimize the PA bias operation at different power levels. This circuitry regulates PA bias conditions, reducing sensitivity to temperature, power supply, and process variation. The Enable input signal (pin 8) provides a standby state to minimize battery drain.

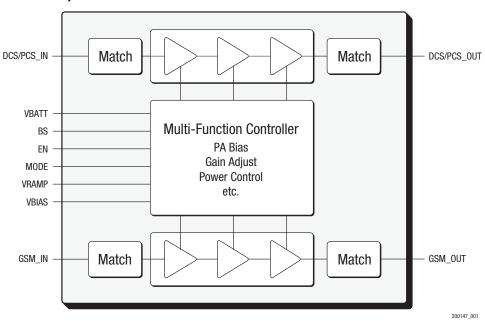


Figure 1. SKY77340 Functional Block Diagram

# **Electrical Specifications**

The absolute maximum ratings of the SKY77340 are provided in Table 1 and the recommended operating conditions are specified

in Table 2. Table 3 provides the control logic and Table 4 provides the electrical specifications.

**Table 1. Absolute Maximum Ratings** 

| 14410 11 1441 |        |             |         |         |      |  |  |
|---|--------|-------------|---------|---------|------|--|--|
| Parameter   | Symbol | Minimum     | Typical | Maximum | Unit |  |  |
| RF Input Power  | Pin    | _           | _       | 12      | dBm  |  |  |
| Case Storage Temperature  | Tstg   | <b>-</b> 55 | _       | 150     | °C   |  |  |
| Supply Voltage  | VBATT  | -0.3        | _       | 7.0     | V    |  |  |
| Power Control Voltage   | VRAMP  | -0.3        | _       | VBATT   | V    |  |  |
| PA Bias Voltage   | VBIAS  | -0.3        | _       | VBATT   | V    |  |  |
| Transmit enable   | EN     | -0.3        | _       | VBATT   | V    |  |  |
| Band select   | BS     | -0.3        | _       | VBATT   | V    |  |  |
| Mode select   | MODE   | -0.3        | _       | VBATT   | V    |  |  |

**Table 2. SKY77340 Recommended Operating Conditions** 

| Parameter  | Symbol   | Minimum                  | Typical | Maximum                | Unit |
|--|----------|--------------------------|---------|------------------------|------|
| Transmit Duty Cycle <sup>1</sup>   | Dтx      | 1/8                      | _       | 1/2                    | _    |
| Case Operating Temperatures <sup>1</sup> 1-Slot (12.5% duty cycle) 2-Slot (25% duty cycle) 3-Slot (37.5% duty cycle) 4-Slot (50% duty cycle) | (Trange) | -25<br>-25<br>-25<br>-25 |         | 100<br>100<br>85<br>85 | °C   |
| Voltage Operating Range  | Vrange   | 3.0                      | 3.5     | 4.8                    | V    |

 $<sup>^{1}</sup>$  TFRAME = 4.615 mS

Table 3. SKY77340 Control Logic

| Operational State | EN | BS | MODE | NOTES                                       |
|-------------------|----|----|------|---|
| Standby           | 0  | Х  | Х    | X = don't care                              |
| Low band GMSK     | 1  | 0  | 0    | VRAMP controls output power                 |
| Low band EDGE     | 1  | 0  | 1    | VBIAS sets PA bias condition, fixed gain PA |
| High band GMSK    | 1  | 1  | 0    | VRAMP controls output power                 |
| High band EDGE    | 1  | 1  | 1    | VBIAS sets PA bias condition, fixed gain PA |

Table 4. SKY77340 Electrical Specifications (1 of 17)

|                             |   |           | General   |            |         |              |       |
|-----------------------------|---|-----------|---|------------|---------|--------------|-------|
| Paran                       | neter                                       | Symbol    | Test Condition  | Minimum    | Typical | Maximum      | Units |
| Supply voltage              |   | VBATT     | _   | 3.0        | 3.5     | 4.8          | ٧     |
| Analog power control imped  | dance                                       | ZRAMP     | _   |            | 200     | _            | kΩ    |
| Analog PA bias control impe | edance                                      | ZBIAS     | _   | _          | 200     | _            | kΩ    |
| ENable                      | Control voltage LOW<br>Control voltage HIGH | VEN       | _   | 0.0<br>1.5 |         | 0.5<br>Vbatt | ٧     |
| ENable current              |   | len       | _   |            | _       | 30           | μA    |
| Band select                 | Control voltage LOW<br>Control voltage HIGH | VBS       | _   | 0.0<br>1.5 |         | 0.5<br>Vbatt | ٧     |
| Band select current         |   | IBS       | _   |            | _       | 30           | μA    |
| MODE                        |   | VMODE     | GMSK<br>EDGE  | 0.0<br>1.5 | _       | 0.5<br>Vbatt | ٧     |
|                             |   | IMODE     | $V_{\text{MODE}} \leq 2.7 \text{ V}$  | _          | _       | 30           | μA    |
| "Off" Current               |   | loff      | Sum Current on all pins $V_{BATT} = 3.5 \text{ V}$ $EN \leq 0.1 \text{ V}$ $V_{RAMP} \leq 0.1 \text{ V}$ $MODE \leq 0.1 \text{ V}$ $Temp = 25 \text{ °C}$ | _          | _       | 30           | μΑ    |
| "On" Current                |   | IBATT MAX |   | _          | _       | 2.2          | Α     |

Table 4. SKY77340 Electrical Specifications (2 of 17)

|  | Mode: 1                             | ransmit GMSK and EDGE <sup>1</sup> Band: CEL  |               |                            |         |      |  |
|--|-------------------------------------|---|---------------|----------------------------|---------|------|--|
|  |                                     | General Test Conditions   |               |                            |         |      |  |
| Frequency = 824-849 MHz<br>Pulse Rate = 12.5% Duty Cycle | TFRAME = 4.615<br>Control States: I |   | RF Ports = GS | RF Ports = GSM_IN, GSM_OUT |         |      |  |
| Parameter  | Symbol                              | Condition   | Minimum       | Typical                    | Maximum | Unit |  |
|  | Рдзм                                | $Vcc = 3.5 V$ $Tcase = 25 °C$ $-1 dBm \le Pin \le 6 dBm$  | 34.5          | 35.1                       | _       |      |  |
|  | PGSM_EX-L                           | $\label{eq:Vcc} \begin{split} &\text{Vcc} = 3.0 \text{ V} \\ &\text{Tcase} = \text{Trange} \\ &-1 \text{ dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{split}$   | 32.0          | 33.7                       | _       |      |  |
| Output Power   | Pgsm_ex-H                           | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 4.8 \text{ V} \\ &\text{Tcase} = \text{Trange} \\ &-1 \text{ dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{aligned}$   | 34.5          | 35.9                       | _       | dBm  |  |
|  | Pedge                               | Vcc = 3.5 V<br>Tcase = 25 °C<br>Pout = Pin + Gedge  | 28.5          | _                          | _       |      |  |
|  | Pedge_ex                            | Vcc = Vrange<br>Tcase = Trange  | 26.0          | _                          | _       |      |  |
|  | GEDGE                               | Vcc = 3.5 V<br>Vbias = 1.4 V<br>Pout = Pedge<br>Tcase = 25 °C<br>MODE = HIGH<br>Load = 50 ohms  | 31.5          | 33.0                       | 34.5    | dB   |  |
| Linear Gain  | GEDGE                               | $\label{eq:VCC} \begin{split} &\text{VCC} = 3.0 \text{ V} < \text{VBATT} < 4.8 \text{ V} \\ &\text{VBIAS} = 1.4 \text{ V} \\ &\text{POUT} = \text{PEDGE} \\ &\text{TCASE} = \text{TRANGE} \\ &\text{MODE} = \text{HIGH} \\ &\text{Load VSWR} \leq 3.1 \text{ all phase angles} \end{split}$ | 27.0          | 33.0                       | 36.0    | uБ   |  |
|  | Gvar                                | Vcc = 3.5 V<br>Tcase = Trange<br>MODE = HIGH  | _             | -0.034                     | _       | dB/C |  |
| Power Added Efficiency                                   | PAEGSM                              | Vcc = 3.5 V<br>TCASE = 25 °C<br>VRAMP = 1.6 V<br>PIN = 3 dBm  | 49            | 54                         | _       | %    |  |
|  | PAEEDGE                             | Vcc = 3.5 V<br>TCASE = 25 °C<br>POUT = 28.5 dBm   | 20            | 23                         |         |      |  |
|  | IGSM_LOW_POWER                      | Vcc = 3.5 V<br>TCASE = 25 °C<br>POUT = 6.5 dBm  | _             | 116                        | 130     |      |  |
| Low Power Current Consumption                            | ledge_low_power                     | Vcc = 3.5 V<br>Vbias = 0.3 V<br>Tcase = 25 °C<br>Pout = 6.5 dBm   | _             | 120                        | _       | mA   |  |

Table 4. SKY77340 Electrical Specifications (3 of 17)

|  |                          | Mode: Transmit                                      | t GMSK and EDGE <sup>1</sup> Band: CEL [conti  | inued}        |                            |         |       |  |
|--|--------------------------|---|--|---------------|----------------------------|---------|-------|--|
|  |                          |   | General Test Conditions  |               |                            |         |       |  |
| Frequency = 824-849 MHz<br>Pulse Rate = 12.5% Duty Cycle | )                        | TFRAME = 4.615 mS<br>Control States: EN = 1, BS = 0 |  | RF Ports = GS | RF Ports = GSM_IN, GSM_OUT |         |       |  |
| Paramet  | er                       | Symbol Condition                                    |  | Minimum       | Typical                    | Maximum | Unit  |  |
|  | 00 MHz 065 - 4           | NxSat   | $Vcc = 3.5 \text{ V}$ $Tcase = 25 \text{ °C}$ $Pout \leq PGSM$ $RBW = 100 \text{ kHz}$   | _             | -85.0                      | -83.5   |       |  |
| Noise Power  | 20 MHz Offset            | NxLin   | $Vcc = 3.5 \text{ V}$ $Tcase = 25 \text{ °C}$ $Pout \leq Pedge$ $RBW = 100 \text{ kHz}$  | _             | -84.0                      | -83.5   | dD    |  |
|  | 1000 to 1000 Mile        | NxSat _PCS  | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 3.5 \text{ V} \\ &\text{Tcase} = 25 \text{ °C} \\ &\text{Pout} \leq \text{PGSM} \\ &\text{RBW} = 100 \text{ kHz} \end{aligned}$  | _             | -100.0                     | -84.0   | dBm   |  |
|  | 1930 to 1990 MHz         | NxLin _PCS  | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 3.5 \text{ V} \\ &\text{Tcase} = 25 \text{ °C} \\ &\text{Pout} \leq \text{Pedge} \\ &\text{RBW} = 100 \text{ kHz} \end{aligned}$ | _             | -100.0                     | -84.0   |       |  |
| Forward Isolation  |                          | Iso   |  | _             | -40                        | -30     | dBm   |  |
| Our constant and a street                                | Fundamental              | Iso_CEL   | Vcc = Vrange<br>Tcase = Trange   | _             | -10                        | 0       | -ID   |  |
| Crossover Isolation                                      | 2 <sup>nd</sup> Harmonic | Iso_DCS   | Vcc = Vrange<br>Tcase = Trange   | _             | -30                        | -20     | dBm   |  |
| Input VCIAID   |                          | VSWR_SAT  | 6.5 dBm ≤ Pout ≤ Pgsm<br>Vcc = Vrange<br>Tcase = Trange  | _             | 1.8                        | 2.25    | Dotio |  |
| Input VSWR   |                          | VSWR_LIN  | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = \text{Vrange} \\ &\text{Tcase} = \text{Trange} \\ &\text{Pout} \leq \text{Pedge} \end{aligned}$                                  | _             | 1.2                        | 2.0     | Ratio |  |
| Harmania   |                          | <b>2</b> fo   | Measured at GSM_OUT POUT ≤ PGSM VCC = VRANGE TCASE = TRANGE Load = 50 ohms   | _             | -25                        | -10     | 4D    |  |
| Harmonics  |                          | 3fo to 15fo   | Measured at GSM_OUT POUT ≤ PGSM VCC = VRANGE TCASE = TRANGE Load = 50 ohms   | _             | -25                        | -17     | dBm   |  |

Table 4. SKY77340 Electrical Specifications (4 of 17)

|  |        |                                   | 340 Electrical Specifications (4 of the control of |                            |            |         |      |
|--|--------|-----------------------------------|---|----------------------------|------------|---------|------|
|  |        |                                   | General Test Conditions   |                            |            |         |      |
| Frequency = 824-849 MHz<br>Pulse Rate = 12.5% Duty Cycle |        | TFRAME = 4.615<br>Control States: | mS<br>EN = 1, BS = 0  | RF Ports = GSM_IN, GSM_OUT |            |         |      |
| Parameter  |        | Symbol                            | Condition   | Minimum                    | Typical    | Maximum | Unit |
|  | ACPR1  |                                   | RBW = 30 kHz POUT ≤ PEDGE VCC = 3.5 V TCASE = 25 °C MODE = HIGH Offset = ±200 kHz Load = 50 0hms  | _                          | -37        | -33     |      |
|  | AUFILI |                                   | RBW = 30 kHz POUT ≤ PEDGE_EX VCC = VRANGE TCASE = TRANGE MODE = HIGH Offset = ±200 kHz Load VSWR ≤ 3:1, all phase angles  | _                          | -35        | _       |      |
|  | ACPR2  |                                   | $RBW = 30 \text{ kHz}$ $Pout \leq Pedge$ $Vcc = 3.5 \text{ V}$ $Tcase = 25 \text{ °C}$ $MODE = HIGH$ $Offset = \pm 400 \text{ kHz}$ $Load = 50 \text{ Ohms}$  | _                          | -60        | -58     |      |
| Adjacent Channel Leakage  ACPR:                          | AUTIL  |                                   | RBW = 30 kHz POUT ≤ PEDGE_EX VCC = VRANGE TCASE = TRANGE MODE = HIGH Offset = ±400 kHz Load VSWR ≤ 3:1, all phase angles  | _                          | -57        | _       | dΩo  |
|  | ACDDO  |                                   | RBW = 30 kHz POUT ≤ PEDGE VCC = 3.5 V TCASE = 25 °C MODE = HIGH Offset = ±600 kHz Load = 50 0hms  | _                          | -77        | -63     | dBc  |
|  | АСРНЗ  |                                   | $\label{eq:resolvent} \begin{array}{l} RBW = 30 \text{ kHz} \\ Pout \leq Pedge\_ex \\ Vcc = Vrange \\ Tcase = Trange \\ MODE = HIGH \\ Offset = \pm 600 \text{ kHz} \\ Load VSWR \leq 3:1, \text{ all phase angles} \end{array}$  | _                          | -65        | _       |      |
|  | 40004  |                                   | RBW = 100 kHz POUT ≤ PEDGE VCC = 3.5 V TCASE = 25 °C MODE = HIGH Offset = ±1.8 MHz Load = 50 0hms   | _                          | -81        | -66     |      |
|  | ACPR4  |                                   | $\label{eq:bounds} \begin{split} RBW &= 100 \text{ kHz} \\ Pout &\leq \text{Pedge\_ex} \\ Vcc &= \text{Vrange} \\ Tcase &= \text{Trange} \\ MODE &= \text{HIGH} \\ Offset &= \pm 1.8 \text{ MHz} \\ Load VSWR &\leq 3:1, \text{ all phase angles} \end{split}$  | _                          | <b>-75</b> | _       |      |

## Table 4. SKY77340 Electrical Specifications (5 of 17)

|   | Mode: Transmit                    | t GMSK and EDGE <sup>1</sup> Band: CEL [continued   | }                         |              |         |       |
|---|-----------------------------------|---|---------------------------|--------------|---------|-------|
|   |                                   | General Test Conditions   |                           |              |         |       |
| Frequency = 824-849 MHz<br>Pulse Rate = 12.5% Duty Cycle  | TFRAME = 4.615<br>Control States: |   | RF Ports = GS             | 6M_IN, GSM_C | DUT     |       |
| Parameter   | Symbol                            | Condition   | Minimum                   | Typical      | Maximum | Unit  |
| Error Vector Magnitude  | EVM1                              | $\label{eq:VCC} \begin{split} &\text{VCC} = 3.5 \text{ V} \\ &\text{TCASE} = 25 \text{ °C} \\ &\text{RBW} = 30 \text{ kHz} \\ &\text{Pout} \leq \text{Pedge} \\ &\text{Load} = 50 \Omega \end{split}$ | _                         | 1.5          | 5.0     |       |
|   | EVM2                              | VCC = VRANGE TCASE = TRANGE RBW = 30 kHz POUT \( \leq \text{PEDGE_EX} \) Load VSWR \( \leq 3:1 \), all phase angles   | _                         | 3.0          | 9.0     | %<br> |
| Stability (all spurious)  |                                   | $\begin{tabular}{ll} VCC &= VRANGE \\ TCASE &= TRANGE \\ 6.5 \ dBm &\leq POUT \leq PGSM \\ Load \ VSWR &= 8:1, \ all \ phase \ angles \\ \end{tabular}$   | _                         | _            | -36     | dBm   |
| Ruggedness  |                                   | Vcc = 4.8 V Tcase = Trange Pout = PGSM Pin = 6 dBm Load VSWR = 10:1, all phase angles   | No degradation  No damage |              |         |       |
| Mode Switching Time<br>(Time does not include loop lock time [pedestal] for<br>GMSK PAC operation.) | тморе                             | VCC = VRANGE<br>TCASE = TRANGE  |                           | 2            | 4       | μS    |

<sup>&</sup>lt;sup>1</sup> All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ ACPR).

Table 4. SKY77340 Electrical Specifications (6 of 17)

|  | Mode: Tra                           | nsmit GMSK and EDGE <sup>1</sup> Band: EGSM   |               |                            |         |      |  |
|--|-------------------------------------|---|---------------|----------------------------|---------|------|--|
|  |                                     | General Test Conditions   |               |                            |         |      |  |
| Frequency = 880-915 MHz<br>Pulse Rate = 12.5% Duty Cycle | TFRAME = 4.615<br>Control States: I |   | RF Ports = GS | RF Ports = GSM_IN, GSM_OUT |         |      |  |
| Parameter  | Symbol                              | Condition   | Minimum       | Typical                    | Maximum | Unit |  |
|  | PGSM                                | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 3.5 \text{ V} \\ &\text{Tcase} = 25 \text{ °C} \\ &-1 \text{ dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{aligned}$   | 34.5          | 34.9                       | _       |      |  |
|  | PGSM_EX-L                           | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 3.0 \text{ V} \\ &\text{Tcase} = \text{Trange} \\ &-1 \text{ dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{aligned}$   | 32.0          | 33.4                       |         |      |  |
| Output Power   | Pgsm_ex-H                           | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 4.8 \text{ V} \\ &\text{Tcase} = &\text{Trange} \\ &-1 \text{ dBm} \leq &\text{Pin} \leq &\text{6 dBm} \end{aligned}$   | 34.5          | 35.5                       | _       | dBm  |  |
|  | Pedge                               | Vcc = 3.5 V<br>Tcase = 25 °C<br>Pout = Pin + Gedge  | 28.5          | _                          | _       |      |  |
|  | PEDGE_EX                            | VCC = VRANGE<br>TCASE = TRANGE  | 26.0          | _                          | _       |      |  |
|  | GEDGE                               | Vcc = 3.5 V<br>Vbias = 1.4 V<br>Pout = Pedge<br>Tcase = 25 °C<br>MODE = HIGH<br>Load = 50 ohms  | 31.5          | 33.0                       | 34.5    | dB   |  |
| Linear Gain  | GEDGE                               | $\label{eq:VCC} \begin{split} &\text{VCC} = 3.0 \text{ V} < \text{VBATT} < 4.8 \text{ V} \\ &\text{VBIAS} = 1.4 \text{ V} \\ &\text{POUT} = \text{PEDGE} \\ &\text{TCASE} = \text{TRANGE} \\ &\text{MODE} = \text{HIGH} \\ &\text{Load VSWR} \leq 3:1 \text{ all phase angles} \end{split}$ | 27.0          | 33.0                       | 36.0    | ub   |  |
|  | Gvar                                | Vcc = 3.5 V<br>Tcase = Trange<br>MODE = HIGH  | _             | -0.034                     | _       | dB/C |  |
| Power Added Efficiency                                   | PAEgsm                              | Vcc = 3.5 V<br>TCASE = 25 °C<br>VRAMP = 1.6 V<br>PIN = 3 dBm  | 49            | 53                         |         | %    |  |
| over radical Emoleony                                    | PAEEDGE                             | Vcc = 3.5 V<br>TCASE = 25 °C<br>POUT = 28.5 dBm   | 20            | 23                         |         |      |  |
|  | IGSM_LOW_POWER                      | Vcc = 3.5 V $Tcase = 25 °C$ $Pout = 6.5 dBm$  | _             | 110                        | 130     |      |  |
| Low Power Current Consumption                            | ledge_low_power                     | Vcc = 3.5 V<br>Vbias = 0.3 V<br>Tcase = 25 °C<br>Pout = 6.5 dBm   | _             | 120                        | _       | mA   |  |

# Table 4. SKY77340 Electrical Specifications (7 of 17)

|   |                          | Mode: Transmit                        | GMSK and EDGE <sup>1</sup> Band: EGSM [con   | tinued}        |             |         |       |
|---|--------------------------|---------------------------------------|--|----------------|-------------|---------|-------|
|   |                          |                                       | General Test Conditions  |                |             |         |       |
| Frequency = 880-915 MHz<br>Pulse Rate = 12.5% Duty Cy | cle                      | TFRAME = 4.615 r<br>Control States: E |  | RF Ports = GSI | M_IN, GSM_O | JT      |       |
| Parame  | eter                     | Symbol                                | Condition  | Minimum        | Typical     | Maximum | Unit  |
|   | 20 MHz Offset            | NxSat                                 | Vcc = 3.5 V<br>Tcase = 25 °C<br>Pout ≤ Pgsm<br>RBW = 100 kHz   | _              | -86.0       | -83.5   |       |
|   | 20 WHZ 011361            | NxLin                                 | VCC = $3.5$ V<br>TCASE = $25$ °C<br>POUT $\leq$ PEDGE<br>RBW = $100$ kHz   | _              | -84.0       | -83.5   |       |
|   | 10 MHz Offset            | NxSat_10 MHz                          | VCC = $3.5$ V<br>TCASE = $25$ °C<br>POUT $\leq$ PGSM<br>RBW = $100$ kHz  | _              | -86.0       | -76.0   | dBm   |
|   | TO MIN2 Offset           | NxLin _10 MHz                         | $Vcc = 3.5 V$ $Tcase = 25 °C$ $Pout \le Pedge$ $RBW = 100 kHz$   | _              | -84.0       | -78.0   | иын   |
|   | 1805 to 1880 MHz         | NxSat _DCS                            | VCC = $3.5$ V<br>TCASE = $25$ °C<br>POUT $\leq$ PGSM<br>RBW = $100$ kHz  | _              | -100.0      | -84.0   |       |
|   | 1003 to 1000 WILL        | NxLin _DCS                            | Vcc = 3.5 V<br>Tcase = 25 °C<br>Pout ≤ Pedge<br>RBW = 100 kHz  | _              | -100.0      | -84.0   |       |
| Forward Isolation                                     |                          | Iso                                   | $\label{eq:CC} \begin{split} &\text{VCC} = \text{Vrange} \\ &\text{TCase} = \text{Trange} \\ &\text{Pin} \leq 6 \text{ dBm} \\ &\text{EN} = 0 \end{split}$ | _              | -40         | -30     | dBm   |
| Crossover Isolation                                   | Fundamental              | Iso_EGSM-DCS                          | VCC = VRANGE<br>TCASE = TRANGE   | _              | _           | 0       | dBm   |
| or second indication                                  | 2 <sup>nd</sup> Harmonic | LUCIVI DOO                            | VCC = VRANGE<br>TCASE = TRANGE   | _              | _           | -20     | ווופט |
| nput VSWR   |                          | VSWR_SAT                              | VCC = VRANGE<br>TCASE = TRANGE<br>6.5 dBm ≤ POUT ≤ PGSM  | _              | 1.7         | 2.25    | Ratio |
| mpar tottii   |                          | VSWR_LIN                              | VCC = VRANGE<br>TCASE = TRANGE<br>POUT ≤ PEDGE   | _              | 1.5         | 2.0     | natio |

Table 4. SKY77340 Electrical Specifications (8 of 17)

|  |         | Mode: Transm | it GMSK and EDGE <sup>1</sup> Band: EGSM [contin  | nued}   |                            |         |      |  |
|--|---------|--------------|---|---------|----------------------------|---------|------|--|
|  |         |              | General Test Conditions   |         |                            |         |      |  |
| Frequency = 880-915 MHz<br>Pulse Rate = 12.5% Duty Cycle |         |              | TFRAME = 4.615 mS<br>Control States: EN = 1, BS = 0   |         | RF Ports = GSM_IN, GSM_OUT |         |      |  |
| Parameter  |         | Symbol       | Condition   | Minimum | Typical                    | Maximum | Unit |  |
| Harmonica  |         | 2fo          | Measured at GSM_OUT POUT ≤ PGSM VCC = VRANGE TCASE = TRANGE Load = 50 ohms  | _       | -20                        | -10     | dDm  |  |
| Harmonics  |         | 3fo to 15fo  | Measured at GSM_OUT POUT ≤ PGSM VCC = VRANGE TCASE = TRANGE Load = 50 ohms  | _       | -25                        | -17     | dBm  |  |
| Adjacent Channel Leakage                                 | ACPR1   |              | $RBW = 30 \text{ kHz}$ $Pout \leq Pedge$ $MODE = HIGH$ $Vcc = 3.5 \text{ V}$ $Tcase = 25 \text{ °C}$ $Offset = \pm 200 \text{ kHz}$ $Load = 50 \text{ Ohms}$  | _       | -36                        | -33     |      |  |
|  | AUFNI   |              | RBW = 30 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Offset = ±200 kHz Load VSWR ≤ 3:1, all phase angles  | _       | -35                        | _       | dBc  |  |
|  | ACPR2   |              | $RBW = 30 \text{ kHz}$ $Pout \leq Pedge$ $MODE = HIGH$ $Vcc = 3.5 \text{ V}$ $Tcase = 25 \text{ °C}$ $Offset = \pm 400 \text{ kHz}$ $Load = 50 \text{ Ohms}$  | _       | -60                        | -58     | u Do |  |
|  | ,,,,,,, |              | $\label{eq:bounds} \begin{array}{l} RBW = 30 \text{ kHz} \\ Pout \leq Pedge\_ex \\ MODE = HIGH \\ Vcc = Vrange \\ Tcase = Trange \\ Offset = \pm 400 \text{ kHz} \\ Load VSWR \leq 3:1, \text{ all phase angles} \end{array}$ | _       | <b>–</b> 57                | _       |      |  |

## Table 4. SKY77340 Electrical Specifications (9 of 17)

|   |              | Mode: Transmit  | GMSK and EDGE <sup>1</sup> Band: EGSM [conti   | nued}         |                |         |      |
|---|--------------|---|--|---------------|----------------|---------|------|
|   |              |   | General Test Conditions  |               |                |         |      |
| Frequency = 880-915 MHz<br>Pulse Rate = 12.5% Duty Cycle                                |              | TFRAME = 4.615 r<br>Control States: E   |  | RF Ports = GS | M_IN, GSM_0    | UT      |      |
| Parameter   |              | Symbol  | Condition  | Minimum       | Typical        | Maximum | Unit |
|   | 40000        |   | RBW = 30 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm$ 600 kHz<br>Load = 50 Ohms   | _             | -77            | -63     |      |
| Adjacent Channel Leakage [continued]  | ACPR3        |   | $RBW = 30 \text{ kHz}$ $POUT \leq PEDGE_EX$ $MODE = HIGH$ $VCC = VRANGE$ $TCASE = TRANGE$ $Offset = \pm 600 \text{ kHz}$ $Load VSWR \leq 3:1, \text{ all phase angles}$  | _             | -65            | _       |      |
|   | ACPR4        |   | RBW = 100 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm 1.8$ MHz<br>Load = 50 0hms  | _             | -81            | -66     |      |
|   | АСРН4        | $\label{eq:resolvent} \begin{split} RBW &= 100 \text{ kHz} \\ Pout &\leq Pedge\_ex \\ MODE &= HIGH \\ Vcc &= Vrange \\ Tcase &= Trange \\ Offset &= \pm 1.8 \text{ MHz} \\ Load VSWR &\leq 3:1, \text{ all phase angles} \end{split}$ | _  | <b>-75</b>    | _              |         |      |
| Eway Vestas Magailiude  |              | EVM1  | RBW = 30 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Load = 50 $\Omega$   | _             | 1.5            | 5.0     | 0/   |
| Error Vector Magnitude  |              | EVM2  | $\label{eq:RBW} \begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge\_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Load VSWR} \leq 3:1 \text{ , all phase angles} \\ \end{array}$ | _             | 3.0            | 9.0     | %    |
| Stability (all spurious)  |              |   | 6.5 dBm ≤ Pout ≤ PGSM<br>Vcc = VRANGE<br>TCASE = TRANGE<br>Load VSWR = 8:1, all phase angles   | _             | _              | -36     | dBm  |
| Ruggedness  |              |   | Pout = PGSM<br>Pin = 6 dBm<br>Vcc = 4.8 V  |               | No degradation |         |      |
|   |              |   | TCASE = TRANGE<br>Load VSWR = 10:1, all phase angles   |               | No damage      |         |      |
| Mode Switching Time<br>(Time does not include loop lock time [p<br>GMSK PAC operation.) | edestal] for | TMODE   | Vcc = Vrange<br>Tcase = Trange   |               | 2              | 4       | μS   |

 $<sup>^{\</sup>rm 1}$  All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ ACPR).

Table 4. SKY77340 Electrical Specifications (10 of 17)

|  |                                     | <b>340 Electrical Specifications (10</b> <i>ansmit GMSK and EDGE</i> <sup>1</sup> <i>Band: DCS</i>  | UI 1 <i>1)</i>                     |         |         |      |
|--|-------------------------------------|---|------------------------------------|---------|---------|------|
|  |                                     | General Test Conditions   |                                    |         |         |      |
| Frequency = 1710-1785 MHz<br>Pulse Rate = 12.5% Duty Cycle | TFRAME = 4.615<br>Control States: E |   | RF Ports = DCS/PCS_IN, DCS/PCS_OUT |         |         |      |
| Parameter  | Symbol                              | Condition   | Minimum                            | Typical | Maximum | Unit |
|  | PGSM                                | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 3.5 \text{ V} \\ &\text{Tcase} = 25 \text{ °C} \\ &\text{0 dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{aligned}$   | 32.5                               | 33.5    | _       |      |
|  | PGSM_EX-L                           | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 3.0 \text{ V} \\ &\text{Tcase} = \text{Trange} \\ &\text{0 dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{aligned}$   | 29.0                               | 32.0    | _       |      |
| Output Power   | PGSM_EX-H                           | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 4.8 \text{ V} \\ &\text{Tcase} = \text{Trange} \\ &\text{0 dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{aligned}$   | 29.0                               | 34.2    | _       | dBm  |
|  | Pedge                               | Vcc = 3.5 V<br>TCASE = 25 °C<br>Pout = PIN + GEDGE  | 27.3                               | _       | _       |      |
|  | PEDGE_EX                            | Vcc = Vrange<br>Tcase = Trange  | 25.0                               | _       | _       |      |
|  | Gedge                               | Vcc = 3.5 V<br>Vbias = 1.3 V<br>Pout = Pedge<br>Tcase = 25 °C<br>MODE = HIGH<br>Load = 50 ohms  | 33.0                               | 34.8    | 36.0    | dB   |
| Linear Gain  | GEUGE                               | $\label{eq:VCC} \begin{array}{l} \text{VCC} = 3.0 \text{ V} < \text{VBATT} < 4.8 \text{ V} \\ \text{VBIAS} = 1.3 \text{ V} \\ \text{POUT} = \text{PEDGE} \\ \text{TCASE} = \text{TRANGE} \\ \text{MODE} = \text{HIGH} \\ \text{Load VSWR} \leq 3.1 \text{ all phase angles} \\ \end{array}$ | 28.0                               | 34.8    | 38.0    | иБ   |
|  | Gvar                                | Vcc = 3.5 V<br>Tcase = Trange<br>MODE = HIGH  | _                                  | -0.025  | _       | dB/C |
| Power Added Efficiency                                     | PAEgsm                              | Vcc = 3.5 V<br>TCASE = 25 °C<br>VRAMP = 1.6 V<br>PIN = 3 dBm  | 48                                 | 52      |         | %    |
|  | PAEEDGE                             | Vcc = 3.5 V<br>TCASE = 25 °C<br>POUT = 27.3 dBm   | 20                                 | 25      |         |      |
| Low Power Current Consumption                              | IGSM_LOW_POWER                      | Vcc = 3.5 V<br>TCASE = 25 °C<br>POUT = 1.5 dBm  | _                                  | 80      | 100     |      |
|  | ledge_low_power                     | Vcc = 3.5 V<br>VBIAS = 0.4 V<br>TCASE = 25 °C<br>POUT = 1.5 dBm   | _                                  | 110     | _       | mA   |

Table 4. SKY77340 Electrical Specifications (11 of 17)

|   |                | Mode: Transmi                         | t GMSK and EDGE <sup>1</sup> Band: DCS [cont  | tinued}       |               |           |       |
|---|----------------|---------------------------------------|---|---------------|---------------|-----------|-------|
|   |                |                                       | General Test Conditions   |               |               |           |       |
| Frequency = 1710-1785 MHz<br>Pulse Rate = 12.5% Duty Cycl |                | TFRAME = 4.615 r<br>Control States: E |   | RF Ports = DC | S/PCS_IN, DCS | S/PCS_OUT |       |
| Paramet   | ter            | Symbol                                | Condition   | Minimum       | Typical       | Maximum   | Unit  |
|   | 20 MHz Offset  | NxSat                                 | Vcc = $3.5 \text{ V}$<br>TCASE = $25 ^{\circ}\text{C}$<br>POUT $\leq$ PGSM<br>RBW = $100 \text{kHz}$        | _             | -82           | -80       |       |
| Noise Power  925 to 960                                   | 20 WII 2 OHSEL | NxLin                                 | $Vcc = 3.5 \text{ V}$ $Tcase = 25 \text{ °C}$ $Pout \le Pedge$ $RBW = 100 \text{ kHz}$                      |               | -82           | -80       | dBm   |
|   | 925 to 960 MHz | NxSat_EGSM                            | VCC = $3.5 \text{ V}$<br>TCASE = $25 ^{\circ}\text{C}$<br>POUT $\leq$ PGSM<br>RBW = $100 \text{kHz}$        | _             | -88           | -84       | иын   |
|   | 923 to 900 WHZ | NxLin_EGSM                            | Vcc = $3.5 \text{ V}$<br>TCASE = $25 \text{ °C}$<br>POUT $\leq$ PEDGE<br>RBW = $100 \text{ kHz}$            | _             | -86           | -84       |       |
| Forward Isolation   |                | Iso                                   | $\label{eq:CC} \begin{split} &VCC = VRANGE \\ &TCASE = TRANGE \\ &Pin \leq 6 \; dBm \\ &EN = 0 \end{split}$ |               | -33           | -30       | dBm   |
| Crossover Isolation                                       | Fundamental    | Iso_DCS-EGSM                          | Vcc = Vrange<br>Tcase = Trange  | _             | -17           | -10       | dBm   |
| Input VSWR  |                | VSWR_SAT                              | $\label{eq:Vcc}                                   $   | _             | 1.5           | 2.25      | Ratio |
| iliput vown   |                | VSWR_LIN                              | VCC = VRANGE<br>TCASE = TRANGE<br>POUT ≤ PEDGE  | _             | 1.2           | 2.0       | nauo  |
| Harmonics   |                | 2fo to 4fo                            | Measured at GSM_OUT POUT ≤ PGSM VCC = VRANGE TCASE = TRANGE Load = 50 ohms                                  | _             | -25           | -10       | dBm   |
|   |                | 5fo to 7fo                            | Measured at GSM_OUT POUT ≤ PGSMVCC = VRANGE TCASE = TRANGE Load = 50 ohms                                   | _             | _             | -17       |       |

Table 4. SKY77340 Electrical Specifications (12 of 17)

|  |                                       | t GMSK and EDGE <sup>1</sup> Band: DCS [contin   |                                    |             |         |      |  |
|--|---------------------------------------|--|------------------------------------|-------------|---------|------|--|
|  |                                       | General Test Conditions  |                                    |             |         |      |  |
| Frequency = 1710-1785 MHz<br>Pulse Rate = 12.5% Duty Cycle | TFRAME = 4.615 r<br>Control States: E |  | RF Ports = DCS/PCS_IN, DCS/PCS_OUT |             |         |      |  |
| Parameter  | Symbol                                | Condition  | Minimum                            | Typical     | Maximum | Unit |  |
|  |                                       | RBW = 30 kHz<br>POUT $\leq$ PEDGE<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm$ 200 kHz<br>Load = 50 Ohms  | _                                  | -36         | -33     |      |  |
| ACPR1  |                                       | $\label{eq:RBW} \begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{POUT} \leq \text{PEDGE\_EX} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{VRANGE} \\ \text{TCASE} = \text{TRANGE} \\ \text{Offset} = \pm 200 \text{ kHz} \\ \text{Load VSWR} \leq 3:1, \text{ all phase angles} \\ \end{array}$ | _                                  | -30         | _       |      |  |
| 40000  |                                       | RBW = 30 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm 400$ kHz<br>Load = 50 0hms   | _                                  | -60         | -57     |      |  |
| ACPR2  |                                       | RBW = 30 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Offset = ±400 kHz Load VSWR ≤ 3:1, all phase angles   | _                                  | -55         | _       |      |  |
| Adjacent Channel Leakage                                   |                                       | RBW = 30 kHz POUT ≤ PEDGE MODE = HIGH Vcc = 3.5 V TCASE = 25 °C Offset = ±600 kHz Load = 50 Ohms   | _                                  | <b>-</b> 75 | -63     | dBc  |  |
| ACPR3  |                                       | RBW = 30 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Offset = ±600 kHz Load VSWR ≤ 3:1, all phase angles   | _                                  | -65         | _       |      |  |
| 4000   |                                       | RBW = 100 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm$ 1.8 MHz<br>Load = 50 Ohms  | _                                  | -77         | -66     |      |  |
| ACPR4  |                                       | $RBW = 100 \text{ kHz}$ $POUT \leq PEDGE_EX$ $MODE = HIGH$ $VCC = VRANGE$ $TCASE = TRANGE$ $Offset = \pm 1.8 \text{ MHz}$ $Load VSWR \leq 3:1, \text{ all phase angles}$   | _                                  | -77         | _       |      |  |

## Table 4. SKY77340 Electrical Specifications (13 of 17)

|  | Mode: Transm                      | nit GMSK and EDGE <sup>1</sup> Band: DCS [continu   | ed}                                 |         |         |      |
|--|-----------------------------------|---|-------------------------------------|---------|---------|------|
|  |                                   | General Test Conditions   |                                     |         |         |      |
| Frequency = 1710-1785 MHz<br>Pulse Rate = 12.5% Duty Cycle                                   | TFRAME = 4.615<br>Control States: |   | RF Ports = DCS/PCS_IN, DCS/PCS _OUT |         |         |      |
| Parameter  | Symbol                            | Condition   | Minimum                             | Typical | Maximum | Unit |
| Error Vector Magnitude   | EVM1                              | $\label{eq:bound} \begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \text{ °C} \\ \text{Load} = 50 \ \Omega \\ \end{array}$ | _                                   | 1.9     | 5.0     | %    |
|  | EVM2                              | RBW = 30 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Load VSWR ≤ 2.5:1 , all phase angles   | _                                   | 6.0     | 9.0     |      |
| Stability (all spurious)   |                                   | 1.5 dBm ≤ Pout ≤ Pgsm<br>Vcc = Vrange<br>Tcase = Trange<br>Load VSWR = 8:1, all phase angles  | _                                   | _       | -36     | dBm  |
| Ruggedness   |                                   | POUT = PGSM PIN = 6 dBm Vcc = 4.8 V TCASE = TRANGE Load VSWR = 10:1, all phase angles   | No degradation  No damage           |         |         |      |
| Mode Switching Time<br>(Time does not include loop lock time [pedestal] GMSK PAC operation.) | for TMODE                         | Vcc = Vrange<br>Tcase = Trange  |                                     | 2       | 4       | μS   |

<sup>&</sup>lt;sup>1</sup> All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ACPR).

Table 4. SKY77340 Electric3al Specifications (14 of 17)

|  | Mode: Tra       | ansmit GMSK and EDGE <sup>1</sup> Band: PCS   |         |                                    |         |      |  |
|--|-----------------|---|---------|------------------------------------|---------|------|--|
|  |                 | General Test Conditions   |         |                                    |         |      |  |
| Frequency = 1850-1910 MHz<br>Pulse Rate = 12.5% Duty Cycle |                 | TFRAME = 4.615 mS<br>Control States: EN = 1, BS = 1   |         | RF Ports = DCS/PCS_IN, DCS/PCS_OUT |         |      |  |
| Parameter  | Symbol          | Condition   | Minimum | Typical                            | Maximum | Unit |  |
|  | Pgsm            | $\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.5 \mbox{ V} \\ \mbox{Tcase} = 25 \mbox{ °C} \\ \mbox{0 dBm} \le \mbox{Pin} \le 6 \mbox{ dBm} \end{array}$   | 32.5    | 33.2                               | _       |      |  |
| Output Power   | PGSM_EX-L       | $\label{eq:Vcc} \begin{split} &\text{Vcc} = 3.0 \text{ V} \\ &\text{Tcase} = \text{Trange} \\ &\text{0 dBm} \leq \text{Pin} \leq 6 \text{ dBm} \end{split}$   | 29.0    | 32.0                               | _       |      |  |
|  | PGSM_EX-H       | Vcc = 4.8 V<br>Tcase = Trange<br>0 dBm≤ Pin ≤ 6 dBm   | 29.0    | 34.0                               | _       | dBm  |  |
|  | Pedge           | Vcc = 3.5 V<br>Tcase = 25 °C<br>Pout = PIN + Gedge  | 27.3    | _                                  | _       |      |  |
|  | Pedge_ex        | VCC = VRANGE<br>TCASE = TRANGE  | 25.0    | _                                  | _       |      |  |
|  | Gedge           | Vcc = 3.5 V<br>Vbias = 1.3 V<br>Pout = Pedge<br>Tcase = 25 °C<br>MODE = HIGH<br>Load = 50 ohms  | 33.0    | 35.0                               | 36.0    | dB   |  |
| Linear Gain  | <b>GLDGL</b>    | $\label{eq:VCC} \begin{split} &\text{VCC} = 3.0 \text{ V} < \text{VBATT} < 4.8 \text{ V} \\ &\text{VBIAS} = 1.3 \text{ V} \\ &\text{POUT} = \text{PEDGE} \\ &\text{TCASE} = \text{TRANGE} \\ &\text{MODE} = \text{HIGH} \\ &\text{Load VSWR} \leq 3:1 \text{ all phase angles} \end{split}$ | 28.0    | 35.0                               | 38.0    | uБ   |  |
|  | Gvar            | Vcc = 3.5 V<br>Tcase = Trange<br>MODE = HIGH  | _       | -0.025                             | _       | dB/C |  |
| Power Added Efficiency                                     | PAEGSM          | VCC = 3.5 V<br>TCASE = 25 °C<br>VRAMP = 1.6 V<br>PIN = 3 dBm  | 48      | 52                                 | _       | %    |  |
|  | PAEEDGE         | Vcc = 3.5 V $Tcase = 25 °C$ $Pout = 27.3 dBm$   | 20      | 25                                 |         |      |  |
| Low Power Current Consumption                              | IGSM_LOW_POWER  | Vcc = 3.5 V<br>TCASE = 25 °C<br>POUT = 1.5 dBm  | _       | 80                                 | 100     |      |  |
|  | ledge_low_power | Vcc = 3.5 V<br>VBIAS = 0.4 V<br>TCASE = 25 °C<br>POUT = 1.5 dBm   | _       | 110                                | _       | mA   |  |

Table 4. SKY77340 Electrical Specifications (15 of 17)

|  |                | Mode: Transmit                        | t GMSK and EDGE <sup>1</sup> Band: PCS [continu  | ed}           |                                    |         |       |  |
|--|----------------|---------------------------------------|--|---------------|------------------------------------|---------|-------|--|
|  |                |                                       | General Test Conditions  |               |                                    |         |       |  |
| Frequency = 1850-1910 MHz<br>Pulse Rate = 12.5% Duty Cycle | )              | TFRAME = 4.615 r<br>Control States: E |  | RF Ports = DC | RF Ports = DCS/PCS_IN, DCS/PCS_OUT |         |       |  |
| Paramete   | er             | Symbol                                | Condition  | Minimum       | Typical                            | Maximum | Unit  |  |
|  | 20 MHz Offcot  | NxSat                                 | VCC = $3.5$ V<br>TCASE = $25$ °C<br>POUT $\leq$ PGSM<br>RBW = $100$ kHz  | _             | -82                                | -80     |       |  |
| 20 MHz Offs  Noise Power  869 to 894 M                     | ZO WITZ OTISEL | NxLin                                 | $\begin{tabular}{ll} VCC &= 3.5 \ V \\ TCASE &= 25 \ ^{\circ}C \\ POUT &\leq PEDGE \\ RBW &= 100 \ \text{kHz} \end{tabular}$                               | _             | -82                                | -80     | dBm   |  |
|  | 860 to 804 MHz | NxSat_CEL                             | $\begin{tabular}{ll} VCC &= 3.5 \ V \\ TCASE &= 25 \ ^{\circ}C \\ POUT &\leq PGSM \\ RBW &= 100 \ \text{kHz} \end{tabular}$                                | _             | -88                                | -84     | иып   |  |
|  | 003 to 034 WHZ | NxLin_CEL                             | $Vcc = 3.5 V$ $Tcase = 25 °C$ $Pout \le Pedge$ $RBW = 100 kHz$   | _             | -86                                | -84     |       |  |
| Forward Isolation  |                | Iso                                   | $\label{eq:CC} \begin{split} &\text{VCC} = \text{VRANGE} \\ &\text{TCASE} = \text{TRANGE} \\ &\text{PIN} \leq 6 \text{ dBm} \\ &\text{EN} = 0 \end{split}$ | _             | -33                                | -30     | dBm   |  |
| Crossover Isolation  | Fundamental    | Iso_PCS-EGSM                          | VCC = VRANGE<br>TCASE = TRANGE   | _             | _                                  | -10     | dBm   |  |
| Input VSWR   |                | VSWR_SAT                              | VCC = VRANGE<br>TCASE = TRANGE<br>1.5 dBm ≤ POUT ≤ PGSM  | _             | 2.1                                | 2.25    | Ratio |  |
| iliput vown  |                | VSWR_Lin                              | VCC = VRANGE<br>TCASE = TRANGE<br>POUT ≤ PEDGE   | _             | 1.5                                | 2.0     | natio |  |
| Harmonics  |                | 2fo to 3fo                            | Measured at DCS/PCS_OUT POUT_RANGE VCC = VRANGE TCASE = TRANGE Load = 50 ohms  | _             | -10                                | -7      |       |  |
|  |                | 4fo                                   | Measured at DCS/PCS_OUT POUT_RANGE VCC = VRANGE TCASE = TRANGE Load = 50 ohms  | _             | -11                                | -5      | dBm   |  |
|  |                | 5fo to 7fo                            | Measured at DCS/PCS _OUT POUT_RANGE Vcc = VRANGE TCASE = TRANGE Load = 50 ohms   | _             | -30                                | -17     |       |  |

Table 4. SKY77340 Electrical Specifications (16 of 17)

|  |        |  | 340 Electrical Specifications (16 it GMSK and EDGE <sup>1</sup> Band: PCS [continued]  |         |              |             |      |  |
|--|--------|--|--|---------|--------------|-------------|------|--|
|  |        |  | General Test Conditions  |         |              |             |      |  |
| Frequency = 1850-1910 MHz<br>Pulse Rate = 12.5% Duty Cycle |        | TFRAME = 4.615 r<br>Control States: E  |  |         | S/PCS_IN, DC | DCS/PCS_OUT |      |  |
| Parameter  |        | Symbol   | Condition  | Minimum | Typical      | Maximum     | Unit |  |
|  | ACPR1  |  | RBW = 30 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm$ 200 kHz<br>Load = 50 0hms   | _       | -36          | -33         |      |  |
|  | Admi   |  | RBW = 30 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Offset = ±200 kHz Load VSWR ≤ 3:1, all phase angles       | _       | -30          | _           |      |  |
|  | ACDD2  |  | RBW = 30 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm$ 400 kHz<br>Load = 50 0hms   | _       | -60          | -57         |      |  |
| ACPR2  |        | RBW = 30 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Offset = ±400 kHz Load VSWR ≤ 3:1, all phase angles | _  | -55     | _            | dD.         |      |  |
| Adjacent Channel Leakage                                   | 40000  |  | RBW = 30 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 °C<br>Offset = $\pm$ 600 kHz<br>Load = 50 0hms   | _       | <b>-75</b>   | -63         | dBc  |  |
| ACPR3  | асрк3  |  | RBW = 30 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Offset = ±600 kHz Load VSWR ≤ 3:1, all phase angles       | _       | -65          | _           |      |  |
|  | AODD 4 |  | RBW = 100 kHz<br>POUT $\leq$ PEDGE<br>MODE = HIGH<br>VCC = 3.5 V<br>TCASE = 25 ° C<br>Offset = $\pm$ 1.8 MHz<br>Load = 50 0hms | _       | -77          | -66         |      |  |
|  | ACPR4  |  | RBW = 100 kHz POUT ≤ PEDGE_EX MODE = HIGH VCC = VRANGE TCASE = TRANGE Offset = ±1.8 MHz Load VSWR ≤ 3:1, all phase angles      | _       | -77          | _           |      |  |

## Table 4. SKY77340 Electrical Specifications (17 of 17)

|  | Mode: Transn                      | nit GMSK and EDGE <sup>1</sup> Band: PCS [continu  | ied}          |                           |           |      |
|--|-----------------------------------|--|---------------|---------------------------|-----------|------|
|  |                                   | General Test Conditions  |               |                           |           |      |
| Frequency = 1850-1910 MHz<br>Pulse Rate = 12.5% Duty Cycle   | TFRAME = 4.615<br>Control States: |  | RF Ports = DC | S/PCS_IN, DCS             | S/PCS_OUT |      |
| Parameter  | Symbol                            | Condition  | Minimum       | Typical                   | Maximum   | Unit |
| Error Vector Magnitude   | EVM1                              | $\label{eq:BW} \begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = 3.5 \text{ V} \\ \text{Tcase} = 25 \text{ C} \\ \text{Load} = 50 \Omega \end{array}$   | _             | 1.9                       | 5.0       | %    |
|  | EVM2                              | $\label{eq:RBW} \begin{array}{l} \text{RBW} = 30 \text{ kHz} \\ \text{Pout} \leq \text{Pedge\_ex} \\ \text{MODE} = \text{HIGH} \\ \text{Vcc} = \text{Vrange} \\ \text{Tcase} = \text{Trange} \\ \text{Load VSWR} \leq 2.5:1 \text{ , all phase angles} \\ \end{array}$ | _             | 6.0                       | 9.0       | 74   |
| Stability (all spurious)   |                                   | 1.5 dBm ≤ Pout ≤ Pgsm MODE = HIGH Vcc = VRANGE TCASE = TRANGE Load VSWR = 8:1, all phase angles  | _             | _                         | -36       | dBm  |
| Ruggedness   |                                   | POUT = PGSM PIN = 6 dBm VCC = 4.8 V TCASE = TRANGE Load VSWR = 10:1, all phase angles  |               | No degradation  No damage |           |      |
| Mode Switching Time<br>(Time does not include loop lock time [pedestal] fo<br>GMSK PAC operation.) | T TMODE                           | VCC = VRANGE<br>TCASE = TRANGE   |               | 2                         | 4         | μS   |

<sup>&</sup>lt;sup>1</sup> All specifications related to modulated waveforms are for the EDGE waveform (i.e., EVM/ACPR).

Table 5. SKY77340 Functional Specifications – GMSK PA Control Operation (1 of 4)

|                                       | Mode: Closed Loo | p PA Control (GMSK Mode) Band: CEL   | & EGSM  |  |         |      |  |  |  |  |
|---------------------------------------|------------------|--|---------|--|---------|------|--|--|--|--|
| General Test Conditions               |                  |  |         |  |         |      |  |  |  |  |
| Frequency = 824-849 MHz & 880-915 MHz | Pulse Rate = 12  | Pulse Rate = 12.5% Duty Cycle, TFRAME = 4.615 mS   |         | Control States:<br>EN = 1,MODE = 0, BS = 0,<br>VBIAS = don't care VRAMP = active |         |      |  |  |  |  |
| Parameter                             | Symbol           | Condition  | Minimum | Typical  | Maximum | Unit |  |  |  |  |
| VRAMP Input Voltage                   | Vramp            | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = 3.5 \text{ V} \\ &\text{Tcase} = \text{Trange} \\ &6.5 \text{ dBm} \leq \text{Pout} \leq 34.5 \text{ dBm} \end{aligned}$ | 0.2     |  | 1.6     | V    |  |  |  |  |
| Dynamic Range                         |                  | VCC = VRANGE<br>TCASE = TRANGE   | 37.0    | 40.0   | _       | dB   |  |  |  |  |
| Tolerance                             |                  | Vcc = Vrange<br>Tcase = Trange<br>Pout = 6.5 dBm   | -4.0    | _  | 3.0     | dB   |  |  |  |  |
| Tolerance                             |                  | VCC = VRANGE<br>TCASE = TRANGE<br>POUT = 34.5 dBm  | -3.0    | _  | 2.0     | uБ   |  |  |  |  |
| Power Control Slope                   | Pcs              | $\label{eq:Vcc} \begin{aligned} &\text{Vcc} = \text{Vrange} \\ &\text{Tcase} = \text{Trange} \\ &6.5 \text{ dBm} \leq \text{Pout} \leq 34.5 \text{ dBm} \end{aligned}$ | _       | _  | 250.0   | dB/V |  |  |  |  |
| PAC Loop Enable Time                  | TEN              | VCC = VRANGE<br>TCASE = TRANGE<br>After EN transitions LOW ≥ HIGH  | _       | 1  | 2       | μS   |  |  |  |  |
| Minimum Pedestal Duration             | TPED             |  | _       | 2  | _       | μS   |  |  |  |  |

#### Note:

Response is monotonic over frequency, temperature, and POUT

Table 5. SKY77340 Functional Specifications – EDGE PA Control Operation (2 of 4)

| Mode: Open Loop, Fixed PA Gain (EDGE Mode) Band: CEL & EGSM |                         |  |  |         |         |      |  |  |  |  |
|---|-------------------------|--|--|---------|---------|------|--|--|--|--|
|   | General Test Conditions |  |  |         |         |      |  |  |  |  |
| Frequency = 824-849 MHz & 880-915 MHz                       | Pulse Rate = 12.        | 5% Duty Cycle, TFRAME = 4.615 mS   | Control States:<br>EN = 1, MODE = 1, BS = 0,<br>VRAMP = don't care, VBIAS = active |         |         |      |  |  |  |  |
| Parameter   | Symbol                  | Condition  | Minimum  | Typical | Maximum | Unit |  |  |  |  |
| VBIAS Input Voltage   | VBIAS                   | $\begin{tabular}{ll} VCC &= 3.5 \ V \\ TCASE &= TRANGE \\ POUT &= 28.5 \ dBm \\ EVM1 &\le 5\% \\ ACPR2 &\le 58 \ dBc \\ Gain &= GEDGE \end{tabular}$ | _  | 1.4     | 1.6     | V    |  |  |  |  |

Table 5. SKY77340 Functional Specifications – GMSK PA Control Operation (3 of 4)

|   | Mode: Closed Loop PA Control (GMSK Mode) Band: DCS & PCS |  |         |   |         |      |  |  |  |  |
|---|--|--|---------|---|---------|------|--|--|--|--|
|   |  | General Test Conditions  |         |   |         |      |  |  |  |  |
| Frequency = 1710-1785 MHz & 1850-1910 MHz | Pulse Rate = 25  | Ise Rate = 25% Duty Cycle, TFRAME = 4.615 mS   |         | Control States:<br>EN = 1,MODE = 0, BS = 1,<br>VBIAS = don't care, VRAMP = active |         |      |  |  |  |  |
| Parameter                                 | Symbol   | Condition  | Minimum | Typical   | Maximum | Unit |  |  |  |  |
| VRAMP Input Voltage                       | VRAMP  | $\label{eq:VCC} \begin{aligned} &\text{VCC} = 3.5 \text{ V} \\ &\text{TCASE} = \text{TRANGE} \\ &1.5 \text{ dBm} \leq \text{Pout} \leq 32.5 \text{ dBm} \end{aligned}$ | 0.2     |   | 1.6     | V    |  |  |  |  |
| Dynamic Range                             |  | Vcc = Vrange<br>Tcase = Trange   | 32.0    | 35.0  | _       | dB   |  |  |  |  |
| Tolerance                                 |  | VCC = VRANGE<br>TCASE = TRANGE<br>POUT = 1.5 dBm   | -5.0    | _   | 4.0     | dB   |  |  |  |  |
| Toerance                                  |  | VCC = VRANGE<br>TCASE = TRANGE<br>POUT = 32.5 dBm  | -3.0    | _   | 2.0     | ub   |  |  |  |  |
| Power Control Slope                       | Pcs  |  | _       | _   | 250     | dB/V |  |  |  |  |
| PAC Loop Enable Time                      | TEN  | VCC = VRANGE TCASE = TRANGE After EN transitions LOW ≥ HIGH  | _       | 1   | 2       | μS   |  |  |  |  |
| Minimum Pedestal Duration                 | TPED   | $\label{eq:VCC} \begin{aligned} &\text{VCC} = \text{VRANGE} \\ &\text{TCASE} = \text{TRANGE} \\ &1.5 \text{ dBm} \leq \text{Pout} \leq 32.5 \text{ dBm} \end{aligned}$ | _       | 2.0   | _       | μS   |  |  |  |  |

#### Note:

Response is monotonic over frequency, temperature, and POUT

Table 5. SKY77340 Functional Specifications – EDGE PA Control Operation (4 of 4)

| Mode: Open Loop, Fixed PA Gain (EDGE Mode) Band: DCS & PCS |                  |   |  |         |         |      |  |  |  |
|--|------------------|---|--|---------|---------|------|--|--|--|
| General Test Conditions                                    |                  |   |  |         |         |      |  |  |  |
| Frequency = 1710-1785 MHz & 1850-1910 MHz                  | Pulse Rate = 25% | 6 Duty Cycle, Тғаме = 4.615 mS  | Control States:<br>EN = 1, MODE = 1, BS = 1,<br>VRAMP = don't care, VBIAS = active |         |         |      |  |  |  |
| Parameter  | Symbol           | Condition   | Minimum  | Typical | Maximum | Unit |  |  |  |
| VBIAS Input Voltage  | VBIAS            | $\begin{tabular}{lll} Vcc &= 3.5 \ V \\ Tcase &= Trange \\ POUT &= 27.3 \ dBm \\ EVM1 &\le 5\% \\ ACPR2 &\le 58 \ dBc \\ Gain &= Gedge \end{tabular}$ | _  | 1.3     | 1.6     | V    |  |  |  |

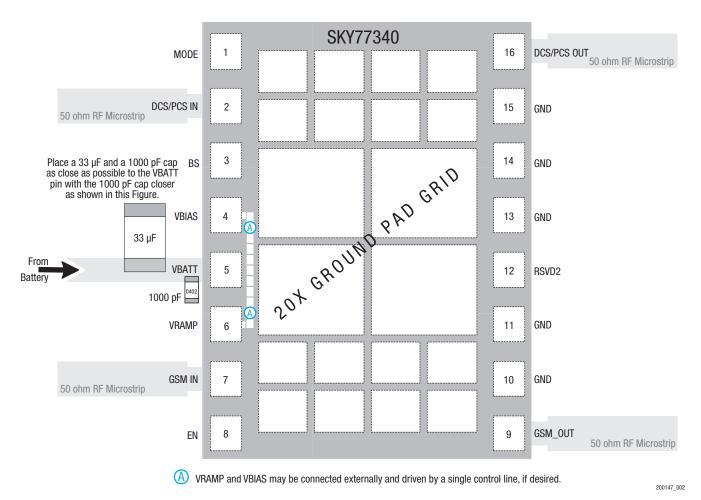
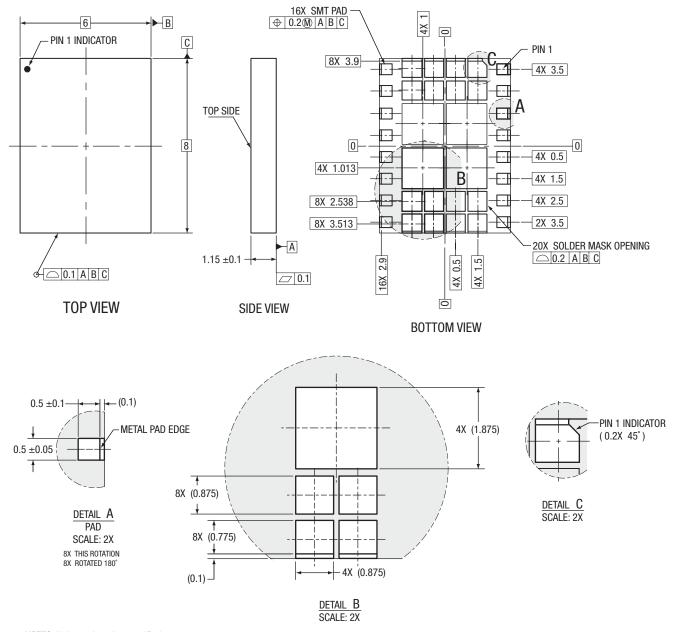


Figure 2. Typical SKY77340 Application Circuit

# **Package Dimensions and Pin Descriptions**

Figure 3 is a mechanical diagram of the pad layout for the SKY77340, a 16-pin leadless quad-band PA module. Figure 4 provides a recommended phone board layout footprint for the PAM to help the designer attain optimum thermal conductivity, good grounding, and minimum RF discontinuity for the 50-ohm terminals.

Figure 5 shows the device pin configuration and numbering convention, which starts with pin 1 at the upper left, as indicated, and increments counter-clockwise around the package. Table 6 lists the pin names and signal descriptions. Figure 6 interprets typical Case Markings.

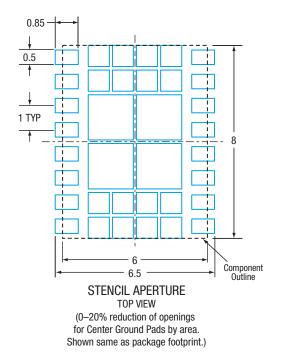


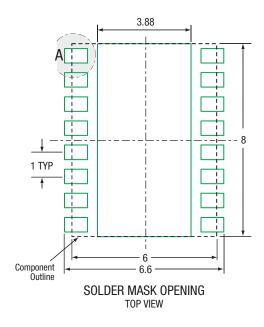
NOTES: Unless otherwise specified.

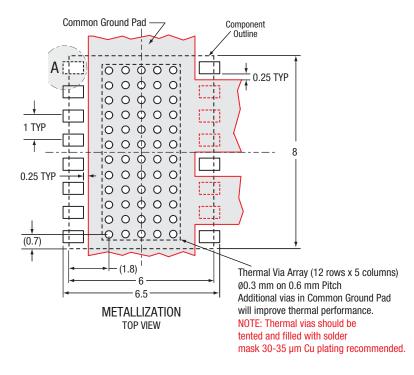
- 1. Dimensioning and Tolerancing in accordance with ASME Y14.5M-1994.
- 2. Pads are solder mask defined on 3 edges.

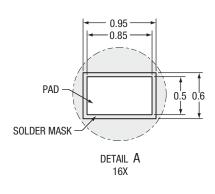
200147\_003

Figure 3. SKY77340 16-Pin MCM Package Dimensional Drawing









ALL DIMENSIONS IN MILLIMETERS

Figure 4. Phone Board Layout Footprint for 6 x 8 mm Package - SKY77340 Specific

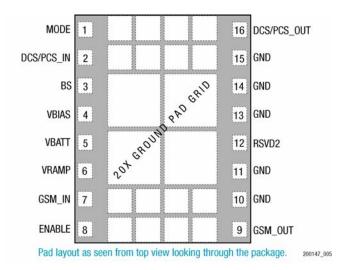
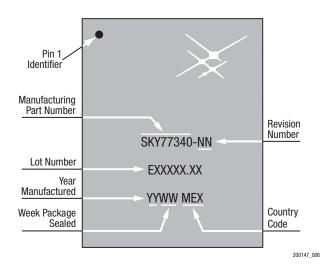


Figure 5. SKY77340 Pin Configuration (Top View)

**Table 6. SKY77340 Pin Names and Signal Descriptions** 

| Pad               | Name         | Description  |
|-------------------|--------------|--|
| 1                 | MODE         | GMSK/EDGE Power Control Mode:<br>Low = GMSK, High = EDGE |
| 2                 | DCS/PCS_IN   | RF Input (DCS / PCS Bands) DC Blocked                    |
| 3                 | BS           | Band Select  |
| 4                 | VBIAS        | Analog PA Bias Control (ALL BANDS, EDGE MODE)            |
| 5                 | VBATT        | DC Supply  |
| 6                 | VRAMP        | Analog Output Power Control<br>(ALL BANDS, GMSK MODE)    |
| 7                 | GSM_IN       | RF Input (CEL / EGSM Bands) DC Blocked                   |
| 8                 | EN           | Transmit Enable / Disable. Low = Disable                 |
| 9                 | GSM_OUT      | RF Output (CEL / EGSM Bands) DC Blocked                  |
| *12               | RSVD2        | Reserved   |
| *16               | DCS/PCS_OUT  | RF Output (DCS / PCS Bands) DC Blocked                   |
| *10, 11,<br>13–15 | GND          | Ground   |
| Pad               | GND PAD GRID | Ground pad grid is device underside.                     |



**Figure 6. Typical Case Markings** 

# **Package and Handling Information**

Because of its sensitivity to moisture absorption, this device package is baked and vacuum-packed prior to shipment in accordance with IPC J-STD 033 guidelines. Instructions on the shipping container label are in accordance with IPC J-STD 020B regarding exposure to moisture after the container seal is broken. These instructions must be followed; otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY77340 is capable of withstanding an MSL3/250 °C solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is attached in a reflow oven, the temperature ramp rate should not exceed 3 °C per second; maximum temperature should not exceed 250 °C. If the part is manually attached, precaution should be taken to insure that the part is not subjected to temperatures exceeding 250 °C for more than 10 seconds. For details on attachment techniques, precautions, and handling procedures recommended by Skyworks, please refer to Skyworks Application Note: *PCB Design and SMT Assembly/Rework*, Document Number 101752. Additional information on standard SMT reflow profiles can also be found in the *JEDEC Standard J-STD-020*.

Production quantities of this product are shipped in the standard tape-and-reel format. For packaging details, refer to Skyworks Application Note: *Tape and Reel Information – RF Modules*, Document Number 101568.

# **Electrostatic Discharge Sensitivity**

The SKY77340 is a Class 1 device. ESD testing was performed in compliance with JEDEC standards JESD22-A114 (Human Body Model), JESD22-A115 (Machine Model), and JESD22-C101 (Charged Device Model).

Various failure criteria can be utilized when performing ESD testing. Many vendors employ relaxed ESD failure standards, which fail devices only after "the pin fails the electrical specification limits" or "the pin becomes completely nonfunctional". Skyworks' most stringent criteria fail devices as soon as the pin begins to show any degradation on a curve tracer. To avoid ESD damage, both latent and visible, it is very important that the product assembly and test areas follow the Class-1 ESD handling precautions listed in Table 7.

Table 7. Precautions for Handling GaAs IC-based Products to
Avoid ESD-Induced Damage

| 711                         | ola Lob illadoca ballage                            |
|-----------------------------|---|
|                             | Wrist Straps  |
| Personnel                   | Conductive Smocks                                   |
| Grounding                   | Gloves and Finger Cots                              |
|                             | Antistatic ID Badges                                |
| Eggility                    | Relative Humidity Control and Air Ionizers          |
| Facility                    | Dissipative Floors (less than $10^9 \Omega$ to GND) |
|                             | Dissipative Table Tops                              |
| Protective                  | Protective Test Equipment (Properly Grounded)       |
| Workstation                 | Grounded Tip Soldering Irons                        |
| WUIKSLALIUII                | Conductive Solder Suckers                           |
|                             | Static Sensors                                      |
|                             | Bags and Pouches (Faraday Shield)                   |
| <b>Protective Packaging</b> | Protective Tote Boxes (Conductive Static Shielding) |
| and                         | Protective Trays                                    |
| Transportation              | Grounded Carts                                      |
|                             | Protective Work Order Holders                       |

# **Ordering Information**

| Model Number | Manufacturing<br>Part Number | Product Revision | Package        | Operating Temperature |
|--------------|------------------------------|------------------|----------------|-----------------------|
| SKY77340     | SKY77340                     |                  | 6 x 8 x 1.2 mm | −25 °C to 85 °C       |

## **Revision History**

| Revision | Level | Date             | Description     |
|----------|-------|------------------|-----------------|
| А        |       | October 17, 2006 | Initial Release |

#### References

Application Note: Tape and Reel Information – RF Modules, Document Number 101568 Application Note: PCB Design and SMT Assembly/Rework, Document Number 101752

JEDEC JESD22-A114 (Human Body Model) JEDEC JESD22-A115 (Machine Model)

JEDEC JESD22-C101 (Charged Device Model)

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