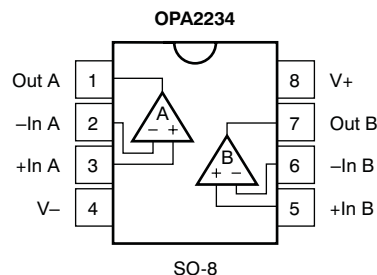


## 低功耗、精密 单电源运算放大器

查询样品: [OPA2234M](#)

### 特性

- 宽电源范围:
  - 单电源:  $V_S = 2.7\text{ V}$  至  $36\text{ V}$
  - 双电源:  $V_S = \pm 1.35\text{ V}$  至  $\pm 18\text{ V}$
- 规定性能:
  - $2.7\text{ V}$ 、 $5\text{ V}$ 、和  $\pm 15\text{ V}$
- 低静态电流: 每个放大器为  $250\text{ }\mu\text{A}$
- 低输入偏置电流:  $35\text{ nA}$  (最大值)
- 低失调电压:  $100\text{ }\mu\text{V}$  (典型值)
- 高 **CMRR**、**PSRR**、和 **A<sub>OL</sub>**
- 双通道版本



### 说明

OPA2234 系列低成本运算放大器非常适合于单电源、低电压、低功耗应用。该系列的静态电流低于较早的老式 "1013" 型产品，并采用现行的业界标准封装及引出脚配置。低失调电压、高共模抑制、高电源抑制比和宽泛的电源范围可提供卓越的准确度及通用性。双通道版本具有相同的规格指标，以实现最大的设计灵活性。这些通用型运算放大器非常适合于便携式及电池供电式应用。

OPA2234 系列运算放大器可采用单工作电源或双工作电源。采用单工作电源时，输入共模范围扩展至地电位以下，而且输出能摆动至地电位的  $50\text{mV}$  以内。极佳的相位裕度使 OPA2234 系列非常适合于要求苛刻的应用，包括高负载电容。双通道设计具有完全独立的电路，旨在实现极低的串扰并彻底消除相互干扰。

单通道及双通道器件采用 SO-8 表面贴装型封装，并具有  $-55^\circ\text{C}$  至  $125^\circ\text{C}$  的规定工作温度范围。

### 订购信息<sup>(1)</sup>

产品	封装	封装标识
OPA2234MDR	SO-8 表面贴装型封装	2234M

(1) 有关最新的封装和订购信息，请参阅本数据手册末尾的“封装选择方案附录”。



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English Data Sheet: [SGDS040](#)



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## ABSOLUTE MAXIMUM RATINGS

over operating free-air temperature range (unless otherwise noted)

		VALUE	UNIT
Supply Voltage, V+ to V–			
Input Voltage		(V–) – 0.7 to (V+) + 0.7	V
Output Short-Circuit <sup>(1)</sup>		Continuous	
Operating Temperature		–55 to 125	°C
Storage Temperature		–55 to 125	°C
Junction Temperature	T <sub>JA</sub>	150	°C/W
	T <sub>JC</sub>	39	
Lead Temperature (soldering, 10 s)		300	°C

(1) Short-circuit to ground, one amplifier per package.

**ELECTRICAL CHARACTERISTICS:  $V_S = 5\text{ V}$** 

At  $T_A = -55^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_S = 5\text{ V}$ ,  $R_L = 10\text{ k}\Omega$  connected to  $V_S/2$ , and  $V_{OUT} = V_S/2$ , unless otherwise noted.

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
<b>OFFSET VOLTAGE</b>						
Input Offset Voltage	$V_{OS}$	$T_A = 25^\circ\text{C}$ , $V_{CM} = 2.5\text{ V}$		$\pm 40$	$\pm 100$	$\mu\text{V}$
		$V_{CM} = 2.5\text{ V}$			$\pm 600$	
vs Temperature <sup>(1)</sup>	$dV_{OS}/dT$	Operating Temperature Range		$\pm 3$		$\mu\text{V}/^\circ\text{C}$
vs Power Supply	PSRR	$V_S = 2.7\text{ V}$ to $30\text{ V}$ , $V_{CM} = 1.7\text{ V}$		3	20	$\mu\text{V}/\text{V}$
vs Time				0.2		$\mu\text{V}/\text{mo}$
Channel Separation (Dual)				0.3		$\mu\text{V}/\text{V}$
<b>INPUT BIAS CURRENT</b>						
Input Bias Current <sup>(2)</sup>	$I_B$	$V_{CM} = 2.5\text{ V}$		-15	-35	nA
Input Offset Current	$I_{OS}$	$V_{CM} = 2.5\text{ V}$		$\pm 1$	$\pm 12$	nA
<b>NOISE</b>						
		$f = 1\text{ kHz}$				
Input Voltage Noise Density	$V_n$			25		$\text{nV}/\sqrt{\text{Hz}}$
Current Noise Density	$I_n$			80		$\text{fA}/\sqrt{\text{Hz}}$
<b>INPUT VOLTAGE RANGE</b>						
Common-Mode Voltage Range				0.5	$(V+) - 1$	V
Common-Mode Rejection	CMRR	$V_{CM} = 0.5\text{ V}$ to $4\text{ V}$		86	106	dB
<b>INPUT IMPEDANCE</b>						
Differential				$10^7 \parallel 5$		$\Omega \parallel \text{pF}$
Common-Mode		$V_{CM} = 2.5\text{ V}$		$10^{10} \parallel 6$		$\Omega \parallel \text{pF}$
<b>OPEN-LOOP GAIN</b>						
Open-Loop Voltage Gain	$A_{OL}$	$R_L = 10\text{ k}\Omega$ , $V_O = 0.25\text{ V}$ to $4\text{ V}$		78	120	dB
		$R_L = 2\text{ k}\Omega$ , $V_O = 0.5\text{ V}$ to $4\text{ V}$		75	96	dB
<b>FREQUENCY RESPONSE</b>						
Gain-Bandwidth Product	GBW	$C_L = 100\text{ pF}$		0.35		MHz
Slew Rate	SR			0.2		$\text{V}/\mu\text{s}$
Settling Time:						
0.1%		$G = 1$ , $3\text{ V}$ Step, $C_L = 100\text{ pF}$		15		$\mu\text{s}$
0.01%		$G = 1$ , $3\text{ V}$ Step, $C_L = 100\text{ pF}$		25		$\mu\text{s}$
Overload Recovery Time		$(V_{IN})$ (Gain) = $V_S$		16		$\mu\text{s}$
<b>OUTPUT</b>						
Voltage Output:						
Positive		$R_L = 10\text{ k}\Omega$ to $V_S/2$		$(V+) - 1$	$(V+) - 0.65$	V
Negative		$R_L = 10\text{ k}\Omega$ to $V_S/2$		0.25	0.05	V
Positive		$R_L = 10\text{ k}\Omega$ to Ground		$(V+) - 1$	$(V+) - 0.65$	V
Negative		$R_L = 10\text{ k}\Omega$ to Ground		0.1	0.05	V
Short-Circuit Current	$I_{SC}$			$\pm 11$		mA
Capacitive Load Drive (Stable Operation) <sup>(3)</sup>		$G = 1$		1000		pF
<b>POWER SUPPLY</b>						
Specified Operating Voltage				5		V
Operating Voltage Range				2.7	36	V
Quiescent Current (per amplifier)	$I_Q$	$I_O = 0$		250	550	$\mu\text{A}$

(1) Wafer-level tested to 95% confidence level.

(2) Positive conventional current flows into the input terminals.

(3) See *Small-Signal Overshoot vs Load Capacitance* typical curve.

**ELECTRICAL CHARACTERISTICS:  $V_S = 5\text{ V}$  (continued)**

At  $T_A = -55^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_S = 5\text{ V}$ ,  $R_L = 10\text{ k}\Omega$  connected to  $V_S/2$ , and  $V_{OUT} = V_S/2$ , unless otherwise noted.

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>TEMPERATURE RANGE</b>					
Specified Range		-55		125	$^\circ\text{C}$
Operating Range		-55		125	$^\circ\text{C}$
Storage		-55		125	$^\circ\text{C}$
Thermal Resistance $\theta_{JA}$			150		$^\circ\text{C}/\text{W}$

**ELECTRICAL CHARACTERISTICS:  $V_S = 2.7\text{ V}$** 

At  $T_A = -55^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_S = 2.7\text{ V}$ ,  $R_L = 10\text{ k}\Omega$  connected to  $V_S/2$ , and  $V_{OUT} = V_S/2$ , unless otherwise noted.

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFFSET VOLTAGE</b>						
Input Offset Voltage	$V_{OS}$	$T_A = 25^\circ\text{C}$ , $V_{CM} = 1.35\text{ V}$		$\pm 40$	$\pm 100$	$\mu\text{V}$
		$V_{CM} = 1.35\text{ V}$			$\pm 600$	
vs Temperature <sup>(1)</sup>	$dV_{OS}/dT$	Operating Temperature Range		$\pm 3$		$\mu\text{V}/^\circ\text{C}$
vs Power Supply	PSRR	$V_S = 2.7\text{ V}$ to $30\text{ V}$ , $V_{CM} = 1.7\text{ V}$		3	20	$\mu\text{V}/\text{V}$
vs Time				0.2		$\mu\text{V}/\text{mo}$
Channel Separation (Dual)				0.3		$\mu\text{V}/\text{V}$
<b>INPUT BIAS CURRENT</b>						
Input Bias Current <sup>(2)</sup>	$I_B$	$V_{CM} = 1.35\text{ V}$		-15	-35	nA
Input Offset Current	$I_{OS}$	$V_{CM} = 1.35\text{ V}$		$\pm 1$	$\pm 12$	nA
<b>NOISE</b>						
		$f = 1\text{ kHz}$				
Input Voltage Noise Density	$V_n$			25		$\text{nV}/\sqrt{\text{Hz}}$
Current Noise Density	$I_n$			80		$\text{fA}/\sqrt{\text{Hz}}$
<b>INPUT VOLTAGE RANGE</b>						
Common-Mode Voltage Range			0.5		$(V+) - 1.1$	V
Common-Mode Rejection	CMRR	$V_{CM} = 0.5\text{ V}$ to $1.6\text{ V}$	86	106		dB
<b>INPUT IMPEDANCE</b>						
Differential				$10^7 \parallel 5$		$\Omega \parallel \text{pF}$
Common-Mode		$V_{CM} = 1.35\text{ V}$		$10^{10} \parallel 6$		$\Omega \parallel \text{pF}$
<b>OPEN-LOOP GAIN</b>						
Open-Loop Voltage Gain	$A_{OL}$	$R_L = 10\text{ k}\Omega$ , $V_O = 0.25\text{ V}$ to $1.7\text{ V}$	78	125		dB
		$R_L = 2\text{ k}\Omega$ , $V_O = 0.5\text{ V}$ to $1.7\text{ V}$	69	96		dB
<b>FREQUENCY RESPONSE</b>						
Gain-Bandwidth Product	GBW	$C_L = 100\text{ pF}$		0.35		MHz
Slew Rate	SR			0.2		$\text{V}/\mu\text{s}$
Settling Time:						
0.1%		$G = 1$ , 1 V Step, $C_L = 100\text{ pF}$		6		$\mu\text{s}$
0.01%		$G = 1$ , 1 V Step, $C_L = 100\text{ pF}$		16		$\mu\text{s}$
Overload Recovery Time		$(V_{IN})$ (Gain) = $V_S$		8		$\mu\text{s}$
<b>OUTPUT</b>						
Voltage Output:						
Positive		$R_L = 10\text{ k}\Omega$ to $V_S/2$	$(V+) - 1$	$(V+) - 0.6$		V
Negative		$R_L = 10\text{ k}\Omega$ to $V_S/2$	0.25	0.05		V
Positive		$R_L = 10\text{ k}\Omega$ to Ground	$(V+) - 1$	$(V+) - 0.65$		V
Negative		$R_L = 10\text{ k}\Omega$ to Ground	0.1	0.05		V
Short-Circuit Current	$I_{SC}$			$\pm 8$		mA
Capacitive Load Drive (Stable Operation) <sup>(3)</sup>		$G = 1$		1000		pF
<b>POWER SUPPLY</b>						
Specified Operating Voltage				2.7		V
Operating Voltage Range			2.7		36	V
Quiescent Current (per amplifier)	$I_Q$	$I_O = 0$		250	550	$\mu\text{A}$

(1) Wafer-level tested to 95% confidence level.

(2) Positive conventional current flows into the input terminals.

(3) See *Small-Signal Overshoot vs Load Capacitance* typical curve.

**ELECTRICAL CHARACTERISTICS:  $V_S = 2.7\text{ V}$  (continued)**

At  $T_A = -55^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_S = 2.7\text{ V}$ ,  $R_L = 10\text{ k}\Omega$  connected to  $V_S/2$ , and  $V_{OUT} = V_S/2$ , unless otherwise noted.

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>TEMPERATURE RANGE</b>					
Specified Range		-55		125	$^\circ\text{C}$
Operating Range		-55		125	$^\circ\text{C}$
Storage		-55		125	$^\circ\text{C}$
Thermal Resistance	$\theta_{JA}$		150		$^\circ\text{C/W}$

**ELECTRICAL CHARACTERISTICS:  $V_S = \pm 15\text{ V}$** 

At  $T_A = -55^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_S = \pm 15\text{ V}$ ,  $R_L = 10\text{ k}\Omega$  connected to  $V_S/2$ , and  $V_{OUT} = V_S/2$ , unless otherwise noted.

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFFSET VOLTAGE</b>						
Input Offset Voltage	$V_{OS}$	$T_A = 25^\circ\text{C}$ , $V_{CM} = 0\text{ V}$		$\pm 70$	$\pm 250$	$\mu\text{V}$
		$V_{CM} = 0\text{ V}$			$\pm 750$	
vs Temperature <sup>(1)</sup>	$dV_{OS}/dT$	Operating Temperature Range		$\pm 3$		$\mu\text{V}/^\circ\text{C}$
vs Power Supply	PSRR	$V_S = \pm 1.35\text{ V}$ to $\pm 18\text{ V}$ , $V_{CM} = 0\text{ V}$		3	20	$\mu\text{V}/\text{V}$
vs Time				0.2		$\mu\text{V}/\text{mo}$
Channel Separation (Dual)				0.3		$\mu\text{V}/\text{V}$
<b>INPUT BIAS CURRENT</b>						
Input Bias Current <sup>(2)</sup>	$I_B$	$V_{CM} = 0\text{ V}$		-12	-30	nA
Input Offset Current	$I_{OS}$	$V_{CM} = 0\text{ V}$		$\pm 1$	$\pm 12$	nA
<b>NOISE</b>						
		$f = 1\text{ kHz}$				
Input Voltage Noise Density	$V_n$			25		$\text{nV}/\sqrt{\text{Hz}}$
Current Noise Density	$I_n$			80		$\text{fA}/\sqrt{\text{Hz}}$
<b>INPUT VOLTAGE RANGE</b>						
Common-Mode Voltage Range			$(V-) + 1$		$(V+) - 1$	V
Common-Mode Rejection	CMRR	$V_{CM} = -14\text{ V}$ to $14\text{ V}$	86	106		dB
<b>INPUT IMPEDANCE</b>						
Differential				$10^7 \parallel 5$		$\Omega \parallel \text{pF}$
Common-Mode		$V_{CM} = 0\text{ V}$		$10^{10} \parallel 6$		$\Omega \parallel \text{pF}$
<b>OPEN-LOOP GAIN</b>						
Open-Loop Voltage Gain	$A_{OL}$	$V_O = -13.5\text{ V}$ to $13\text{ V}$	87	120		dB
<b>FREQUENCY RESPONSE</b>						
Gain-Bandwidth Product	GBW	$C_L = 100\text{ pF}$		0.35		MHz
Slew Rate	SR			0.2		$\text{V}/\mu\text{s}$
Settling Time:						
0.1%		$G = 1$ , 10 V Step, $C_L = 100\text{ pF}$		41		$\mu\text{s}$
0.01%		$G = 1$ , 10 V Step, $C_L = 100\text{ pF}$		47		$\mu\text{s}$
Overload Recovery Time		$(V_{IN}) (\text{Gain}) = V_S$		22		$\mu\text{s}$
<b>OUTPUT</b>						
Voltage Output:						
Positive			$(V+) - 2$	$(V+) - 0.7$		V
Negative			$(V-) + 1.5$	$(V-) + 0.15$		V
Short-Circuit Current	$I_{SC}$			$\pm 22$		mA
Capacitive Load Drive (Stable Operation) <sup>(3)</sup>		$G = 1$		1000		pF
<b>POWER SUPPLY</b>						
Specified Operating Voltage				$\pm 15$		V
Operating Voltage Range			$\pm 1.35$		$\pm 18$	V
Quiescent Current (per amplifier)	$I_Q$	$I_O = 0$		$\pm 275$	$\pm 550$	$\mu\text{A}$

(1) Wafer-level tested to 95% confidence level.

(2) Positive conventional current flows into the input terminals.

(3) See *Small-Signal Overshoot vs Load Capacitance* typical curve.

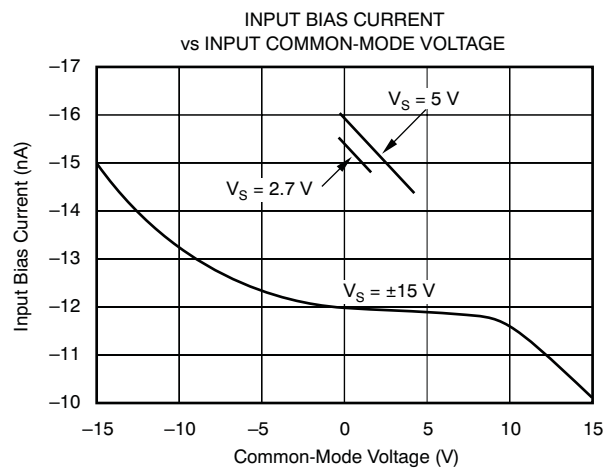
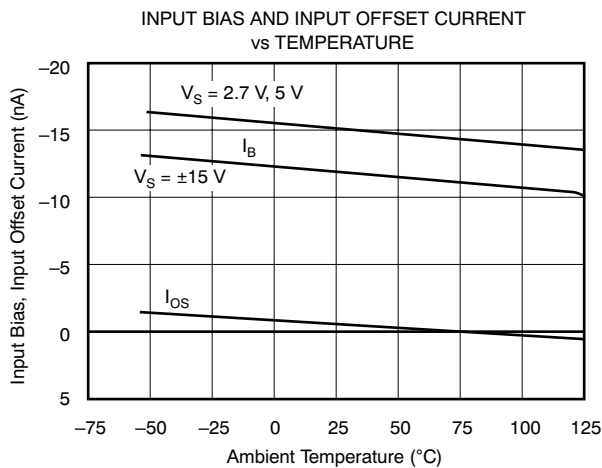
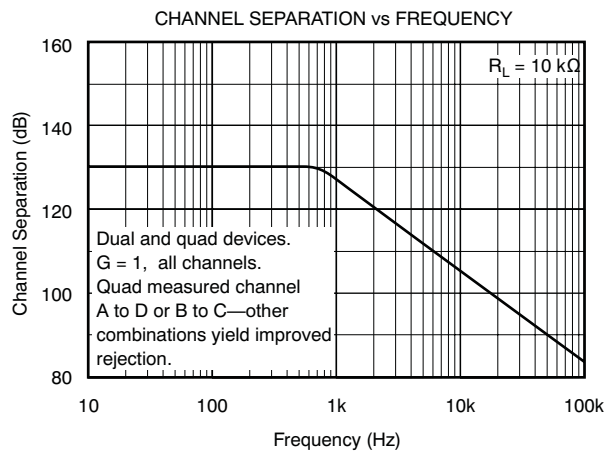
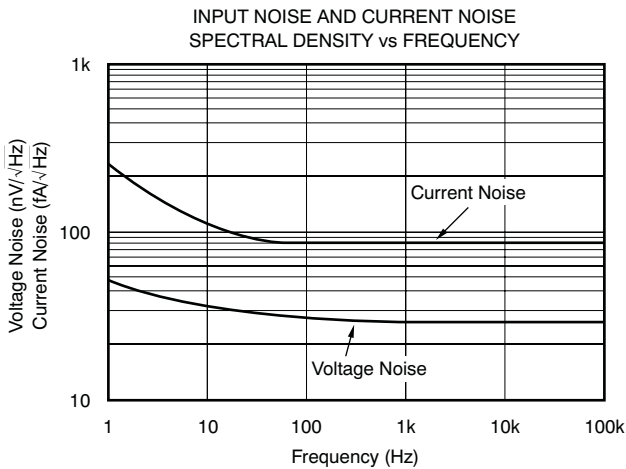
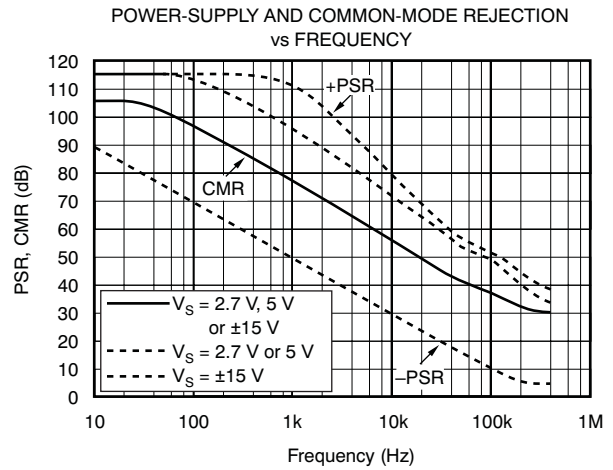
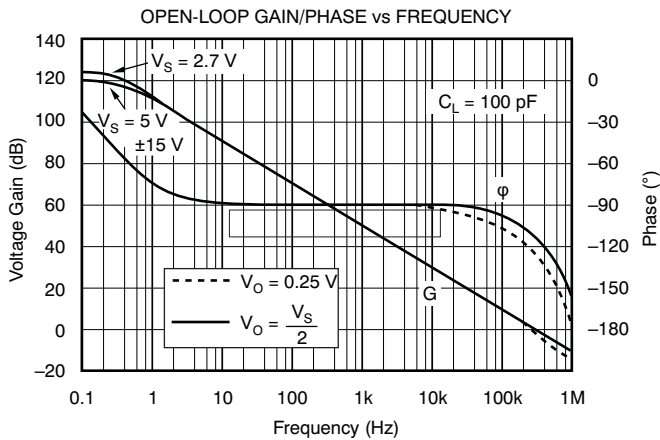
**ELECTRICAL CHARACTERISTICS:  $V_S = \pm 15\text{ V}$  (continued)**

At  $T_A = -55^\circ\text{C}$  to  $125^\circ\text{C}$ ,  $V_S = \pm 15\text{ V}$ ,  $R_L = 10\text{ k}\Omega$  connected to  $V_S/2$ , and  $V_{OUT} = V_S/2$ , unless otherwise noted.

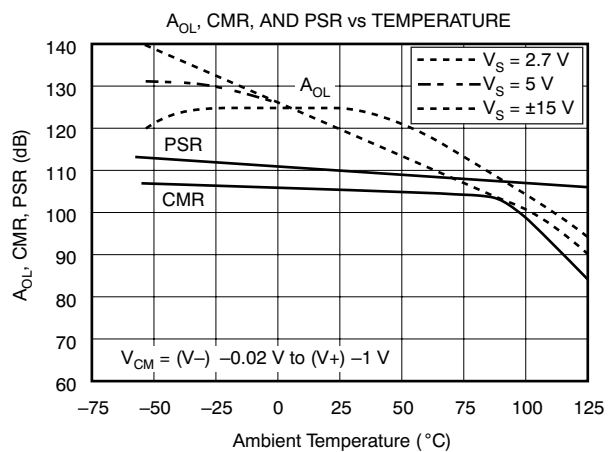
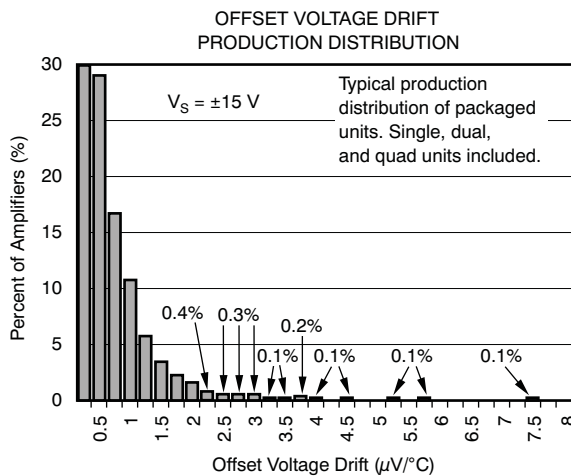
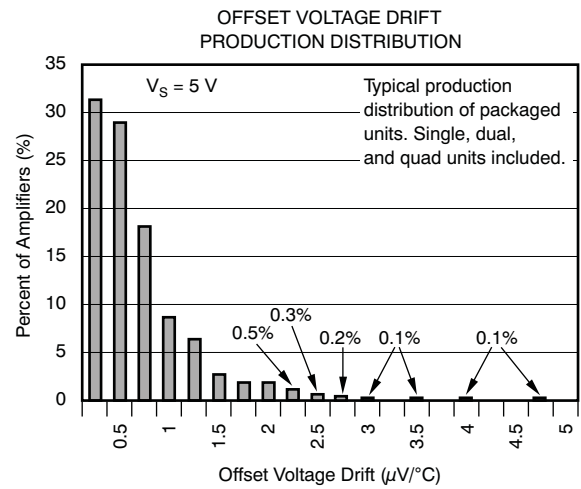
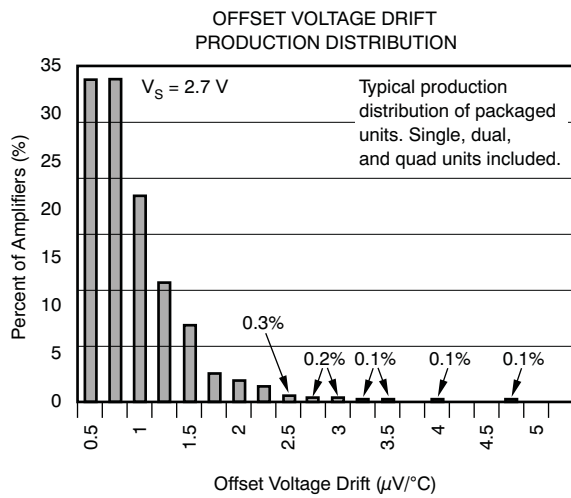
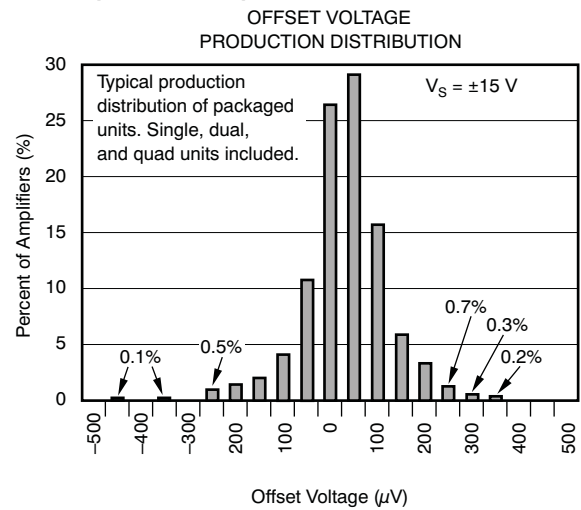
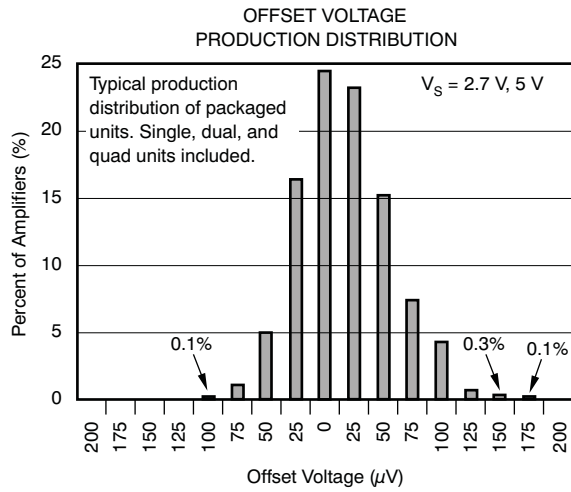
PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>TEMPERATURE RANGE</b>					
Specified Range		-55		125	$^\circ\text{C}$
Operating Range		-55		125	$^\circ\text{C}$
Storage		-55		125	$^\circ\text{C}$
Thermal Resistance	$\theta_{JA}$		150		$^\circ\text{C/W}$



TYPICAL CHARACTERISTICS

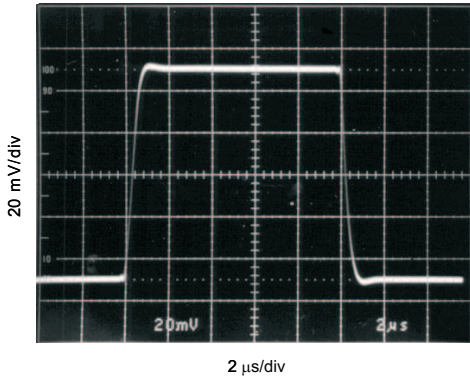


**TYPICAL CHARACTERISTICS (continued)**

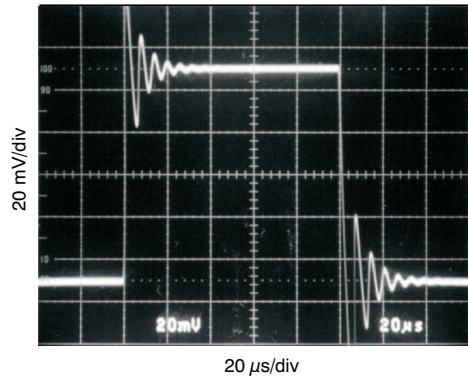


TYPICAL CHARACTERISTICS (continued)

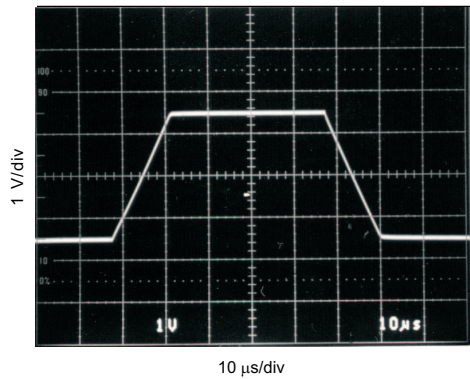
SMALL-SIGNAL STEP RESPONSE  
G = 1, C<sub>L</sub> = 100 pF, V<sub>S</sub> = 5 V



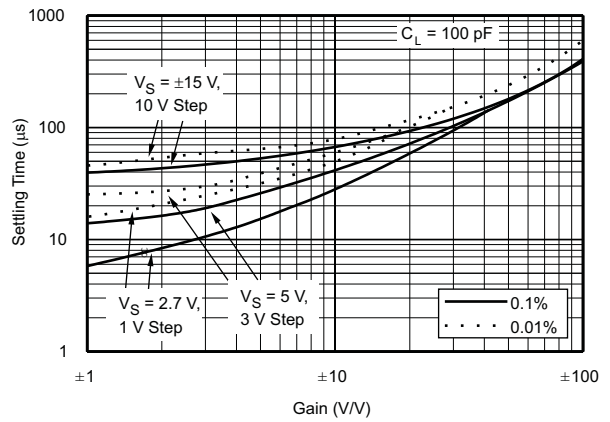
SMALL-SIGNAL STEP RESPONSE  
G = 1, C<sub>L</sub> = 10,000 pF, V<sub>S</sub> = 5 V



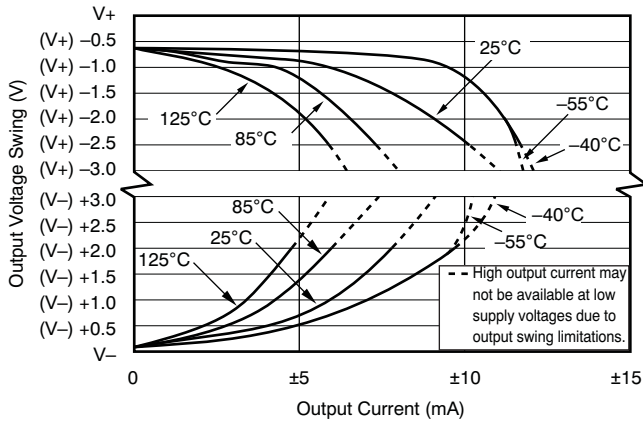
LARGE-SIGNAL STEP RESPONSE  
G = 1, C<sub>L</sub> = 100 pF, V<sub>S</sub> = 5 V



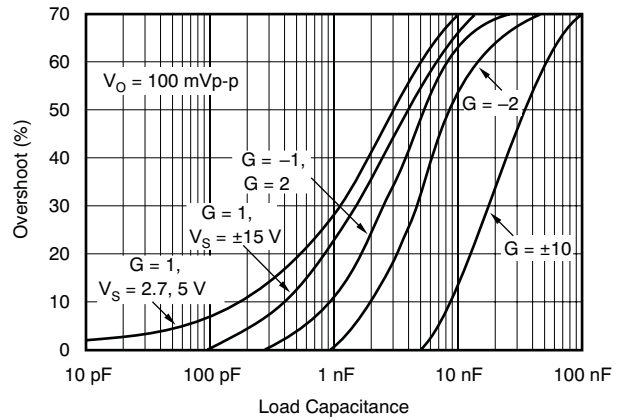
SETTLING TIME vs CLOSED-LOOP GAIN



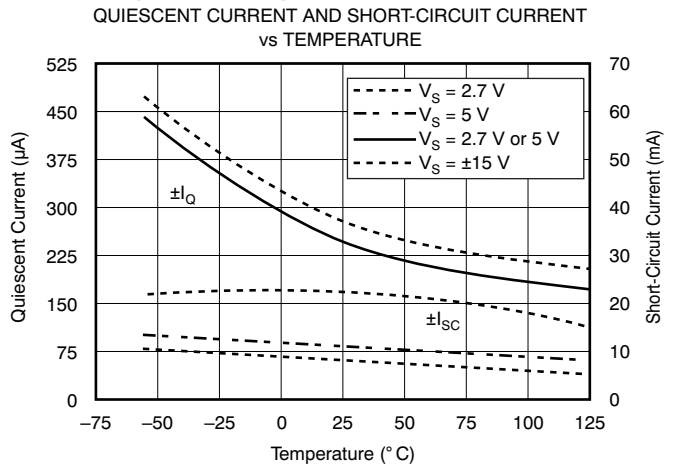
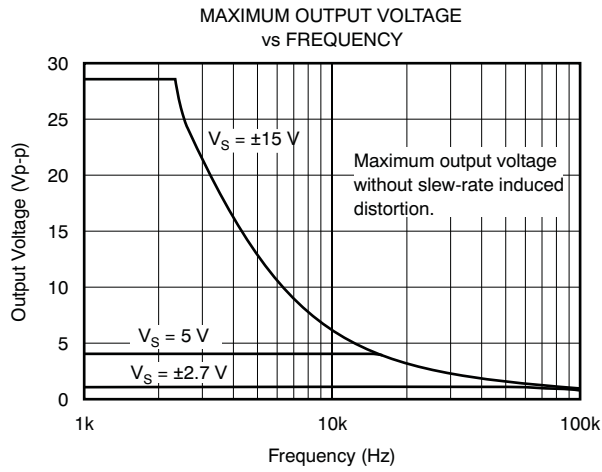
OUTPUT VOLTAGE SWING vs OUTPUT CURRENT



SMALL-SIGNAL OVERSHOOT vs LOAD CAPACITANCE



**TYPICAL CHARACTERISTICS (continued)**



## APPLICATION INFORMATION

The OPA2234 series op amps are unity-gain stable and suitable for a wide range of general-purpose applications. Power-supply pins should be bypassed with 10 nF ceramic capacitors.

### OPERATING VOLTAGE

The OPA2234 series op amps operate from single (2.7 V to 36 V) or dual ( $\pm 1.35$  V to  $\pm 18$  V) supplies with excellent performance. Specifications are production tested with 2.7 V, 5 V, and  $\pm 15$  V supplies. Most behavior remains unchanged throughout the full operating voltage range. Parameters which vary significantly with operating voltage are shown in the Typical Characteristic curves.

**PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
OPA2234MDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-3-260C-168 HR	

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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**OTHER QUALIFIED VERSIONS OF OPA2234M :**

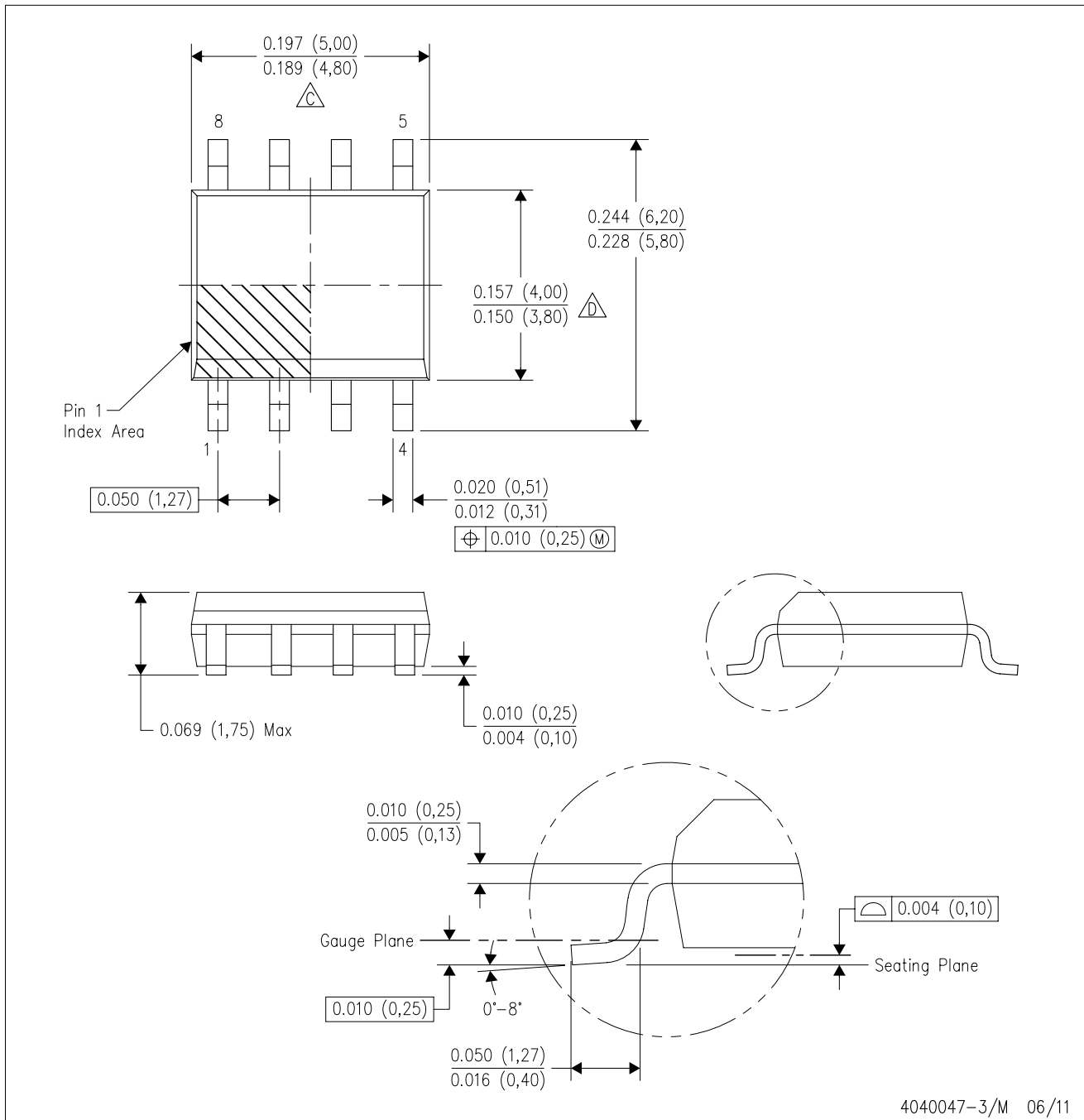
- Catalog: [OPA2234](#)

NOTE: Qualified Version Definitions:

- Catalog - TI's standard catalog product

D (R-PDSO-G8)

PLASTIC SMALL OUTLINE



- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - △ Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
  - △ Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
  - E. Reference JEDEC MS-012 variation AA.

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DSP - 数字信号处理器	<a href="http://www.ti.com.cn/dsp">http://www.ti.com.cn/dsp</a>	工业应用	<a href="http://www.ti.com.cn/industrial">www.ti.com.cn/industrial</a>
时钟和计时器	<a href="http://www.ti.com.cn/clockandtimers">http://www.ti.com.cn/clockandtimers</a>	医疗电子	<a href="http://www.ti.com.cn/medical">www.ti.com.cn/medical</a>
接口	<a href="http://www.ti.com.cn/interface">http://www.ti.com.cn/interface</a>	安防应用	<a href="http://www.ti.com.cn/security">www.ti.com.cn/security</a>
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