- High Output Drive Capability . . . 1 A Min


## - 3-State Outputs

- High Gain-Bandwidth Product 8 MHz Typ
- Low Total Harmonic Distortion <0.08\% Typ
- High Slew Rate . . . 12 V/ $\mu \mathrm{s}$ Typ
- Class AB Output Stage
- Thermal Shutdown
- Mains-Line Driver Circuit Application Included


## description

The TLE2301 is a power operational amplifier that can deliver an output current of 1 A at high frequencies with very low total harmonic distortion. The device has an integral 3-state mode to drive the output stage into a high-impedance state and also to reduce the supply current to less than 3.5 mA .

The combination of high output current and 3-state outputs makes the TLE2301 ideal for implementing the signalling transformer driver in mains-based telemetering modems. This combination of features also makes the device well suited for other high-current applications (e.g., motor drivers and audio circuits).

Using the Texas Instruments established Excalibur process, the TLE2301 is able to achieve slew rates in excess of $12 \mathrm{~V} / \mu \mathrm{s}$ and a gainbandwidth product of 8 MHz . The TLE2301 uses a 16-pin NE power package to provide better power handling capabilities than standard dual-inline packages.

The TLE2301 is characterized for operation over the industrial temperature range of $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$.


Terminals 4, 5, 12 and 13 are connected to the lead frame.

MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE
vs
FREQUENCY


Figure 1

AVAILABLE OPTION

|  |  | PACKAGE |
| :---: | :---: | :---: |
| TA | VIOmax AT $25^{\circ} \mathrm{C}$ | THERMALLY-ENHANCED <br> PLASTIC DIP <br> (NE) |
| $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ | 10 mV | TLE2301INE |

EXCALIBUR 3-STATE-OUTPUT WIDE-BANDWIDTH POWER OPERATIONAL AMPLIFIER
SLOS131 - DECEMBER 1993
equivalent schematic (entire device)

equivalent schematic (TRS1 and TRS2 inputs)


## Terminal Functions

| TERMINAL |  | DESCRIPTION |
| :---: | :---: | :---: |
| NAME | NO. |  |
| COMP1 COMP2 | $\begin{array}{r} 16 \\ 1 \end{array}$ | COMP1 and COMP2 are compensation network terminals |
| IN+ | 14 | Noninverting input |
| IN- | 11 | Inverting input |
| OUT1 OUT2 | $\begin{aligned} & 3 \\ & 6 \end{aligned}$ | Two low-distortion class-AB output stages. Each is capable of sourcing more than 500 mA . OUT1 and OUT2 should be connected together for all applications. |
| $\begin{aligned} & \text { TRS1 } \\ & \text { TRS2 } \end{aligned}$ | $\begin{aligned} & 9 \\ & 8 \end{aligned}$ | TRS1 and TRS2 are 3-state input terminals. TRS2 should be connected to the ground of the circuit generating the 3-state command (normally $\mu \mathrm{P}$ ground). The TLE2301 is brought into 3 -state mode by raising TRS1 2 V above TRS2. Placing the TLE2301 in a 3-state mode reduces the supply current to below 2.2 mA (typ). Normal operation resumes by bringing TRS1 to within 0.8 V of TRS2. The 3 -state function can be disabled by connecting both TRS1 and TRS2 to $\mathrm{V}_{\mathrm{CC}}$-. |
| $\mathrm{V}_{\mathrm{CC}}-$ | 10, 15 | High-impedance $\mathrm{V}_{\mathrm{CC}}$ - input terminals. Although these do not carry any of the device's supply current, they increase the stability of the device and should be connected to the negative supply terminal ( $\mathrm{V}_{\mathrm{CC}}$ ). |
| $\mathrm{V}_{\mathrm{CC}}-$ | $\begin{gathered} 4,5, \\ 12,13 \end{gathered}$ | Negative supply terminals and substrate. As with all NE packages, the substrate is directly connected to the lead frame. The result is that the junction-to-ambient thermal impedance $\left(Z_{\theta J A}\right)$ is greatly reduced by soldering the negative supply terminals to the copper area of the printed-circuit board (PCB). |
| $\mathrm{V}_{\mathrm{CC}+}$ | 2, 7 | Positive supply terminals. Both terminals should be connected to the positive voltage supply. |

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted) $\dagger$

> Supply voltage, $\mathrm{V}_{\mathrm{CC}+}$ (see Note 1) . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . 22 V
> Supply voltage, VCC- $_{\text {CC }}$ (see Note 1) . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . 22 V
> Differential input voltage, $\mathrm{V}_{\text {ID }}$ (see Note 2) . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . $\pm 44 \mathrm{~V}$
> Duration of short-circuit current at (or below) $25^{\circ} \mathrm{C}$ (see Note 3) . . . . . . . . . . . . . . . . . . . . . . . . . . . . unlimited
> Continuous total dissipation at (or below) $25^{\circ} \mathrm{C}$ free-air temperature (see Notes 4 and 5) ...... 2075 mW
> Continuous total dissipation at $85^{\circ} \mathrm{C}$ case temperature (see Note 5) . . . . . . . . . . . . . . . . . . . . . . . . 4640 mW
> Operating free-air temperature range, $\mathrm{T}_{\mathrm{A}}$. . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$
> Operating case or virtual junction temperature range . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . $-40^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$
> Storage temperature range . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . . $65^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$
$\dagger$ Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
NOTES: 1. All voltage values, except differential voltages, are with respect to the midpoint between $\mathrm{V}_{\mathrm{CC}}+$ and $\mathrm{V}_{\mathrm{CC}}$.
2. Differential voltages are at $\mathrm{IN}+$ with respect to $\operatorname{IN}-$.
3. The outputs when connected together may be shorted to either supply. Temperature and/or supply voltages must be limited to ensure that the maximum dissipation rating is not exceeded.
4. For operation above $25^{\circ} \mathrm{C}$ free-air temperature, derate linearly at the rate of $16.56 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$.
5. For operation above $25^{\circ} \mathrm{C}$ case temperature, derate linearly at the rate of $71.4 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$. To avoid exceeding the design maximum virtual junction temperature, these ratings should not be exceeded. Due to variations in individual device electrical characteristics and thermal resistance, the built-in thermal overload protection may be activated at power levels slightly above or below the rated dissipation.


## recommended operating conditions

|  |  | MIN | MAX |
| :--- | ---: | ---: | :---: |
|  | UNIT |  |  |
| Supply voltage, $\mathrm{V}_{\mathrm{CC} \pm}$ | $\pm 4.5$ | $\pm 20$ | V |
| Common-mode input voltage, $\mathrm{V}_{\mathrm{IC}}$ | $\mathrm{V}_{\mathrm{CC} \pm= \pm 5 \mathrm{~V}}$ | -4 | 1.6 |
|  | $\mathrm{~V}_{\mathrm{CC} \pm= \pm 15 \mathrm{~V}}$ | -14 | 11.8 |
| High-level 3-state enable voltage, $\mathrm{V}_{\mathrm{IH}}$ | 2 | V |  |
| Low-level 3-state enable voltage, $\mathrm{V}_{\mathrm{IL}}$ |  | V |  |
| Continuous output current |  | 0.8 | V |
| Operating free-air temperature, $\mathrm{T}_{\mathrm{A}}$ |  | -40 | 85 |

electrical characteristics at specified free-air temperature, $\mathrm{V}_{\mathrm{CC}}{ }_{ \pm}= \pm 5 \mathrm{~V}, \mathrm{C}_{\mathrm{C}}=15 \mathrm{pF}$ (unless otherwise noted) (see Figure 5)

| PARAMETER |  | TEST CONDITIONS |  | TA ${ }^{\dagger}$ | MIN | TYP | MAX | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| VIO | Input offset voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}=0, \\ & \mathrm{R}_{\mathrm{S}}=50 \Omega \end{aligned}$ | $\mathrm{V}_{\text {IC }}=0$, | $25^{\circ} \mathrm{C}$ |  | 0.4 | 7 | mV |
|  |  |  |  | Full range |  |  | 10 |  |
| IB | Input bias current | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}=0, \\ & \mathrm{R}_{\mathrm{S}}=50 \Omega \end{aligned}$ | $\mathrm{V}_{\text {IC }}=0$, | $25^{\circ} \mathrm{C}$ |  | 283 | 450 | nA |
|  |  |  |  | Full range | 500 |  |  |  |
| VICR | Common-mode input voltage range | $\mathrm{R}_{S}=50 \Omega$ |  | Full range | $\begin{array}{r} \text { to } \\ 1.6 \end{array}$ |  |  | V |
| V $\mathrm{OM}_{+}$Maximum positive peak output voltage swing |  | $R_{L}=20 \Omega$, | See Note 6 | $25^{\circ} \mathrm{C}$ | 3.3 | 3.5 |  | V |
|  |  | Full range |  | 3.2 |  |  |  |
| VOM- | Maximum negative peak output voltage swing |  | $R_{L}=20 \Omega$, | See Note 6 | $25^{\circ} \mathrm{C}$ | -3.2 | -3.4 |  | V |
|  |  | Full range |  |  | -3.1 |  |  |  |  |
| AVD | Large-signal differential voltage amplification | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}= \pm 2 \mathrm{~V}, \\ & \mathrm{R}_{\mathrm{L}}=20 \Omega \end{aligned}$ | $V_{\text {IC }}=0$, | $25^{\circ} \mathrm{C}$ | 65 | 87 |  | dB |  |
|  |  |  |  | Full range | 60 |  |  |  |  |
| $\mathrm{r}_{\mathrm{i}}$ | Differential input resistance |  |  | $25^{\circ} \mathrm{C}$ |  | 1 |  | $\mathrm{M} \Omega$ |  |
| ro | Output resistance (see Note 7) | TRS1 $=0.8 \mathrm{~V}$ |  | $25^{\circ} \mathrm{C}$ |  | 1 |  | $\Omega$ |  |
|  |  | TRS1 = 2 V , | 3-state mode |  | 100 |  |  | k $\Omega$ |  |
| CMRR | Common-mode rejection ratio | $\begin{aligned} & \mathrm{V}_{\mathrm{IC}}=\mathrm{V}_{\mathrm{ICR}} \mathrm{~min}, \\ & \mathrm{R}_{\mathrm{S}}=50 \Omega \end{aligned}$ | $\mathrm{V}_{\mathrm{O}}=0,$ | $25^{\circ} \mathrm{C}$ | 65 | 88 |  | dB |  |
| kSVR | Supply-voltage rejection ratio ( $\Delta \mathrm{V}_{\mathrm{CC}} \pm / \Delta \mathrm{V}_{\mathrm{IO}}$ ) | $\begin{aligned} & \mathrm{V}_{\mathrm{CC} \pm}= \pm 4.5 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{IC}}=0, \end{aligned}$ | $\pm 20 \mathrm{~V}$, <br> No load | $25^{\circ} \mathrm{C}$ | 70 | 100 |  | dB |  |
| IIH | Enable input current, high | $\mathrm{V}_{\mathrm{I}}=2 \mathrm{~V}$, | 3-state mode | $25^{\circ} \mathrm{C}$ |  | 0.01 | 0.5 | $\mu \mathrm{A}$ |  |
|  |  |  |  | Full range |  |  | 0.5 |  |  |
| IIL | Enable input current, low | $\mathrm{V}_{\mathrm{I}}=0.8 \mathrm{~V}$ |  | $25^{\circ} \mathrm{C}$ |  | 0.01 | 0.5 | $\mu \mathrm{A}$ |  |
|  |  |  |  | Full range |  |  | 0.5 |  |  |
| Ios | Short-circuit output current (see Note 8) | $\mathrm{V}_{\mathrm{O}}=0$, | $t_{p} \leq 50 \mu \mathrm{~s}$ | $25^{\circ} \mathrm{C}$ | 1 | 1.8 |  | A |  |
| ICC | Supply current | $\mathrm{V}_{\mathrm{O}}=0$, | No load | $25^{\circ} \mathrm{C}$ |  | 10 | 21 | mA |  |
|  |  |  |  | Full range |  |  | 25 |  |  |
|  |  | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}=0, \\ & \text { 3-state mode } \end{aligned}$ | No load, | $25^{\circ} \mathrm{C}$ |  | 1.73 | 2.7 |  |  |
|  |  |  |  | Full range |  |  | 3.5 |  |  |

$\dagger$ Full range is $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$.
NOTES: 6. OUT1 and OUT2 are connected together for all tests.
7. TRS1 voltage is measured with respect to TRS2 potential.
8. Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately ( $t_{p}=$ pulse duration time).
electrical characteristics at specified free-air temperature, $\mathrm{V}_{\mathrm{CC} \pm}= \pm 15 \mathrm{~V}, \mathrm{C}_{\mathrm{C}}=15 \mathrm{pF}$ (unless otherwise noted) (see Figure 5)

|  | PARAMETER | TEST CONDITIONS |  | TA ${ }^{\dagger}$ | MIN | TYP | MAX | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{10}$ | Input offset voltage | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}=0, \\ & \mathrm{R}_{\mathrm{S}}=50 \Omega \end{aligned}$ | $V_{\text {IC }}=0$, | $25^{\circ} \mathrm{C}$ |  | 0.3 | 10 | mV |
|  |  |  |  | Full range |  |  | 15 |  |
| IIB | Input bias current | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}=0, \\ & \mathrm{R}_{\mathrm{S}}=50 \Omega \end{aligned}$ | $V_{\text {IC }}=0$, | $25^{\circ} \mathrm{C}$ |  | 260 | 450 | nA |
|  |  |  |  | Full range |  |  | 500 |  |
| VICR | Common-mode input voltage range | $R_{S}=50 \Omega$ |  | Full range | $\begin{array}{r} -14 \\ \text { to } \\ 11.8 \end{array}$ |  |  | V |
| V $\mathrm{OM}+$ Maximum positive peak output voltage swing |  | $R_{L}=20 \Omega$, | See Note 6 | $25^{\circ} \mathrm{C}$ | 13 | 13.5 |  | V |
|  |  | Full range |  | 13 |  |  |  |
| VOM- | Maximum negative peak output voltage swing |  | $\mathrm{R}_{\mathrm{L}}=20 \Omega$, | See Note 6 | $25^{\circ} \mathrm{C}$ | -12.6 | -13 |  | V |
|  |  | Full range |  |  | -12.5 |  |  |  |  |
| Avd | Large-signal differential voltage amplification | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}= \pm 6 \mathrm{~V}, \\ & \mathrm{R}_{\mathrm{L}}=20 \Omega \end{aligned}$ | $V_{\text {IC }}=0$, | $25^{\circ} \mathrm{C}$ | 70 | 102 |  | dB |  |
|  |  |  |  | Full range | 65 |  |  |  |  |
| $\mathrm{r}_{\mathrm{i}}$ | Differential input resistance |  |  | $25^{\circ} \mathrm{C}$ |  | 1 |  | $\mathrm{M} \Omega$ |  |
| ro | Output resistance (see Note 7) | TRS1 $=0.8 \mathrm{~V}$ |  | $25^{\circ} \mathrm{C}$ |  | 1 |  | $\Omega$ |  |
|  |  | TRS1 $=2 \mathrm{~V}$, | 3-state mode |  |  | 100 |  | k $\Omega$ |  |
| CMRR | Common-mode rejection ratio | $\begin{aligned} & \mathrm{V}_{\mathrm{IC}}=\mathrm{V}_{\text {ICR }} \mathrm{min} \\ & \mathrm{R}_{\mathrm{S}}=50 \Omega \end{aligned}$ | $\mathrm{V}_{\mathrm{O}}=0,$ | $25^{\circ} \mathrm{C}$ | 70 | 97 |  | dB |  |
| kSVR | Supply-voltage rejection ratio ( $\left.\Delta \mathrm{V}_{\mathrm{CC}} \pm / \Delta \mathrm{V}_{1 \mathrm{O}}\right)$ | $\begin{aligned} & \mathrm{V}_{\mathrm{CC} \pm}= \pm 4.5 \\ & \mathrm{~V}_{\mathrm{IC}}=0, \end{aligned}$ | to $\pm 20 \mathrm{~V}$, <br> No load | $25^{\circ} \mathrm{C}$ | 70 | 100 |  | dB |  |
| ${ }_{\text {IH }}$ | Enable input current, high | V I $=2 \mathrm{~V}$, | 3 -state mode | $25^{\circ} \mathrm{C}$ |  | 0.01 | 0.5 | $\mu \mathrm{A}$ |  |
|  |  |  |  | Full range |  |  | 0.5 |  |  |
| IIL | Enable input current, low | $\mathrm{V}_{\mathrm{I}}=0.8 \mathrm{~V}$ |  | $25^{\circ} \mathrm{C}$ |  | 0.01 | 0.5 | $\mu \mathrm{A}$ |  |
|  |  |  |  | Full range |  |  | 0.5 |  |  |
| Ios | Short-circuit output current (see Note 8) | $\mathrm{V}_{\mathrm{O}}=0$, | $\mathrm{t}_{\mathrm{p}} \leq 50 \mu \mathrm{~s}$ | $25^{\circ} \mathrm{C}$ | 1 | 3 |  | A |  |
| ICC | Supply current | $\mathrm{V}_{\mathrm{O}}=0$, | No load | $25^{\circ} \mathrm{C}$ |  | 11 | 25 | mA |  |
|  |  |  |  | Full range |  |  | 30 |  |  |
|  |  | $\mathrm{V}_{\mathrm{O}}=0,$ <br> 3-state mode | No load, | $25^{\circ} \mathrm{C}$ |  | 2.2 | 3.5 |  |  |
|  |  |  |  | Full range |  |  | 5 |  |  |

$\dagger$ Full range is $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$.
NOTES: 6. OUT1 and OUT2 are connected together for all tests.
7. TRS1 voltage is measured with respect to TRS2 potential.
8. Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible. Thermal effects must be taken into account separately ( $t_{p}=$ pulse duration time).
operating characteristics at specified free-air temperature, $\mathrm{V}_{\mathrm{CC}}+= \pm 5 \mathrm{~V}, \mathrm{C}_{\mathrm{C}}=15 \mathrm{pF}, \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ (unless otherwise noted) (see Figure 5)

|  | PARAMETER | TEST CONDITIONS | MIN TYP | MAX | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: |
| SR | Slew rate at unity gain (see Figure 1) | $\begin{array}{ll} \mathrm{V}_{\mathrm{O}}= \pm 1.5 \mathrm{~V}, \quad \mathrm{R}_{\mathrm{L}}=20 \Omega, \\ \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF} & \end{array}$ | 912 |  | V/us |
| $t_{s}$ | Settling time (see Figure 1) | $\begin{aligned} & \begin{array}{l} \mathrm{R}_{\mathrm{L}}=20 \Omega, \quad \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF}, \\ 3 \text {-V step to } \\ 30 \mathrm{mV}(1 \%) \end{array} \\ & \hline \end{aligned}$ | 0.7 |  | $\mu \mathrm{S}$ |
| $\mathrm{V}_{\mathrm{n}}$ | Equivalent input noise voltage (see Figure 2) | $\mathrm{R}_{\mathrm{S}}=50 \Omega, \quad \mathrm{f}=1 \mathrm{kHz}$ | 44 |  | $\mathrm{nV} / \sqrt{\mathrm{Hz}}$ |
| THD | Total harmonic distortion | $\begin{array}{ll} \hline \mathrm{V}_{\mathrm{O}}=1 \mathrm{~V}_{\mathrm{rms}}, & \mathrm{f}=50 \mathrm{kHz}, \\ \mathrm{R}_{\mathrm{L}}=20 \Omega, & \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF} \\ \hline \end{array}$ | 0.04\% |  |  |
| $\mathrm{B}_{1}$ | Unity-gain bandwidth (see Figure 3) | $\mathrm{R}_{\mathrm{L}}=20 \Omega, \quad \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF}$ | 8 |  | MHz |
| $\phi_{m}$ | Phase margin at unity gain (see Figure 3) | $\mathrm{R}_{\mathrm{L}}=20 \Omega, \quad \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF}$ | $30^{\circ}$ |  |  |

operating characteristics at specified free-air temperature, $\mathrm{V}_{\mathrm{CC} \pm}= \pm 15 \mathrm{~V}, \mathrm{C}_{\mathrm{C}}=15 \mathrm{pF}, \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ (unless otherwise noted) (see Figure 5)

|  | PARAMETER | TEST CONDITIONS | MIN TYP | MAX | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: |
| SR | Slew rate at unity gain (see Figure 1) | $\begin{array}{ll} \mathrm{V}_{\mathrm{O}}= \pm 10 \mathrm{~V}, \quad \mathrm{R}_{\mathrm{L}}=20 \Omega, \\ \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF} & \\ \hline \end{array}$ | 914 |  | V/us |
| $\mathrm{t}_{\text {s }}$ | Settling time (see Figure 1) | $\begin{aligned} & \hline \mathrm{R}_{\mathrm{L}}=20 \Omega, \quad \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF}, \\ & 20-\mathrm{V} \text { step to } 200 \mathrm{mV}(1 \%) \end{aligned}$ | 1.8 |  | $\mu \mathrm{S}$ |
| $\mathrm{V}_{\mathrm{n}}$ | Equivalent input noise voltage (see Figure 2) | $\mathrm{R}_{\mathrm{S}}=50 \Omega, \quad \mathrm{f}=1 \mathrm{kHz}$ | 44 |  | $\mathrm{nV} / \sqrt{\mathrm{Hz}}$ |
| THD | Total harmonic distortion | $\begin{array}{ll} \hline \mathrm{V}_{\mathrm{O}}=2 \mathrm{~V}_{\mathrm{rms}}, & \mathrm{f}=50 \mathrm{kHz}, \\ \mathrm{R}_{\mathrm{L}}=20 \Omega, & C_{L}=100 \mathrm{pF} \\ \hline \end{array}$ | 0.08\% |  |  |
| $\mathrm{B}_{1}$ | Unity-gain bandwidth (see Figure 3) | $\mathrm{R}_{\mathrm{L}}=20 \Omega, \quad \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF}$ | 8 |  | MHz |
| $\phi_{\text {m }}$ | Phase margin at unity gain (see Figure 3) | $\mathrm{R}_{\mathrm{L}}=20 \Omega, \quad \mathrm{C}_{\mathrm{L}}=100 \mathrm{pF}$ | $35^{\circ}$ |  |  |



NOTE $A: C_{L}$ includes the fixture capacitance.
Figure 2. Slew-Rate Test Circuit


NOTE A: $C_{L}$ includes the fixture capacitance.
Figure 4. Gain-Bandwidth and Phase-Margin Test Circuit


Figure 3. Noise-Voltage Test Circuit


Figure 5. Compensation Configuration

## typical values

Typical values presented in this data sheet represent the median ( $50 \%$ point) of the device parametric performance.

## TYPICAL CHARACTERISTICS

Table of Graphs

|  |  |  | FIGURE |
| :---: | :---: | :---: | :---: |
| IIB | Input bias current | vs Free-air temperature | 6, 7 |
| ${ }^{1} \mathrm{O}$ | Input offset current | vs Free-air temperature | 6,7 |
|  | Differential voltage amplification | vs Frequency | 8 |
| D | Diferentiarvolage amplifation | vs Free-air temperature | 9 |
| V O(PP) | Maximum peak-to-peak output voltage | vs Frequency | 10, 11 |
|  | Max | vs Output current | 12, 13 |
| OM | Maximum peak output voltage | vs Supply voltage | 14 |
| $\mathrm{Z}_{\theta \mathrm{JA}}$ | Transient junction-to-ambient thermal impedance | vs Time | 15 |
|  | Supply current | vs Supply voltage | 16 |
| cc | Supply current | vs Free-air temperature | 17 |
|  | Pulse response | Small signal | 18, 19 |
|  | Pulse response | Large signal | 20, 21 |
| $\mathrm{z}_{0}$ | Output impedance | vs Frequency | 22, 23 |



Figure 6

## TYPICAL CHARACTERISTICS



Figure 8

## MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE VS <br> FREQUENCY



Figure 10

DIFFERENTIAL VOLTAGE AMPLIFICATION
vs
FREE-AIR TEMPERATURE


Figure 9

MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE
vs
FREQUENCY


Figure 11

## TYPICAL CHARACTERISTICS



Figure 12


Figure 14

MAXIMUM NEGATIVE PEAK OUTPUT VOLTAGE VS OUTPUT CURRENT


Figure 13
TRANSIENT JUNCTION-TO-AMBIENT THERMAL IMPEDANCE $\dagger$
vs
ON TIME


Figure 15
$\dagger$ d = duty cycle

## TYPICAL CHARACTERISTICS



Figure 16


Figure 18

SUPPLY CURRENT
vs
FREE-AIR TEMPERATURE


Figure 17


Figure 19

## TYPICAL CHARACTERISTICS



Figure 20


Figure 22

VOLTAGE FOLLOWER LARGE-SIGNAL PULSE RESPONSE


Figure 21
OUTPUT IMPEDANCE
VS FREQUENCY


Figure 23

## APPLICATION INFORMATION

## circuit for mains-line driver over 40-kHz-to-90-kHz utility band

The following application is a circuit for a mains-line driver over $40-\mathrm{kHz}-\mathrm{to}-90-\mathrm{kHz}$ utility band and is based around the European standard (EN56065-1) describing utility and consumer applications. This example shows a possible implementation for differential transmission on the mains line. This applications circuit is designed around the requirements of a domestic electricity meter operating over a utility band of 40 kHz to 90 kHz . A dual-rail power supply of $\pm 5 \mathrm{~V}$ is used for this design example to limit device power dissipation. The same design principles, however, can be applied to other applications.

## frequency band

The frequency band for utility applications extends over an enormous range from 3 kHz to 95 kHz . In order to have a coupling network that is economical and implemented with readily available components, this circuit is designed for a subband from 40 kHz to 90 kHz .

This subband is sufficiently wide to support multichannel operation; i.e., 10 channels of 5 kHz width or more if the channel widths are smaller. To avoid transmission spillover into the next band, a guard band of 5 kHz is allowed. The upper frequency of this circuit is set to 90 kHz , and the lower frequency is chosen for an economical coupling network and still has sufficient bandwidth to support multichannel operation.

## output drive

The impedance of the mains network at these signalling frequencies is relatively low ( $<1 \Omega$ to $30 \Omega$ ). This circuit has been designed to drive a $4-\Omega$ mains line over the $40-\mathrm{kHz}-\mathrm{to}-90-\mathrm{kHz}$ bandwidth.
The signalling impedance of the mains network fluctuates as different loads are switched on during the day or over a season, and it is influenced by many factors such as:

- Localized loading from appliances connected to the mains supply near to the connection of the communication equipment; e.g., heavy loads such as cookers and immersion heaters and reactive loads such as EMC filters and power factor correctors
- Distributed loading from consumers connected to the same mains cable, where their collective loading reduces the mains signalling impedance during times of peak electricity consumption; e.g., meal times
- Network parameters; e.g., transmission properties of cables and the impedance characteristics of distribution transformers and other system elements
With such a diversity of factors, the signalling environment fluctuates enormously, irregularly, and can differ greatly from one installation to another. The signalling system should be designed for reliable communications over a wide range of mains impedances and signalling conditions. Consequently, the transmitter must be able to drive sufficient signal into the mains network under these loading conditions.

The TLE2301 amplifier has 1-A output drive capability with short-circuit protection; hence, it adequately copes with the high current demands required for implementing mains signalling systems.

## 3-state facility

When transmitting, the transmitter appears as a low-impedance signal source on the mains network. If transmitters are left in the active mode whether transmitting or not and a large number of transmitters are installed in close proximity, their combined loading would reduce the mains impedance to unacceptable levels. Not only would each transmitter need to drive into an extremely low mains impedance, but signals arriving from distant transmitters would be severely attenuated.
To overcome this problem, the transmitters need to present a high impedance to the mains network when they are not transmitting. The mains network is then only loaded by a few transmitters at any one time, and the mains signalling impedance is not adversely affected.

## APPLICATION INFORMATION

## 3-state facility (continued)

The TLE2301 incorporates an output 3-state facility, removing the need for additional circuitry to achieve this function. In addition, the TLE2301 has a low standby current in the 3-state mode, making it ideal for applications where low power consumption is also essential.

## circuit configuration

The design methodology is to minimize power dissipation in the TLE2301 by maximizing the use of the available output voltage swing of the amplifier. The amplifier's output can swing to within 2 V of the supply rail before saturation begins. With a chosen supply of $\pm 5 \mathrm{~V}$, the maximum peak-to-peak voltage swing is 6 V . To ensure that the amplifier's output is not likely to clip under heavy loads, the maximum output voltage swing has been reduced by 0.5 V , giving a usable peak-to-peak output voltage swing of 5.5 V .
It is assumed that the input signal to the transmitter stage has a peak-to-peak amplitude of 2.8 V ( 1 Vrms ) as might be expected if the transmission signal is digitally synthesized by circuitry operating solely from the $5-\mathrm{V}$ supply. The gain of the amplifier stage is appropriately set to:

$$
\begin{aligned}
\text { Gain } & =\frac{\text { peak-to-peak output voltage swing }}{\text { peak-to-peak input voltage }} \\
& =\frac{5.5 \mathrm{~V}}{2.8 \mathrm{~V}} \\
& =1.96
\end{aligned}
$$

An inverting amplifier configuration is chosen for this example, as the input signal source is assumed to have a relatively low impedance in relation to the gain-setting resistors.


Figure 24. Full-Circuit Diagram for Utility Band
A noninverting amplifier configuration could be used when the input signal needs to be terminated with high impedance, but the user should take care that the amplitude of the input signal does not exceed the common-mode input range ( $-4 \mathrm{~V}<\mathrm{V}_{I C M}<1.8 \mathrm{~V}$ at $\mathrm{V}_{\mathrm{CC}}= \pm 5 \mathrm{~V}$ ) for low-gain implementations.

## APPLICATION INFORMATION

## component calculations

The following sections contain the calculations for input capacitors, gain resistors, coupling network, coupling capacitors, transformer-leakage inductance, series resistors, decoupling, and frequency compensation.

## input capacitor

The incoming signal is ac coupled to remove any incoming dc offset and to provide only unity gain for the amplifier's input offset voltage. The value of 100 nF is chosen for this input capacitor as it has very little influence on the amplifier's signal gain over the frequency band.

## gain resistors

The gain-setting resistors are chosen for a gain of 1.96 ; i.e., choosing:

$$
\begin{aligned}
\text { Gain } & =\frac{R_{F}}{R_{I}} \\
R_{F} & =4.7 \mathrm{k} \Omega \text { and } R_{I}=2.4 \mathrm{k} \Omega \\
& =\frac{4.7 \mathrm{k} \Omega}{2.4 \mathrm{k} \Omega} \\
& =1.96
\end{aligned}
$$

The resistor values are low enough to ensure that the circuit does not suffer from stray capacitance and signal pick-up problems but not too low as to significantly load the mains impedance when the amplifier is in its high-impedance state.

## coupling network

The function of the line interface is to provide isolation from the mains supply while coupling the communication signals onto the mains network. As the mains voltage is large in comparison with the communication signals, the mains voltage needs to be isolated from the electronic circuitry. The simple coupling network limits the current flowing from the mains supply as well as providing a convenient point at which to implement the safety isolation barrier between the mains supply and the communications circuitry. The transformer can easily achieve an isolation of 4 kV between primary and secondary windings, and the capacitor provides the low frequency roll-off to impede the mains voltage.
The transformer has two other useful properties. First, the turns ratio can be selected to provide efficient power transfer between the TLE2301 amplifier and the mains network. Second, the transformer possesses leakage inductance that can be tuned with the coupling capacitor to form a band-pass filter.
By altering the turns ratio, the power dissipated in the TLE2301 can be reduced while maintaining the required voltage levels on the mains line. A turns ratio of $1.67: 1$ was selected in this design to apply a $120-\mu \mathrm{dBV}$ signal onto the mains line. The calculation for the turns ratio is not straightforward due to the presence of numerous complex impedances. The simplest method for deriving the turns ratio is to model the circuit with an analog simulation program such as $\mathrm{PSpice}^{\mathrm{TM}}$. It is from these simulations that the $1.67: 1$ turns ratio has been selected.

## APPLICATION INFORMATION

## coupling capacitor

With such a wide frequency band, the quality factor of the coupling filter needs to be low in order to avoid unacceptably large attenuation at the band edges and to achieve a good coupling performance that is insensitive to a wide range of loads. For a band-pass filter of this configuration, the quality factor is proportional to the reciprocal of the coupling capacitance. For low $Q$, the value of $C_{C}$ needs to be large.

$$
\begin{aligned}
& \mathrm{Q}=\text { quality factor and } \mathrm{C}_{\mathrm{C}}=\text { coupling capacitor } \\
& \mathrm{Q} \propto \frac{1}{\mathrm{C}_{C}}
\end{aligned}
$$

Counterbalancing this need for a large value of $\mathrm{C}_{\mathrm{C}}$ creates two more considerations. First, the capacitance should not be so large as to allow significant $50-\mathrm{Hz}$ mains current through the transformer ( $\mathrm{I}=2 \times \pi \times \mathrm{f} \times \mathrm{C}_{\mathrm{C}}$ $\times \mathrm{V}$ ). Second, the coupling capacitor is required to meet certain safety standards. The coupling capacitor is actually an RFI-suppression capacitor that has been designed by the manufacturers to provide an adequate level of protection when connected across the various conductors of the mains supply (consult the UL1283 or UL1414 standards for RFI capacitors). These types of capacitors can be expensive, physically large, restricted in capacitance value, and limited in the number of manufacturers.
As a reasonable compromise between all these factors, a coupling capacitor of 470 nF is chosen. This value is multisourced, moderately priced, limits the mains current through the transformer to less than 36 mArms , and has sufficient capacitance to form the desired low-Q filter.

## transformer leakage inductance

The transformer leakage inductance, inherent to the transformer, can be used to form an LC band-pass filter. If the capacitor alone is used to couple onto the mains network, its capacitance value needs to be quite large for it to have a reasonably low reactance at the signalling frequencies. Forming an LC filter greatly reduces the value of capacitor required. The center frequency of the filter is not the same as the midband frequency of 65 kHz . Band-pass filters show a symmetrical shape only when plotted against the logarithm of frequency, so the center frequency $\left(f_{0}\right)$ is given by the following formula:

$$
\begin{aligned}
\mathrm{f}_{0} & =\sqrt{\mathrm{f}_{\text {lower }} \times \mathrm{f}_{\text {upper }}} \\
& =\sqrt{(40 \times 90)} \mathrm{kHz} \\
& =60 \mathrm{kHz}
\end{aligned}
$$

The leakage inductance of the transformer, as viewed from the winding connected to the coupling capacitor, is derived from $2 \pi \mathrm{f}_{\mathrm{O}}=1 / \sqrt{\mathrm{LC}}$. The required leakage inductance of the transformer is:

$$
\begin{aligned}
L & =\frac{1}{\left(2 \pi f_{\mathrm{O}}\right)^{2} \times \mathrm{C}_{\mathrm{C}}} \\
& =\frac{1}{(2 \pi \times 60 \mathrm{kHz})^{2} \times 470 \mathrm{nF}} \\
& =15 \mu \mathrm{H}
\end{aligned}
$$



Figure 25. Band-Pass Coupling Filter

## APPLICATION INFORMATION

## series resistor

The series resistor, $\mathrm{R}_{\mathrm{S}}$, is included to limit the turn-on current, the amplifier's offset current, and the signalling current through the filter. With dual supply rails, there is always a potential problem of large turn-on currents as the amplifier powers up. If one supply rail turns on before the other, the output of the TLE2301 amplifier could saturate near to the applied supply rail, causing a large current to flow through the transformer winding ( $\mathrm{R}_{\text {winding }}=0.1 \Omega$ for the P2820 transformer). The power supply needs to be of sufficient rating to ensure that its rails could rise to the minimum operating voltage of the amplifier, at which point the amplifier is ensured to have returned to stable operation.
With a series resistor of $3.3 \Omega$ and assuming the output saturates at the maximum peak-to-peak voltage excursion of 3 V , this turn-on current is limited to less than the device's 1-A rating ( ${ }^{\text {transient }}=3 \mathrm{~V} / 3.3 \Omega$ $=0.91 \mathrm{~A}$ ). Further reduction of this turn-on current by raising the value of the series resistor deteriorates the filter's performance into low signalling impedances on the mains network.
Alternatively, this turn-on current could be blocked by means of a series capacitor, but for this frequency band the capacitor has to be large in value ( $\geq 3.3 \mu \mathrm{~F}$ ) so as not to adversely affect the filter. A nonpolarized capacitor of this value is relatively expensive, and the resistor is still required to fulfill other functions.
Another way of preventing overcurrent at power up is to use the TLE2301 3-state mode. As the TRS2 control line is intended to be tied to the microprocessor's $0-\mathrm{V}$ rail, the TRS1 control line must be taken high to activate the 3 -state mode, which implies that the positive rail is required to turn on first. Other schemes could be devised to take TRS2 below the $0-\mathrm{V}$ rail until the power supply has stabilized if the negative rail turns on first. Instead of relying on a definite power-supply sequence or elaborate control circuitry, it is simpler to limit the current either with a series resistor or capacitor.
The second function of the series resistor is to limit the dc current flow through the transformer winding due to the dc offset at the amplifier's output, which is caused by its input offset voltage. For a worst case input offset of 20 mV , the output offset is also 20 mV as the dc gain of the circuit is unity. Offsets due to input bias currents are negligible since the values of the gain-setting resistors are low. The dc current through the transformer is therefore less than $7 \mathrm{~mA}(20 \mathrm{mV} / 3.3 \Omega)$. This low level of dc current does not appreciatively increase the power dissipation of the amplifier or noticeably diminish the harmonic performance of the transformer.

The final function of the series resistor is to limit the signalling current in the event that the mains impedance might appear as solely reactive; i.e., without a resistive component. As a rough estimate, the peak signal current from the amplifier is:

$$
\mathrm{I}_{\mathrm{OM}}=\frac{\mathrm{V}_{\mathrm{O}(\mathrm{PP})}}{\mathrm{R}_{\mathrm{S}}}=\frac{\left(\frac{5.5 \mathrm{~V}}{2}\right)}{3.3 \Omega}=833 \mathrm{~mA}
$$

where:
$\mathrm{V}_{\mathrm{O}(\mathrm{PP})}=$ Peak-to-peak output voltage swing ${ }^{\mathrm{I}} \mathrm{OM}=$ Peak-output-signalling current from amplifier

## APPLICATION INFORMATION

## series resistor (continued)

Again, the value of the series resistor is sufficient to limit the peak-signal current below the device's maximum rating. This calculation does not take into account other resistive impedances in the signal path, which would further reduce the peak signal current from the amplifier.

## decoupling

Power-supply decoupling for the amplifier is provided by a $220-\mu \mathrm{F}$ electrolytic capacitor and a $100-\mathrm{nF}$ ceramic capacitor per supply rail located close to the supply terminals of the TLE2301 device.
The decoupling capacitors for the negative supply should be connected to a pair of $\mathrm{V}_{\mathrm{CC}}$-terminals ( 4 and 5 or 12 and 13), whichever pair is most convenient from a printed-circuit-board (PCB) layout point of view. In order to minimize parasitic lead inductances, these capacitors should be located as close as possible to the device terminals to which they are connected. As the $\mathrm{V}_{\mathrm{CC}+}$ terminals are not adjacent on the package, the decoupling capacitors should be connected to one terminal with a thick PCB track going to the other terminal.

The $220-\mu \mathrm{F}$ electrolytic capacitor is chosen to provide good decoupling performance (less than $25-\mathrm{mV}$ ripple under the worst-case loading for the utility circuit). This value could be reduced to $100 \mu \mathrm{~F}$ for higher-frequency consumer bands. The level of ripple depends on the source impedance of the power supply and the equivalent series resistance of the chosen decoupling capacitors. The $100-\mathrm{nF}$ ceramic capacitor provides high-frequency decoupling for the amplifier.


Figure 26. Amplifier Decoupling and Compensation

## APPLICATION INFORMATION

## frequency compensation

The TLE2301 amplifier requires one compensation capacitor. However, when driving heavy loads, stability can be increased by connecting $\mathrm{V}_{\text {CC_- terminals }} 10$ and 15 to $\mathrm{V}_{\text {CC_- terminals }} 12$ and 13 and using another capacitor between COMP2 and the outputs. The circuit included in this application has been designed with two compensation capacitors. The component values chosen are:

$$
\begin{aligned}
& \mathrm{C}_{\mathrm{F} 1}=15 \mathrm{pF} \\
& \mathrm{C}_{\mathrm{F} 2}=33 \mathrm{pF}
\end{aligned}
$$

These component values could be adjusted if the amplifier is used for higher-frequency applications.

## power dissipation

The impedance of the mains network fluctuates greatly for many reasons, but its impedance at the supplydistribution transformer is typically very low, less than $1 \Omega$, whereas the mains impedance in a house commonly has a higher value, from $4 \Omega$ to $40 \Omega$. For utility-metering applications, a master transmitter may be sited at the supply-distribution transformer and would need to deliver more power into the mains network than the household transmitter when generating comparable signal amplitudes.

## NE thermally-enhanced dual in-line package

The TLE2301 utilizes the four center terminals of the dual-in-line package (NE) to transfer heat to a copper area on the PCB. A copper area of $1290 \mathrm{~mm}^{2}$ provides a junction-to-ambient thermal impedance, $\mathrm{Z}_{\theta \mathrm{JA}}$, of $34^{\circ} \mathrm{C} / \mathrm{W}$, allowing the device to dissipate up to 1.9 W at $85^{\circ} \mathrm{C}$ for a junction temperature of $150^{\circ} \mathrm{C}$ or up to 1.5 W at $85^{\circ} \mathrm{C}$ for a junction temperature of $135^{\circ} \mathrm{C}$.


NOTE: When $\mathrm{d}=25 \mathrm{~mm}, \mathrm{Z}_{\theta \mathrm{JA}}=34^{\circ} \mathrm{C} / \mathrm{W}$
Figure 27. PCB Heatsink

## APPLICATION INFORMATION

## power dissipation in amplifier

For sinusoidal waveforms, the dissipation in the amplifier, $\mathrm{P}_{\mathrm{AMP}}$, is:

$$
\mathrm{P}_{\mathrm{AMP}}=\left(2 \times \mathrm{v}_{\mathrm{CC}} \times \mathrm{I}_{\mathrm{CC}}\right)+\frac{\left(2 \times \mathrm{v}_{\mathrm{CC}} \times \mathrm{I}_{\mathrm{OM}}\right)}{\pi}-\mathrm{P}_{\mathrm{O}}
$$

where:

$$
\begin{aligned}
{ }^{\mathrm{I}} \mathrm{CC} & =\text { Amplifier's quiescent current } \\
\mathrm{I}_{\mathrm{OM}} & =\text { Peak-output-signalling current from amplifier } \\
\mathrm{P}_{\mathrm{O}} & =\text { Output power consumed by coupling network and load }
\end{aligned}
$$

The power dissipated in the amplifier is minimized if the amplifier's peak output current, $\mathrm{I}_{\mathrm{OM}}$, is minimized. Since the output power consumed by the coupling and load is a function of current and voltage ( $\mathrm{P}_{\mathrm{O}} \approx \mathrm{l}_{\mathrm{O}} \times \mathrm{V}_{\mathrm{O}}$ ), the amplifier's peak output current can be minimized by maximizing the amplifier's output voltage swing.
circuit parts list
The associated parts list is:

| REFERENCE | FIGURE | COMPONENT | DESCRIPTION |
| :---: | :---: | :---: | :---: |
| IC1 | Figure 24, Figure 26 | TLE2301 operational amplifier | Texas Instruments TLE230INE |
| L1 | Figure 24 | 1.67:1, 15- $\mu \mathrm{H}$ leakage transformer | Electronics Techniques P2820 (European manufacturer) |
| $\mathrm{C}_{\mathrm{C}}$ | Figure 24 | 470-nF capacitor | Metalized paper, safety standards UL1414 |
| $\mathrm{Cl}_{1}$ | Figure 24 | 100-nF capacitor | Ceramic, general purpose |
| $\mathrm{C}_{\mathrm{F} 1}$ | Figure 24, Figure 26 | 15-pF capacitor | Ceramic, general purpose |
| $\mathrm{C}_{\mathrm{F} 2}$ | Figure 24, Figure 26 | 39-pF capacitor | Ceramic, general purpose |
| $\mathrm{C}_{\mathrm{D} 1}, \mathrm{C}_{\mathrm{D} 2}$ | Figure 24 | $220-\mu \mathrm{F}, 10-\mathrm{V}$ min capacitors | Aluminum electrolytic, general purpose |
| $\mathrm{C}_{\text {D3 }}, \mathrm{C}_{\text {D4 }}$ | Figure 24 | $100-\mathrm{nF}$ capacitors | Ceramic, general purpose |
| $\mathrm{R}_{\mathrm{F}}$ | Figure 24 | $4.7-\mathrm{k} \Omega$, 0.125-W min resistor | Metal film, general purpose |
| $\mathrm{R}_{1}$ | Figure 24 | $2.4-\mathrm{k} \Omega, 0.125-\mathrm{W}$ min resistor | Metal film, general purpose |
| $\mathrm{R}_{\mathrm{S}}$ | Figure 24 | $3.3-\mathrm{k} \Omega$, 1-W min, resistor |  |
| D1, D2 | Figure 24 | 1N4001 series, 1-A min diodes | General purpose |

## PACKAGING INFORMATION

| Orderable Device | Status ${ }^{(1)}$ | Package <br> Type | Package <br> Drawing | Pins Package <br> Qty | Eco Plan ${ }^{(2)}$ | Lead/Ball Finish | MSL Peak Temp ${ }^{(3)}$ |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| TLE2301INE | ACTIVE | PDIP | NE | 16 | 25 | Pb-Free <br> (RoHS) | CU NIPDAU | N/A for Pkg Type |
| TLE2301INEE4 | ACTIVE | PDIP | NE | 16 | 25 | Pb-Free <br> (RoHS) | CU NIPDAU | N/A for Pkg Type |

${ }^{(1)}$ The marketing status values are defined as follows:
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NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.
${ }^{(2)}$ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS \& no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.
TBD: The Pb-Free/Green conversion plan has not been defined.
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Green (RoHS \& no $\mathbf{S b} / \mathrm{Br}$ ): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine ( Br ) and Antimony (Sb) based flame retardants ( Br or Sb do not exceed $0.1 \%$ by weight in homogeneous material)
${ }^{(3)}$ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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