
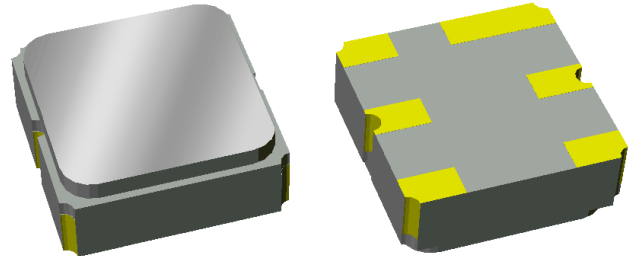


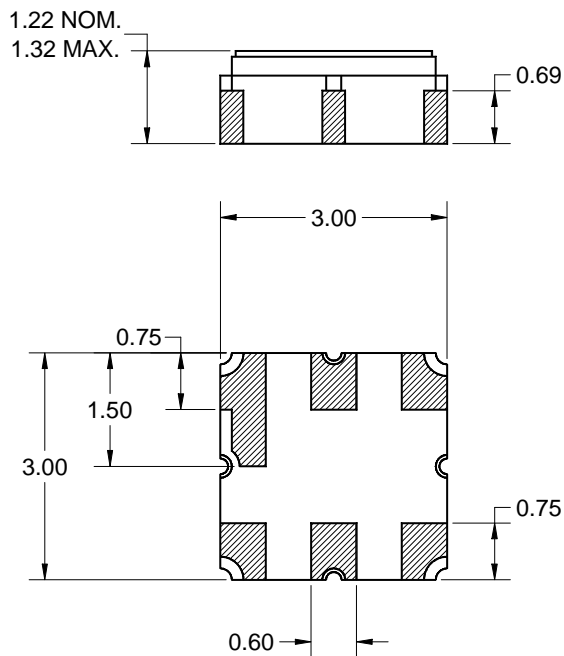
Features

- Usable bandwidth 11 MHz
- Low loss
- High Attenuation
- Single-ended operation
- No impedance matching required for operation at 50 Ω
- Ceramic Surface Mount Package (SMP)
- Hermetic
- RoHS compliant (2002/95/EC), Pb-free 



Package

Surface Mount 3.00 x 3.00 x 1.22 mm
SMP-12

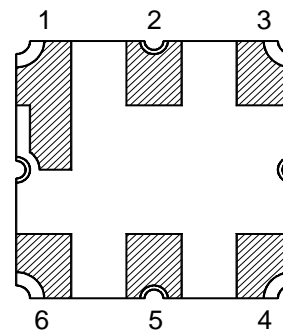


Dimensions shown are nominal in millimeters
All tolerances are ± 0.15 mm except overall
length and width ± 0.10 mm

Body: Al_2O_3 ceramic
Lid: Kovar, Ni plated
Terminations: Au plating 0.5 - 1.0 μ m,
over a 2 - 6 μ m Ni plating

Pin Configuration

Bottom View



Pin No.	Description
2	Input
5	Output
1,3,4,6	Case Ground

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -40 to +85 °C

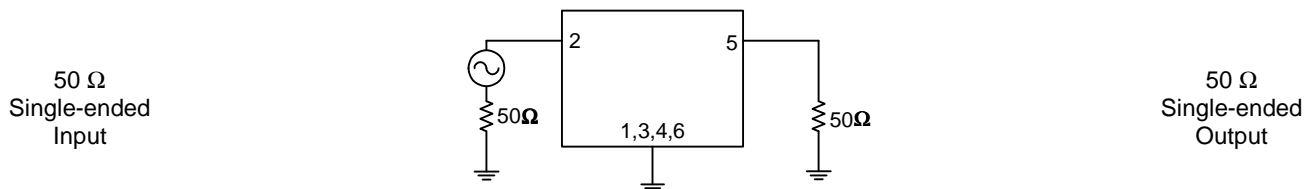
Parameter ⁽³⁾	Minimum	Typical ⁽⁴⁾	Maximum	Unit
Center Frequency	-	751.5	-	MHz
Maximum Insertion Loss 746 - 757 MHz	-	1.54	2.5	dB
Passband Width (relative to Frequency)	11	-	-	MHz
Absolute Attenuation ⁽⁵⁾ 484.5 - 728.0MHz	37	39	-	dB
776.0 - 1000 MHz	40	42	-	dB
Amplitude Variation 746 - 757 MHz	-	0.35	1	dB p-p
Absolute Delay 746 - 757 MHz	-	45	-	ns
Input/Output Return Loss 746 - 757 MHz	10	14	-	dB
Source Impedance (single-ended) ⁽⁶⁾	-	50	-	Ω
Load Impedance (single-ended) ⁽⁶⁾	-	50	-	Ω

Notes:

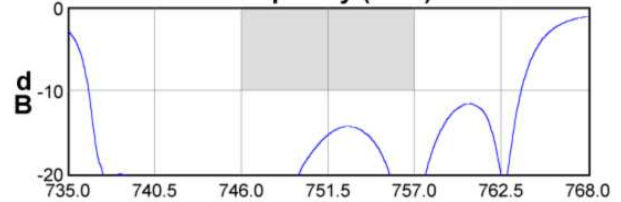
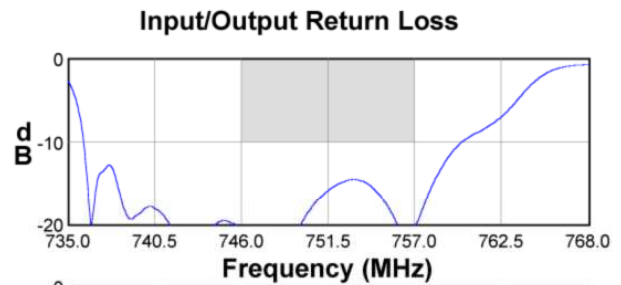
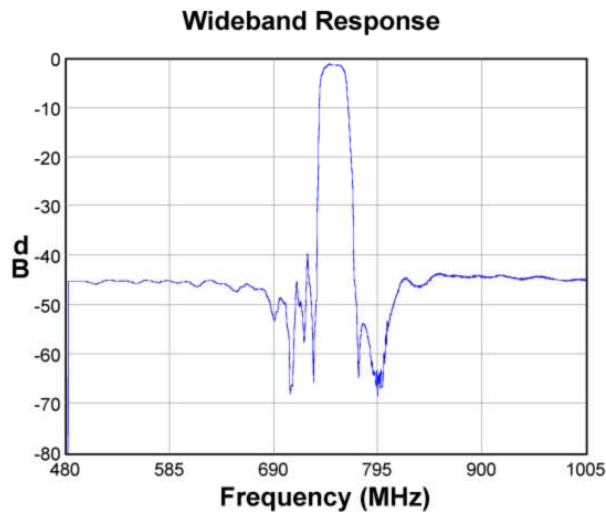
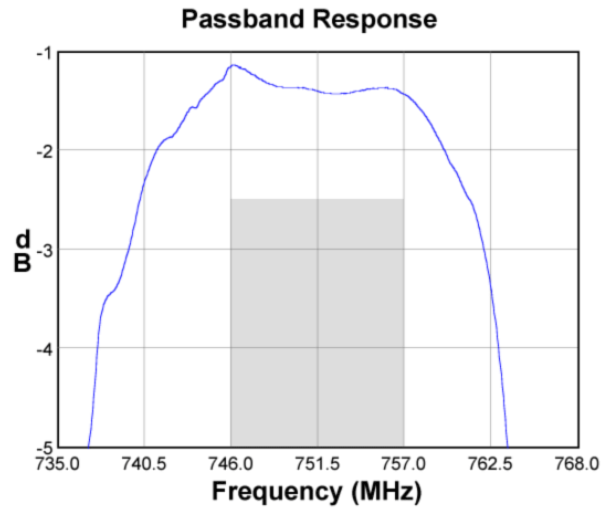
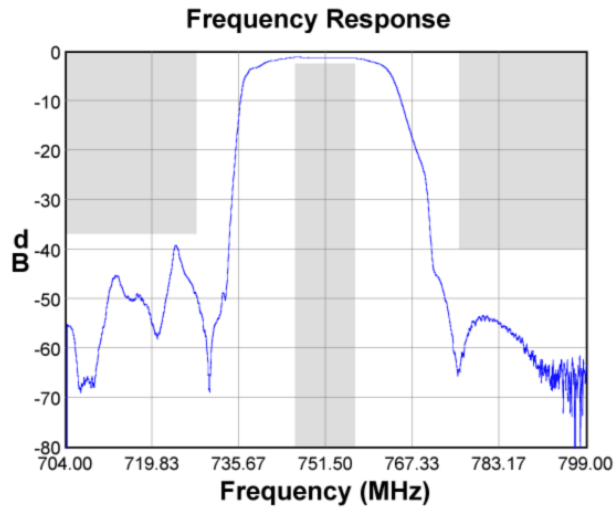
1. All specifications are based on the TriQuint test circuit shown below
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Typical values are based on average measurements at room temperature
5. Relative to zero dB
6. This is the optimum impedance in order to achieve the performance shown

Test Circuit:

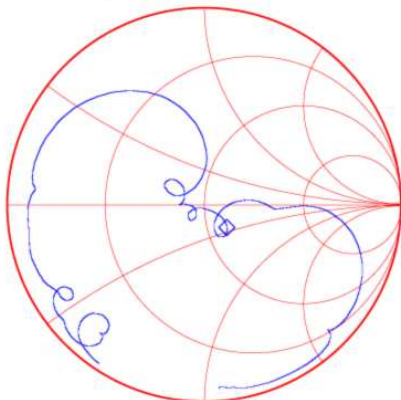
Actual matching values may vary due to PCB layout and parasitics



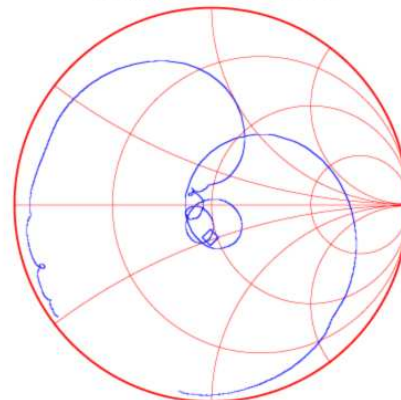
Typical Performance (at room temperature)



Input Smith Chart

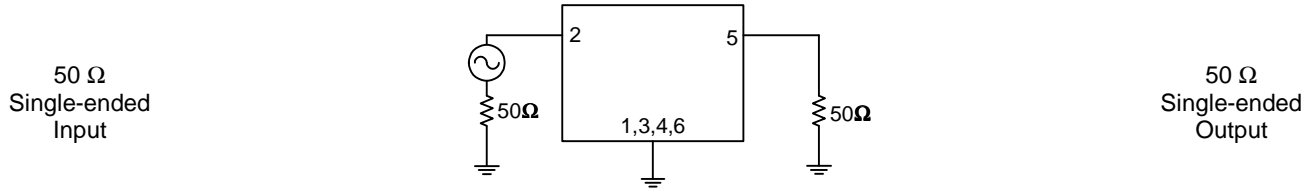


Output Smith Chart

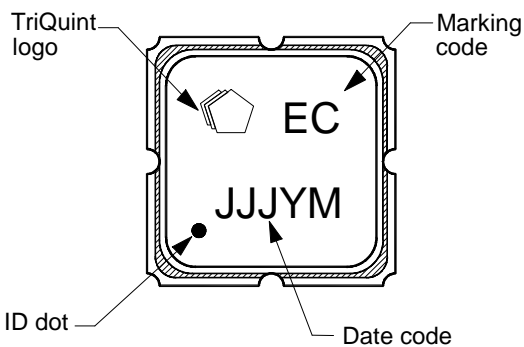


Matching Schematics

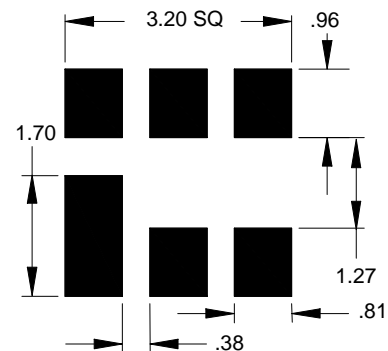
Actual matching values may vary due to PCB layout and parasitics



Marking



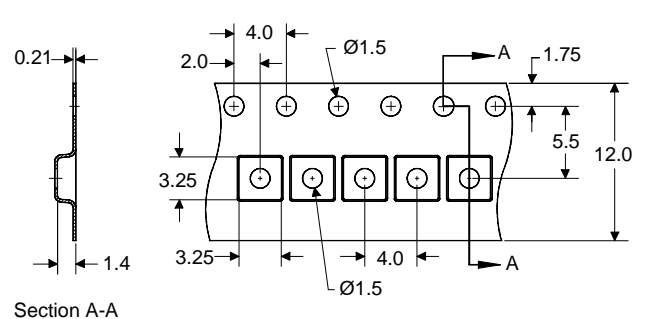
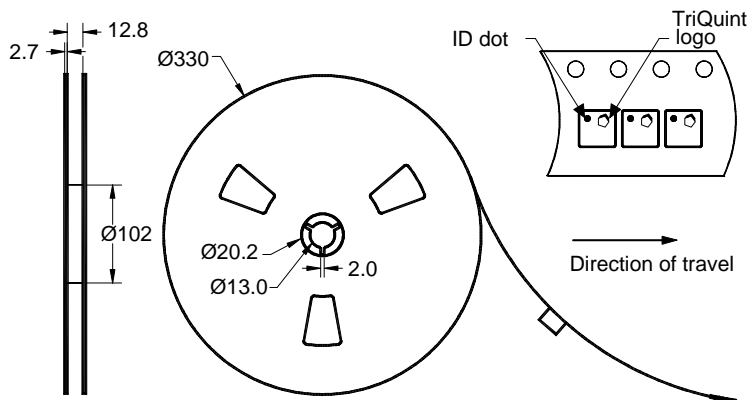
PCB Footprint



The date code consists of: day of the current year (Julian, 3 digits), Y = last digit of the year and M = manufacturing site code

This footprint represents a recommendation only
Dimensions shown are nominal in millimeters

Tape and Reel




Dimensions shown are nominal in millimeters
Packaging quantity: 5000 units/reel

Maximum Ratings


Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T _{stg}	-40	+85	°C
Input Power	P _{in}	-	+20	dBm

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

Solderability

- Compatible with JESD22-B102, Pb-free process, 260C peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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