

ISL9V3036D3S / ISL9V3036S3S / ISL9V3036P3

EcoSPARK® 300mJ, 360V, N-Channel Ignition IGBT

General Description

The ISL9V3036D3S, ISL9V3036S3S, and ISL9V3036P3 are the next generation IGBTs that offer outstanding SCIS capability in the space saving D-Pak (TO-252), as well as the industry standard D²-Pak (TO-263) and TO-220 plastic packages. These devices are intended for use in automotive ignition circuits, specifically as a coil drivers. Internal diodes provide voltage clamping without the need for external components.

EcoSPARK® devices can be custom made to specific clamp voltages. Contact your nearest Fairchild sales office for more information.

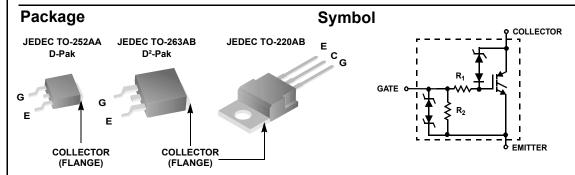
Formerly Developmental Type 49442

Applications

- · Automotive Ignition Coil Driver Circuits
- · Coil- On Plug Applications

Features

- Industry Standard D²-Pak package
- SCIS Energy = 300mJ at T_J = 25°C
- · Logic Level Gate Drive



Device Maximum Ratings T_{.i} = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
BV _{CER}	Collector to Emitter Breakdown Voltage (I _C = 1 mA)	360	V	
BV _{ECS}	Emitter to Collector Voltage - Reverse Battery Condition (I _C = 10 mA)	24	V	
E _{SCIS25}	$T_J = 25$ °C, $I_{SCIS} = 14.2A$, $L = 3.0$ mHy	300	mJ	
E _{SCIS150}	$T_J = 150$ °C, $I_{SCIS} = 10.6$ A, L = 3.0 mHy	170	mJ	
I _{C25}	Collector Current Continuous, At T _C = 25°C, See Fig 9	21	Α	
I _{C110}	Collector Current Continuous, At T _C = 110°C, See Fig 9	17	Α	
V _{GEM}	Gate to Emitter Voltage Continuous	±10	V	
P _D	Power Dissipation Total T _C = 25°C	150	W	
	Power Dissipation Derating T _C > 25°C	1.0	W/°C	
T _J	Operating Junction Temperature Range	-40 to 175	°C	
T _{STG}	Storage Junction Temperature Range	-40 to 175	°C	
TL	Max Lead Temp for Soldering (Leads at 1.6mm from Case for 10s)	300	°C	
T _{pkg}	Max Lead Temp for Soldering (Package Body for 10s)	260	°C	
ESD	Electrostatic Discharge Voltage at $100pF$, 1500Ω			

V3036D V3036S V3036P)		Package	Reel Size			Width		Quantity	
		ISL9V3036D3ST	TO-252AA	330mm		16mm			2500	
V3036P	3	ISL9V3036S3ST	TO-263AB	330mm		24mm			800	
V3036P		ISL9V3036P3	TO-220AA	Tube		N/A			50	
V3036D		ISL9V3036D3S	TO-252AA	Tube		N/A			75	
V3036S		ISL9V3036S3S	TO-263AB	Tube		1	N/A		50	
lectrica Symbol	al Characteristics T _J = 25°C unl			less otherwise noted Test Conditions		Min	Тур	Max	Unit	
	Char	acteristics	.	1001 0011	uniono		.,,,,	III WA	- J Omic	
BV _{CER}	Collector to Emitter Breakdown Voltage			I_C = 2mA, V_{GE} = 0, R_G = 1KΩ, See Fig. 15 T_A = -40 to 150°C		330	360	390	V	
BV _{CES}	Collector to Emitter Breakdown Voltage			$I_C = 10 \text{mA}, V_{GE} = 0,$ $R_G = 0, \text{ See Fig. 15}$ $T_J = -40 \text{ to } 150^{\circ}\text{C}$		350	380	410	V	
BV _{ECS}	Emitter to Collector Breakdown Voltage			$I_C = -75 \text{mA}, V_{GE} = 0 \text{V},$ $T_C = 25 ^{\circ} \text{C}$		30	-	-	V	
BV _{GES}	Gate	e to Emitter Breakdo	wn Voltage	$I_{GES} = \pm 2mA$		±12	±14	-	V	
I _{CER}	Colle	Collector to Emitter Leakage Current		V _{CER} = 250V,	T _C = 25°C	-	-	25	μA	
				R_G = 1KΩ, See Fig. 11	T _C = 150°C	-	-	1	mA	
I _{ECS}	Emit	tter to Collector Lea	V _{EC} = 24V, See		-	-	1	mA		
			Fig. 11 T ₀	T _C = 150°C	-	-	40	mA		
R ₁	Series Gate Resistance					- 10K	70	- 26K	Ω	
V _{CE(SAT)}	_	Characteristics Collector to Emitter Saturation Voltage		I _C = 6A, V _{GE} = 4V	T _C = 25°C,	-	1.25	1.60	V	
V _{CE(SAT)}	Colle	Collector to Emitter Saturation Voltage		$I_{C} = 10A,$ $V_{GE} = 4.5V$	See Fig. 3 T _C = 150°C, See Fig. 4	-	1.58	1.80	V	
V _{CE(SAT)}	Colle	Collector to Emitter Saturation Voltage		I _C = 15A, V _{GE} = 4.5V	T _C = 150°C	-	1.90	2.20	V	
ynamic (Char	acteristics								
$Q_{G(ON)}$	Gate Charge		I _C = 10A, V _{CE} = 12V, V _{GE} = 5V, See Fig. 14		-	17	-	nC		
V _{GE(TH)}	Gate	e to Emitter Thresho	old Voltage	I _C = 1.0mA,	T _C = 25°C	1.3	-	2.2	V	
				V _{CE} = V _{GE,} See Fig. 10	T _C = 150°C	0.75	-	1.8	V	
V_{GEP}	Gate	e to Emitter Plateau	Voltage	I _C = 10A,	V _{CE} = 12V	-	3.0	-	V	
witching		aracteristics		_		•	_			
$t_{d(ON)R}$	_	Current Turn-On Delay Time-Resistive		$V_{CE} = 14V, R_{L} = 1\Omega,$		-	0.7	4	μs	
t _{rR}		ent Rise Time-Resi	$V_{GE} = 5V$, $R_G = 1K\Omega$ $T_J = 25^{\circ}C$, See Fig. 12		-	2.1	7	μs		
t _{d(OFF)L}	_	Current Turn-Off Delay Time-Inductive		$V_{CE} = 300V, R_{L} = 500\mu H,$		-	4.8	15	μs	
t _{fL}				$V_{GE} = 5V, R_G = 1K\Omega$ $T_J = 25^{\circ}C, See Fig. 12$		-	2.8	15	μs	
SCIS				$T_J = 25^{\circ}C, L = 3$ $R_G = 1K\Omega, V_{GH}$		-	-	300	mJ	
hermal C	har	acteristics								

Typical Performance Curves

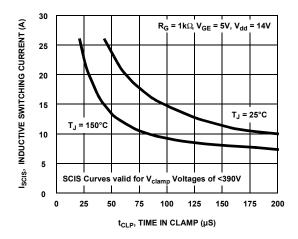


Figure 1. Self Clamped Inductive Switching Current vs Time in Clamp

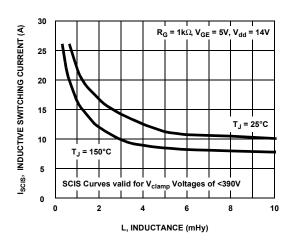


Figure 2. Self Clamped Inductive Switching Current vs Inductance

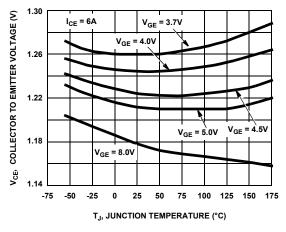


Figure 3. Collector to Emitter On-State Voltage vs Junction Temperature

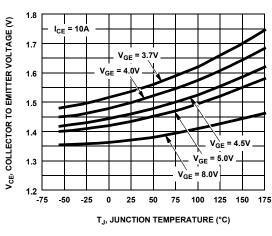


Figure 4. Collector to Emitter On-State Voltage vs Junction Temperature

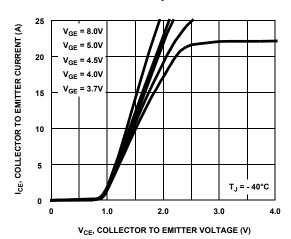


Figure 5. Collector to Emitter On-State Voltage vs Collector Current

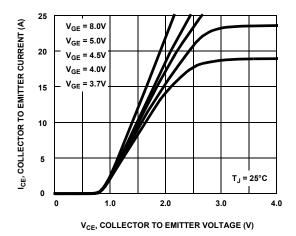


Figure 6. Collector to Emitter On-State Voltage vs Collector Current

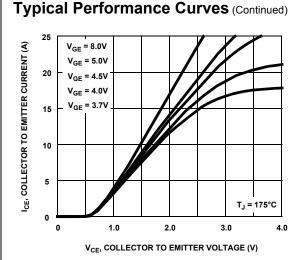


Figure 7. Collector to Emitter On-State Voltage vs Collector Current

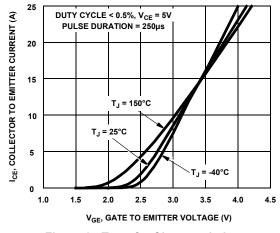


Figure 8. Transfer Characteristics

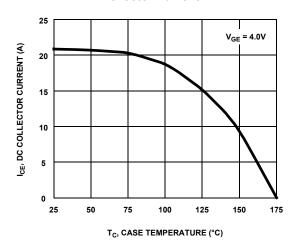


Figure 9. DC Collector Current vs Case Temperature

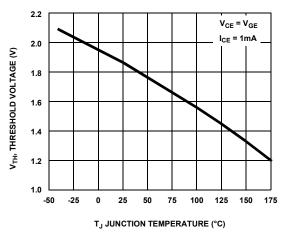


Figure 10. Threshold Voltage vs Junction Temperature

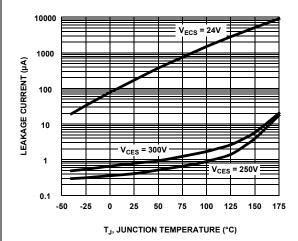


Figure 11. Leakage Current vs Junction Temperature

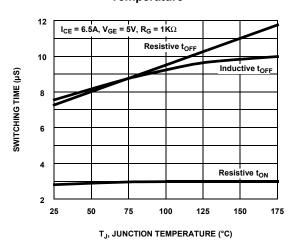


Figure 12. Switching Time vs Junction Temperature

Typical Performance Curves (Continued) 1600 FREQUENCY = 1 MHz 1200 C_{IES} C_{RES} C_{OES}

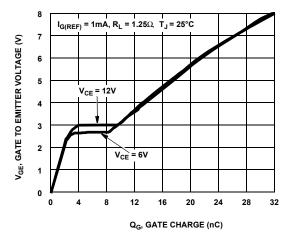


Figure 13. Capacitance vs Collector to Emitter Voltage

V_{CE}, COLLECTOR TO EMITTER VOLTAGE (V)

10

15

20

0

Figure 14. Gate Charge

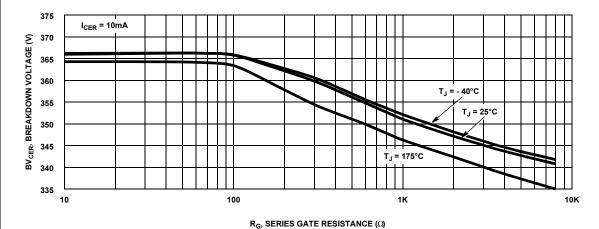


Figure 15. Breakdown Voltage vs Series Gate Resistance

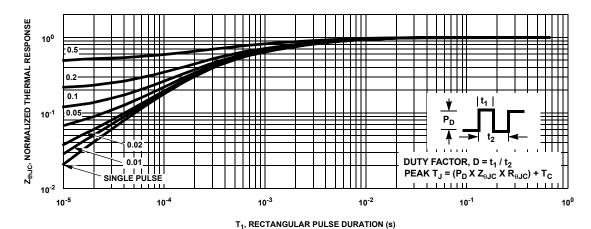
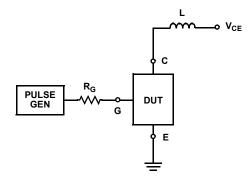


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

Test Circuit and Waveforms



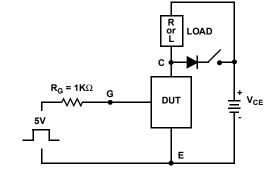
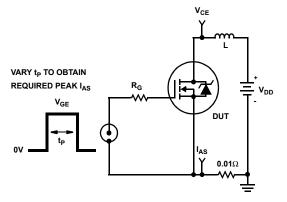
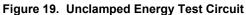


Figure 17. Inductive Switching Test Circuit

Figure 18. $t_{\rm ON}$ and $t_{\rm OFF}$ Switching Test Circuit





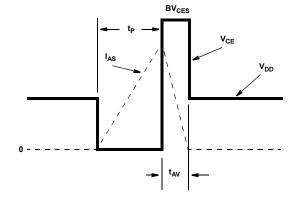


Figure 20. Unclamped Energy Waveforms

SPICE Thermal Model JUNCTION **REV 24 April 2002** ISL9V3036D3S/ ISL9V3036S3S / ISL9V3036P3 CTHERM1 th 6 2.1e -3 CTHERM2 6 5 1.4e -1 RTHERM1 CTHERM1 CTHERM3 5 4 7.3e -3 CTHERM4 4 3 2.1e -1 CTHERM5 3 2 1.1e -1 CTHERM6 2 tl 6.2e +6 6 RTHERM1 th 6 1.2e -1 RTHERM2 6 5 1.9e -1 RTHERM2 CTHERM2 RTHERM3 5 4 2.2e -1 RTHERM4 4 3 6.0e -2 RTHERM5 3 2 5.8e -2 RTHERM6 2 tl 1.6e -3 5 SABER Thermal Model RTHERM3 CTHERM3 SABER thermal model ISL9V3036D3S / ISL9V3036S3S / ISL9V3036P3 template thermal_model th tl thermal_c th, tl ctherm.ctherm1 th 6 = 2.1e - 3ctherm.ctherm2 6 5 = 1.4e -1 ctherm.ctherm3 5 4 = 7.3e -3 RTHERM4 CTHERM4 ctherm.ctherm4 4 3 = 2.2e -1 ctherm.ctherm5 3 2 =1.1e -1 ctherm.ctherm6 2 tl = 6.2e +6 3 rtherm.rtherm1 th 6 = 1.2e -1 rtherm.rtherm2 6 5 = 1.9e -1 rtherm.rtherm3 5 4 = 2.2e -1 RTHERM5 CTHERM5 rtherm.rtherm4 4 3 = 6.0e -2 rtherm.rtherm5 3 2 = 5.8e -2 rtherm.rtherm6 2 tl = 1.6e -3 2 RTHERM6 CTHERM6 CASE





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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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