NCP2991 - 1.35 Watt Audio Power Amplifier with Selectable Fast Turn On Time Evaluation Board User's Manual



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EVAL BOARD USER'S MANUAL

Goal of Document

The purpose of the document is to explain how to use the NCP2991 Evaluation Board User's Manual.

Applicable & Reference Documents

NCP2991/D: NCP2991 Datasheet

Overview

The NCP2991 is an audio power amplifier designed for portable communication device applications such as mobile phone applications. The NCP2991 is capable of delivering 1.35 W of continuous average power to an 8.0 Ω BTL load from a 5.0 V power supply, and 1.1 W to a 4.0 Ω BTL load from a 3.6 V power supply.

The NCP2991 provides high quality audio while requiring few external components and minimal power consumption. It features a low-power consumption shutdown mode, which is achieved by driving the SHUTDOWN pin with logic low.

The NCP2991 contains circuitry to prevent from 'pop and click' noise that would otherwise occur during turn-on and turn-off transitions. It is a zero pop noise device when a single ended or a differential audio input is used.

For maximum flexibility, the NCP2991 provides an externally controlled gain (with resistors). In addition, it integrates 2 different Turn On times (15 ms or 30 ms) adjustable with the TON pin.

Due to its superior PSRR, it can be directly connected to the battery, saving the use of an LDO.

This device is available in a 9-Pin Flip-Chip CSP (Lead-Free).



Figure 1. NCP2991FCT2GEVB Board Picture



BOARD SCHEMATIC





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PCB LAYOUT



Figure 3. Evaluation Board Layout - Top View



Figure 4. Evaluation Board Layout – Bottom View

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SINGLE-ENDED OR DIFFERENTIAL CONFIGURATION



Figure 5. Single-ended Configuration

Connect J20, disconnect J18 and J19 to use the evaluation board in Single-ended configuration.



Figure 6. Differential Configuration

Connect J18 and J19, disconnect J20 to use the demoboard in Differential configuration.

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Table 1. BILL OF MATERIALS

Item	Part Description	Ref.	PCB Footprint	Manufacturer	Manufacturer Reference
1	NCP2991 Audio Amplifier			ON Semiconductor	NCP2991
2	SMD Resistor 24 K Ω	R1, R2, R5, R6	0805	Panasonic	ERJ-6GEYJ243V
3	SMD Resistor 150 K Ω	R3, R4	0805	Panasonic	ERJ-6GEYJ154V
4	Ceramic Capacitor 100 nF 16 V X5R	C1, C5	0805	Murata	GRM21BR71E104KA01
5	Ceramic Capacitor 1 μ F 16 V X5R	C3, C4	0805	Murata	GRM216R61C105KA88
6	Jumper Header Vertical Mount, 2 positions, 100 mils	J2, J6, J8, J13, J14, J16	100 mils	Tyco Electronics / AMP	58266290
7	I/O Connector, 2 positions	J1, J5	200 mils	Phoenix Contact	1757242
8	Jumper Connector	J7, J15	400 mils	Harwin	D3082B01
9	Not Mounted	J3, J4			

TEST PROCEDURE

Output Power:

- 1. Set Vp = 5 V to power supply connector (J1).
- 2. Set an 8 Ω load (resistance) on the output connector (J5).
- 3. With the function generator, set a single ended signal at 1 kHz and 0.5 Vrms input signal on the negative input. Apply this signal either on J2 or J3 connector. As R1 = R2 = 24 k, VO1 will see 0.5 Vrms. As VO1 signal is inverted by the second amplifier, VO2 will also see 0.5 Vrms with 180° delay. Thus, the load between VO1 and VO2 will see 1 Vrms.
- 4. Place 2 oscilloscope probes on the output (differential measurement). You should get 1 Vrms output signal with a "perfect sine wave". That is to say no clipping at the minima and maxima of the sine wave.

Quiescent Current:

Check the quiescent current. Place an 8 Ω load, no input signal. Vp set to 5 V and J6 closed. You should measure around 1.9 mA.

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