A 70 W Low Standby Power Supply with the NCP120x Series Evaluation Board User's Manual



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EVAL BOARD USER'S MANUAL

Introduction

The NCP1200 represents one of the cheapest solutions to build efficient and cost-effective Switch-Mode Power Supplies (SMPS). As this design example will show, the part definition does not confine the component in low-power applications only, but it can actually be used in Flyback and Forward supplies for virtually any output power. The below example depicts a universal mains 90–260 VAC power supply delivering 16.5 V @ 4.5 A.

Beside its ease of implementation, the NCP1200 excels in true low standby power designs. This application note details how an amazing standby power of less than 100 mW can be reached at high line with a nominal 70 W board.

DSS or Not DSS?

The Dynamic Self Supply (DSS) lets you directly drive MOSFETs from the high-voltage rail. This option brings you several advantages, as stated below:

- True overload detection: with UC384X-based systems, the switching oscillations are stopped in case the Vcc line drops below a given Undervoltage Lockout level (UVLO). This principle considers a good coupling between the primary auxiliary winding and the power secondary winding. Unfortunately, leakage elements often degrade this coupling and you only can detect true short-circuit (when Vout is close to zero) and not overload conditions. Thanks to the DSS, the NCP1200 does not need an auxiliary information to sense an overload condition. By detecting a current setpoint pushed to the maximum, the internal logic takes the decision to enter into a safe burst operation, auto-recovering when the default leaves. Precise overload levels can thus be implemented.
- Guaranteed operation at low output levels: the Vcc
 delivered by an auxiliary winding moves with the
 power output level because a coupling exists between
 both windings. When the supply is used in battery
 charging applications, Vout can move depending on the
 charging state. That is to say, when the battery is nearly
 empty, its voltage can be close to zero, forcing Vout at

this level. Thanks to the natural secondary / auxiliary reflection, the primary auxiliary winding cannot maintain a sufficient voltage on the control IC: Vcc collapses and puts the controller in trouble, probably entering an hiccup mode, similar to that of a startup sequence. DSS being decoupled from Vout, you never see that phenomenon.

As you can see, the DSS offers interesting features but, on the other hand, it can sometimes compromise key design parameters. Standby power and power dissipation are one of these:

- Standby power: the DSS standby power contribution can easily be evaluated: $V_{HV} \times I_{avg}$ with I_{avg} , the current consumption taken by the controller and V_{HV} , the high-voltage supply rail. If I_{avg} equals 1 mA, then we have a standby power of 350 mW at a 350 VDC voltage rail. Tricks exist to slightly reduce it, like the half-wave diode, but you will only gain between 20–30%.
- Power dissipation: as stated above, all the current consumed by the IC is seen through pin8. This is due to the self-adaptive feature of the DSS. Should the IC current move up or down, the DSS duty-cycle will automatically adjust to deliver it. The controller current depends on the internal IC consumption, but also on the type of MOSFET connected to the output. It therefore important to assess the total current drawn from the HV rail and checks the right compatibility with the package type. All details are given in the NCP1200 dedicated data sheet and the application note AND8023/D.

As a result, the answer lies behind your design constraints. If you would like to have a precise Over Current Protection (OCP) trip point while driving a moderate size MOSFET, DSS can be a good choice, provided low standby power is not an absolute necessity. In our case, we want to drive a large MOSFET for a better efficiency but we need to reach the lowest possible standby power. We will thus adopt an auxiliary winding configuration to permanently disable the DSS. Solutions to various combinations of these constraints are described in the application note "Tips and Tricks for the NCP1200," document number AND8069/D.



Figure 1. Evaluation Board Picture

Self-Powering the Controller in Standby

An auxiliary winding does not usually cause any self-supply problem with a continuous pulses flow. In standby, whatever implemented frequency reduction techniques (e.g. skip or frequency foldback), the recurrence between pulses can become very low. By definition, the feedback loop manages to keep the energy content in each burst high enough to maintain the nominal output voltage. However, on the auxiliary side, it can be difficult to keep the Vcc above the controller's UVLO. Remember, to permanently disable the DSS, you need to guarantee a level above VccON max. which is 11 V for the NCP1200. Failure to do this will re-activate the DSS in no-load conditions and standby power will be degraded. Figure 2 offers a view of a typical bunch of pulses captured in standby at a 127 VDC input voltage.

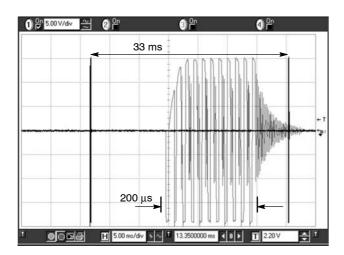


Figure 2. A Bunch of Auxiliary Pulses Captured
While the Supply Operates at No-Load
(Vin = 127 VDC)

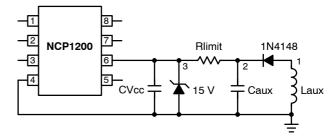


Figure 3. The Auxiliary Is Clamped to Avoid Exceeding the 16 V Maximum Rating

As we previously stated, we want to deliver 70 W with a 16.5 V output level. The maximum rating for the NCP1200 states a level less than 16 V. As a result, the auxiliary Vcc shall be less than 16V but also above VccON in any conditions to ensure full DSS de-activation. A solution consists in artificially raising the ratio between the power winding and the auxiliary one to ensure adequate supply at no-load. We successfully tested a 0.9 ratio, where the auxiliary output gets clamped by a 15V Zener diode in nominal operation. Figure 3 shows the option.

We measured a Vcc of 11.5 V @ 230 VAC and 12.2 V @ 90 VAC. Rlimit on Figure 3 can easily be adjusted to move these values up or down, depending on the final winding ratios. Care must be taken to avoid over-dissipation of the 15 V Zener diode in nominal conditions.

Power Supply, Element-by-Element Design

Let's first detail the specs of our power supply:

Vin: 90-265 VAC

Vout: 16.8 V @ 4.2 A (Pout = 70 W)

Short-circuit protection Over-voltage protection

Efficiency > 80% Pin = 70 / 0.8 = 87.5

The below sequence details step-by-step the calculation procedure for every component of the power supply.

DC High-Voltage Rail

From these above numbers, we can deduce the level of the high-voltage rail, neglecting the dual Vf drop:

$$V_{HV} max = 265 \cdot \sqrt{2} = 374 VDC$$

$$V_{HV} min = 90 \cdot \sqrt{2} = 127 VDC$$

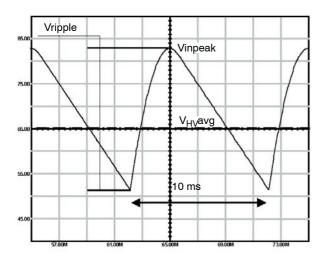


Figure 4. A Typical Ripple Voltage Over the Bulk Capacitor

Bulk Capacitor

Figure 4 portrays the typical waveform captured across a bulk capacitor delivering power to a given charge. To simplify the calculation, we will neglect the charging period and thus consider a total discharge time equal to $1/(2 \cdot \text{Fline})$. From the design characteristics, we can evaluate the equivalent current (Iload) drawn by the charge at the lowest input line condition. Let's us adopt a 40% ripple level, or a 50 V drop from the corresponding Vin_{peak}. To evaluate the equivalent load current (which discharges Cbulk between the peaks), we divide the input power by the average rectified voltage:

Iload =
$$\frac{\text{Pin}}{\text{Vrectavg}} = \frac{\text{Pout}}{\eta \cdot \left(\text{Vpeak} - \frac{\text{Vripple}}{2}\right)}$$
 (1)

≈ 860 mA DC @ 90 VAC input voltage

Thanks to Figure 4 information, we can evaluate the capacitor value which allows the drop from Vpeak down to Vavg – (Vripple/2) to stay within our 50 V target, dV \cdot C = $i_{load} \cdot dt$:

$$Cbulk = \frac{Pout}{2 \cdot \eta \cdot Fline \cdot Vripple \cdot \left(Vpeak - \frac{Vripple}{2}\right)}$$
 (2)

 \geq 171 μF or 180 μF for a normalized value

(Fline = 50 Hz worse case).

Diode Bridge Selection

To select the right rectifiers, it is necessary to know the RMS current flowing through its internal diodes. Prior to reach this final result, we need to evaluate the diode conduction time. From Figure 5, we can see that the diode starts to conduct when V_{AC} in reaches Vmin and stops when reaching Vin_{peak} :

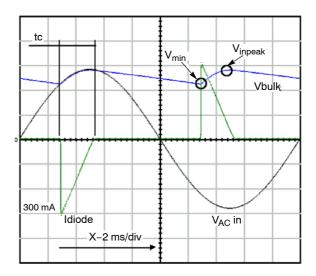


Figure 5. When V_{AC}in Reaches Vpeak, the Diode Stops Conducting

From a mathematical point of view, we can calculate the time V_{AC} in takes to reach Vmin, with Vmin = Vpeak – Vripple:

$$V_{AC}$$
in · $sin(\omega \cdot t) = Vmin$

Since Vpeak is reached at the input sinusoid top (or one fourth of the input period), then the diode conducting time tc is simply:

$$tc = \frac{1}{4 \cdot Fline} - \frac{\sin^{-1}\left(\frac{Vmin}{VACin \cdot \sqrt{2}}\right)}{360 \cdot Fline}$$
(3)

$$\approx$$
 3 ms @ Vin = 90 VAC

During these 3 ms, Vbulk is the seat of a rising voltage equal to Vripple or 50 Vpp. This corresponds to a brought charge O of:

Qbulk =
$$Vripple \cdot Cbulk = 9 mC$$
 (4)

From Figure 5, we can calculate the amount of charge Q drawn from the input by integrating the input current over the diode conduction time:

$$Qin = \int_{0}^{tc} i_{diode}(t).dt$$
 (5)

The expression of $i_{diode}(t)$ is:

$$lpeak \cdot \frac{tc - t}{tc}$$
 (6)

After proper integration, it comes:

$$Qin = \frac{1}{2} \cdot Ipeak \cdot tc$$

If we now equate Qbulk and Qin and solve for Ipeak, it comes:

$$Ipeak = \frac{Qbulk \cdot 2}{tc}$$
 (7)

or 6 A peak. We can now evaluate the RMS current flowing through the diodes:

Irms =
$$\sqrt{\text{Fline} \cdot \int_{0}^{\text{tc}} (i_{\text{diode}}(t))^{2} \cdot dt}$$
= Ipeak \cdot \sqrt{\frac{tc}{3} \cdot 2 \cdot \text{Fline}} (8)

We selected a KBU4J diode bridge (600 V/4 A) for the rectifying function. A small resistor, or best an NTC, can however be put in series to limit the surge current (when you plug the SMPS in the AC outlet) to less than the diode maximum peak current (Ifsm) or what the standard imposes you.

Thanks to these numbers, we compute the apparent power at low line: $1.9 \text{ A} \times 90 \text{ V} = 170 \text{ VA}$ which compared to our 87.5 Watts of active power (neglecting the input diode bridge and Cbulk losses) gives a power factor of:

$$PF = \frac{W}{V \Delta} = 0.51 \tag{9}$$

conform to what we could expect from this kind of offline power supply.

Transformer Calculation

Transformer calculation can be done in several manners: a) you evaluate ALL the transformer parameters, electrical but also physical ones, including wire type, bobbin stack-up etc. b) you only evaluate the electrical data and leave the rest of the process to a transformer manufacturer. We will adhere to the latest option by providing you with a list of potential transformer manufacturers you can use for prototyping and manufacturing. However, as you will discover, designing a transformer for SMPS is an iterative process: once you freeze some numbers, it is likely that they finally appear either over or under estimated. As a result, you re-start with new values and see if they finally fit your needs. To help you speed-up the transformer design, a design-aid spreadsheet is available from the ON Semiconductor web site, www.onsemi.com/pub/NCP1200. Let's start the process with the turn ratio calculation.

Turn Ratio and Output Diode Selection

The primary/secondary turn ratio affects several parameters:

 The drain plateau voltage during the OFF time: the lowest plateau gives room for the leakage inductance spike before reaching the MOSFET's BVdss:

$$Vplateau = \frac{Np}{Ns} \cdot (Vout + Vf) + VinDC_{max}$$
 (10)

• The secondary Peak Inverse Voltage (PIV) is linked to the turn ratio and the regulated output voltage by:

$$PIV = \frac{Ns}{Np} \cdot VinDC_{max} + Vout$$
 (11)

If you lower the plateau voltage, you will increase the reverse voltage the secondary diode must sustain.

With these numbers in mind, you can tweak the turn ratio according to the MOSFET BVdss and the diode maximum reverse voltage. A Schottky diode represents a good choice, especially with a power supply that can possibly enter Continuous Conduction Mode (CCM). The lack of reverse recovery loss and a low forward drop play in favor of this component. However, because of the metal–silicon junction, moderate breakdown voltages are available for a moderate cost. The MBR20100 represents an interesting choice since it welcomes two 100 V Schottky in a TO–220 package. Being in thermal contact, a parallel wiring is possible. The 100 V V_{RRM} lets us calculate the minimum turn ratio we can go down to, keeping an acceptable safety margin:

$$N = \frac{PIV - Vout}{VinDC \max}$$
 (12)

 \rightarrow Np:Ns \leq 1:0.221. A final ratio of 1:0.166 offers an adequate safety margin (Vreverse = 80 V max). The diode's conduction power is evaluating using the following formula:

$$Pdiode_{avg} = Vf \cdot Id_{avg} + Rd \cdot Id_{rms}^{2}$$
(13)

Rather than manually calculating these numbers, we will see later on how a Spice simulator can do the job for us.

Primary Inductance and Peak Current

For AC/DC adapters delivering this amount of power in a small place, it is of common practice to make the power supply enter CCM in the middle of the total AC range (around 180 VAC in our case). When the input AC voltage diminishes, the on–time increases and the primary / secondary RMS current go up. This implies a greater heatsink for the MOSFET but also larger aluminum cans for the secondary filters. For this reason, a transition from Discontinuous Conduction Mode (DCM) to CCM will be envisaged here. Figure 6 depicts these different modes.

Different methods exist to find the point transition takes place (also called the *critical* or *borderline* point). The idea consists in finding the critical inductance Lc that will make the supply enter CCM at 180 VAC. From Figure 6, we can write:

$$ton = \frac{Lp \cdot lp}{VinDC}$$
 (14)

$$toff = \frac{Lp \cdot lp}{N \cdot (Vout + Vf)}$$
 (15)

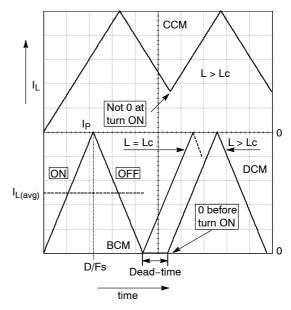


Figure 6. Depending on the Primary Current at Turn-On, the Supply Crosses Various Operating Modes

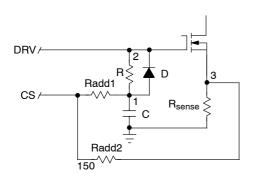


Figure 7. A Very Simple Way to Generate a Ramp from a Square Wave Signal

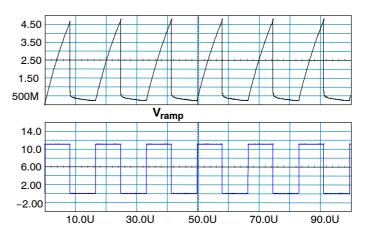


Figure 8. Simulations Show a Capacitor Voltage Ramping Up from a Few Hundred of mV Up to Nearly 5 V

From the Flyback formula, we obtain:

$$Ip = \sqrt{\frac{2 \cdot Pout}{\eta \cdot Lp \cdot FSW}}$$
 (16)

Ip = primary peak current

N = Np / Ns = 1/0.166 = 6

Pout = output power

 $\eta = efficiency$

Lp = primary inductance

Fsw = switching frequency

Vf = secondary diode forward drop

 $VinDC \approx Vac \cdot \sqrt{2}$ (neglecting ripple)

Combining equations 14, 15 and 16 we obtain an Lp value to be in BCM at 180 VAC input voltage:

$$Lp = \frac{(Vout^2 + 2 \cdot Vout \cdot Vf + Vf^2) \cdot (eff \cdot N^2 \cdot Vin^2)}{[Pout \cdot [(N \cdot Vout + N \cdot Vf + Vin)^2 \cdot F_{SW}]] \cdot 2}$$
(17)

The numerical application gives a 484 μ H inductance with a peak current of 2.36 A. The NCP1200 incorporates a skip-cycle feature that forces the controller to slice the switching pattern when the power supply drives light loads. Depending on the system time constants, the recurrence of the burst can enter the audible frequency range. Since the default skip-cycle takes place at one third of maximum peak current, it is better to avoid working at high peak current in normal operation. Should noise still appear in skip mode, pin1 lets you select a different lower skip level (unfortunately to the detriment of the standby power) generating less mechanical noise. As a result, we slightly increased the primary inductance to 700 μ H to further limit the noise in standby operation.

MOSFET Selection

The MOSFET drain voltage sees, in normal operation, a maximum voltage of:

$$VinDC_{max} + (Vout + Vf) \cdot N + Ip \cdot \sqrt{\frac{Lleak}{Clump}}$$
 (18)

The first term represents the maximum rectified DC voltage and goes up to 375 V. The reflected voltage pushes further up by 101 V. Summing up these levels gives a total steady–state drain voltage of 476 V. The last term in equation 18 depicts the leakage inductance action which further stresses the MOSFET at the opening. If we select a 600 V device, it leaves more than 100 V for this leakage action. A clamping network will stop its rise anyway. A 2SK2843 from Toshiba can be a good choice. This is a TO–220 600 V 10 A component which features a 1.2 Ω RDS_(ON) @ Tj = 100°C.

Ramp Compensation

With a supply entering CCM together with a duty-cycle greater than 50%, we need to inject ramp compensation into the controller to prevent subharmonic oscillations. An easy way to generate a ramp, is to take the driving signal available from pin5 and integrate it through a RC network. Figure 7 shows how to wire the components and Figure 8 shows the signal obtain with a 18 $k\Omega\,/\,1$ nF RC time constant.

To calculate the necessary amount of ramp m, several methods exist. We will stick to the standard one which consists in injecting between 50 and 75% of the off-time downslope. The calculation is as follow:

Primary off-slope:

$$\frac{\text{N} \cdot (\text{Vout} + \text{Vf})}{\text{Lp}} = 153 \text{ mA/}\mu\text{s}$$
 (19)

Once reflected over Rsense, it becomes: $50.5 \text{ mV} / \mu s (S')$ Duty-cycle in CCM:

$$D = \frac{\text{Vout}}{\text{N} \cdot \text{Vin} + \text{Vout}} = 45\% \text{ @ Vin} = 120 \text{ VDC}^{(20)}$$

From Figure 7 network, the maximum voltage is given by R and Radd1 + Radd2. With a 11 V driving voltage delivered by the NCP1200, we recommend a 18 k Ω for R and 1 nF for C. These values offer an acceptable tradeoff in terms of power consumption but also in terms of noise immunity. The corresponding time constant of 18 μ s gives a ramp maximum peak voltage of:

$$Vcc = Vdrv \cdot \left(1 - e^{\frac{1}{\tau}}\right) = 5 V$$
 (21)

with $t = 0.45 \times 1/61$ k. This provides an available ramp level of 677 mV/ μ s (S). By setting Radd2 to 1 k Ω , Radd1 can be computed using the following formula:

Radd1 = 1 k
$$\cdot \frac{S}{S' \cdot m}$$
 = 22.5 k Ω

for m = 60%. Final tweak gives an 18 k Ω resistor for Radd1.

Component Constraints

In this section, we will see how Spice can help us to precisely determine all the component constraints and thus calculate the necessary amount of heatsink they need. The simulation schematic we adopted is given on Figure 9 and shows the NCP1200 wired as recommended in the data sheet. Please note that the simulation fixture has been simplified to allow faster simulation time. For instance, the TL431 and its network usually hamper the simulation time to find out the right operating point. A Zener diode and a resistor help finding it in a much quicker way.

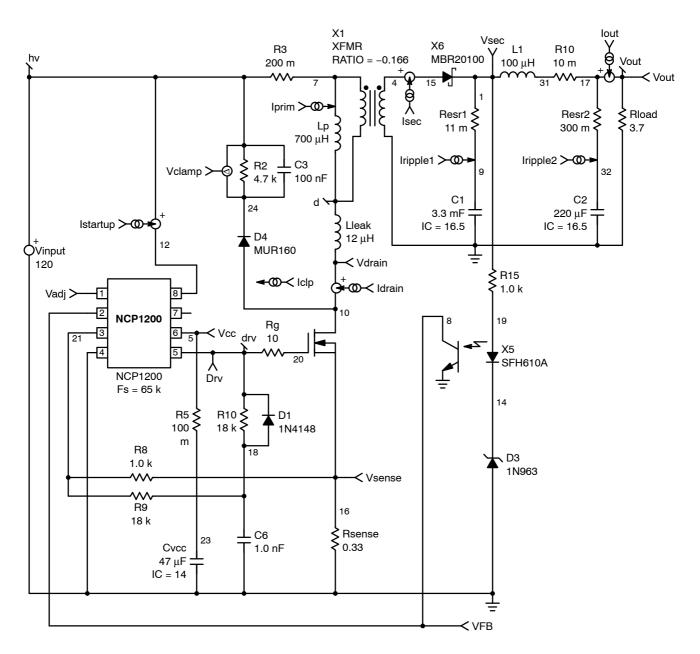


Figure 9. The Simplified Simulation Schematic Helps to Determine All the Component Key Parameters

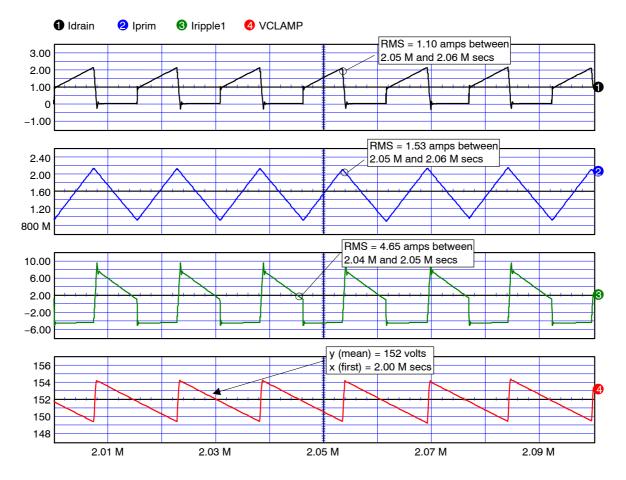


Figure 10. Complete Simulation Results of the 70 W Converter Operated at 120 VDC Input Voltage

Important results appear in Figure 10. Please note that the maximum RMS current occur at the lowest line where the duty-cycle is pushed to the limit.

As you can see, the ramp compensation works fine and no subharmonic oscillations can be noted. Once everything is extracted, below are summarized the most important design constraints:

MOSFET

Rdson @ 100° C = 1.2 ohms RthetaJC = 2.8° C/W Pcond = $1.2 * 1.1^{2} = 1.5$ W

The conduction losses are the strongest at low line. The total simulated losses, including switching events are evaluated to be around 2.6 W. Further breadboard measurements confirmed this number. If we want to keep the junction temperature around 100°C at an ambient of 50°C, then we shall add a proper heatsink according to the following calculation:

$$\begin{split} R\theta_{heatsink-air} &= \frac{(Tj_{max} - Tamb_{max})}{P} \\ &- R\theta_{Junction-Case} - R\theta_{Case-Heatsink} \\ &\approx 15^{\circ}\,C/W \end{split}$$

Lower $R\theta_{heatsink-air}$ resistances can of course be selected to run the device cooler.

Diode

The MBR20100 welcomes two diodes that share nearly equal current thanks to their equal forward drops. The total forward drop dissipation will remain the same but the RMS losses sensitive to the dynamic resistance will divide by two:

 I_{RMS} total = 6.8 A I_{AVG} total = Iout = 4.2 A

 $Rd @ 3.4 A_{rms} = 27 m\Omega$

 $Vf @ 2.2 A_{avg} = 0.7 V$

Pcond for one diode = $3.4^2 \times 0.027 + 2.2 \times 0.7 = 1.85$ W or 3.7 W for the whole TO–220 package. Simulations gives a bit less to 3.4 W. Heat calculations (Tj < 100° C and 50° C ambient) recommend a heatsink of 8° C/W for the MBR20100. As stated before, lower $R\theta_{heatsink-air}$ resistances can of course be selected to run the device cooler.

Capacitors

Icapacitor RMS = 5 A

The paralleling of capacitors will help achieve the right ripple current shared between all the devices. We selected three 2.2 mF capacitors capable of handling 1.7 Arms each.

Transformer

Below are the key parameters you will pass to your transformer manufacturer to help him select the right winding size and tailor the internal gap:

Maximum peak primary current, including 160 ns propagation delay: $1/0.33 + 374 \times 160$ n /700 $\mu = 3.2$ A

Maximum primary RMS current at low line: 1.6 A

Maximum secondary RMS current: 6.9 A

Primary inductance: 700 µH

Turn-ratio, power section: Np:Ns = 1:0.166 Turn-ratio, auxiliary section: Np:Naux = 1:0.15

Clamping Network

The clamping network can be calculated using the following formulae:

$$\label{eq:Rclamp} \begin{split} & = \frac{2 \cdot \text{Vclamp} \cdot (\text{Vclamp} - (\text{Vout} + \text{Vf sec}) \cdot \text{N})}{\text{Lleak} \cdot \text{Ip}^2 \cdot \text{F}_{SW}} \end{split}$$

$$Cclamp = \frac{Vclamp}{Vripple \cdot F_{SW} \cdot Rclamp}$$
 (23)

The power dissipated by Rclamp can also be expressed by:

$$PRclamp = \frac{1}{2} \cdot Lleak \cdot Ip^2 \cdot F_{SW} \cdot \frac{\frac{Vclamp}{(Vout + Vf \text{ sec}) \cdot N}}{\frac{Vclamp}{(Vout + Vf \text{ sec}) \cdot N} - 1}$$
 (2)

with:

Vclamp: the desired clamping level;

Ip: the maximum peak current (e.g. during overload);

Vout + Vf: the regulated output voltage level + the secondary diode voltage drop;

Lleak: the primary leakage inductance;

N: the Ns:Np conversion ratio;

Fsw: the switching frequency;

Vripple: the clamping ripple, could be around 20 V.

With a measured leakage inductance of 12 μH and a final clamping level of 150 V, Rclamp is found to be 4.7 k $\Omega/6$ W and Cclamp 100 nF. The RMS current flowing through Cclamp is 220 mA. RC networks are economical clamping devices and care must be taken to not exceed the MOSFET BVdss in the most stringent conditions, e.g. a cold startup sequence at high line. Worse case arises when Ip is maximum and Vout reaches the target.

Stability Analysis

The stability analysis can be investigated using different approaches. Spice has proven to be rather accurate for feedback loop analysis with SMPS. We will use the NCP1200 average model which is available to download from our web site (www.onsemi.com/pub/ncp1200). Figure 11 shows the simulation template where the feedback network on the TL431 has been simplified to a simple 100nF capacitor.

Thanks to average modeling, the simulation time is kept short and results are delivered in a snap-shot, as testified by Figure 12. Figure 13 unveils the results obtained using a network analyzer and confirms the validity of our approach (Vin = 240 VAC).

Stability has been checked at various line/loads combinations and gave good results. Final transient step did not reveal any overshoot or unwanted oscillations.

The Adapter Schematic

The final schematic implements a current-mode Flyback architecture, driving a 600 V MOSFET. The 2SK2545 features a 10 A capability but a 6.0 A/600 V can also be mounted, such as the FQP6N60 from Fairchild but to the expense of increased conduction losses. Figure 14 offers a complete view of the electrical sketch. The board can actually be used with either auxiliary or without auxiliary winding. By removing the resistance R4, you reactivate the DSS on a NCP120X controller featuring this ability. The board can therefore accept the following controllers:

NCP1200, featuring DSS.

NCP1200A, featuring DSS.

NCP1203, auxiliary winding only.

NCP1216, featuring DSS and internal ramp compensation. Improved EMI jittering with DSS.

NCP1217, auxiliary winding only, internal ramp compensation.

On NCP1216 and 1217, the internal ramp compensation avoids using the external circuitry made of R1–R7–D4 and C10. If one of these two parts are plugged in the demoboard, you must disable this network by simply disconnecting R1 and growing R6 up to $2.7~\mathrm{k}\Omega$ (for a 65 kHz operation).

The NCP120X takes place with two other bipolars that implement a discrete SCR, activated in presence of an OVP, e.g. an optocoupler failure. D5 senses the overvoltage condition and can easily be adjusted to fit any other levels. Thanks to R10, the OVP permanently latches—off the supply and the user shall cycle $V_{\rm CC}$ off and on again to restart the supply. Shutdown is obtained by pulling the feedback pin down through D6. The clamp resistor is split in two different components to avoid an excessive heat burden on one single device. Both main MOSFET and secondary diode are mounted on an adequate heatsink to evacuate the heat.

To ease the designer task, or simply help evaluating the board performance faster, we have experimented different transformers, available through Appendix B manufacturers. Please note that some include the auxiliary winding for DSS deactivation whereas other only offer a dual winding arrangement where the DSS no longer activates and offers the best standby power. All details are given in appendix B. The final demoboard will not accommodate with all these transformers simply because multiple footprints was not possible. They however have all been tested okay. Measurements were taken with the Coilcraft transformer.

As a final note, the actual demoboard delivers 19 VDC/70 W versus the original design-based 16.5 V. As a result, figure 1b circuit has been replaced by L3-R13 and C12 to improve the short-circuit protection when using an auxiliary winding. Output voltage can be adjusted by changing the feedback network made of R12/R20/R21.

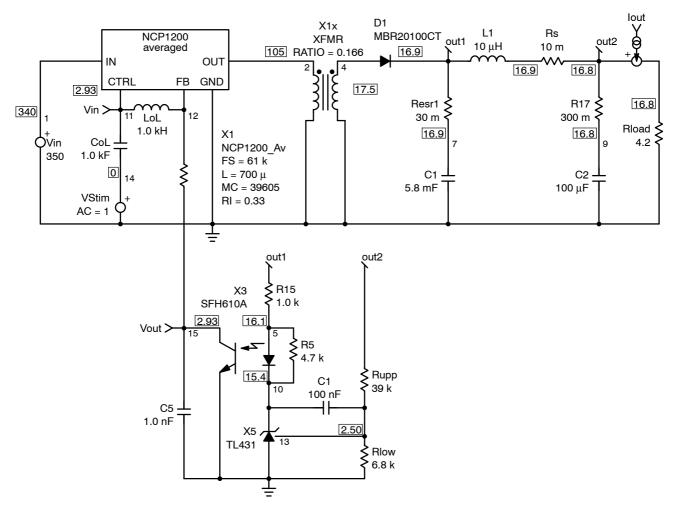


Figure 11. The Simulation Schematic for Our 70 W Current-Mode Power Supply

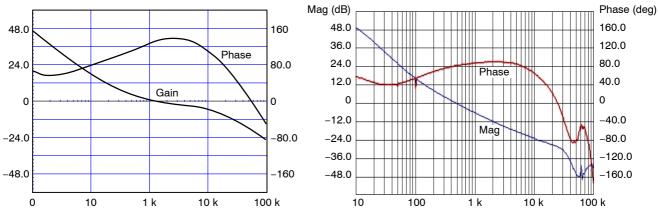


Figure 12. Simulated Bode Plot of the Current Mode Flyback

Figure 13. Measured Open-Loop Gain with a Network Analyzer

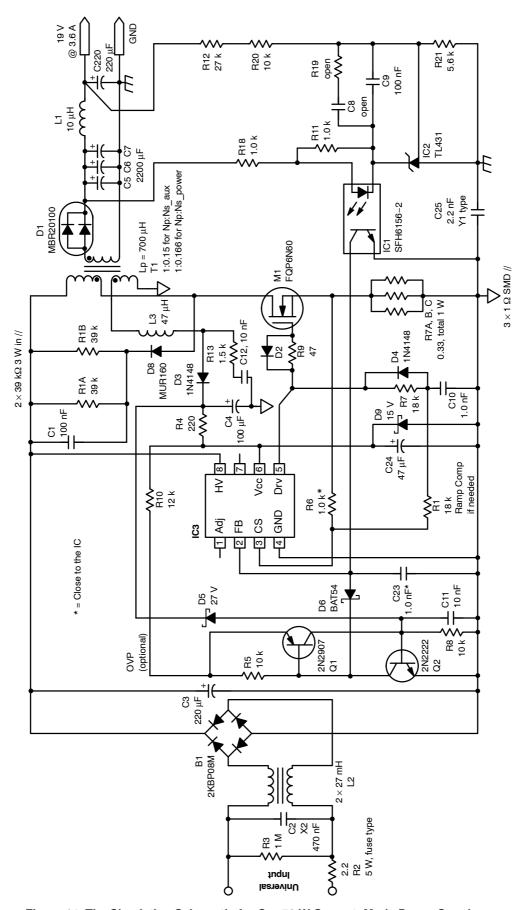


Figure 14. The Simulation Schematic for Our 70 W Current-Mode Power Supply

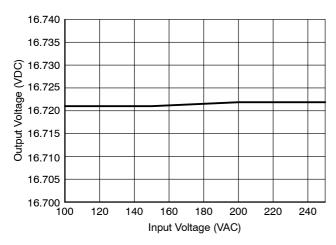


Figure 15. Line Regulation Is Excellent Thanks to Current Mode and a Good Open-Loop DC Gain

Board Final Results

Standby Power

Measured on an *Infratek* watt-meter operated in Watt-hour accumulation mode for best accuracy (run length = 30 minutes).

Vin = 120 VAC, Vout = 16.76 V, Iout = $0 \rightarrow Pin = 78 \text{ mW}$ Vin = 240 VAC, Vout = 16.76 V, Iout = $0 \rightarrow Pin = 84 \text{ mW}$

Line Regulation

The array in Figure 15 shows the performance when the input voltage is moving between both range ends. As one can see, current mode control with good open-loop gain ensures a Δ Vout less than 1 mV for a 212 VDC input variation (-106 dB DC audio susceptibility).

Load Regulation

By varying the load current between 11 W and 70 W, it is possible to plot the load regulation of the board as shown in Figure 16.

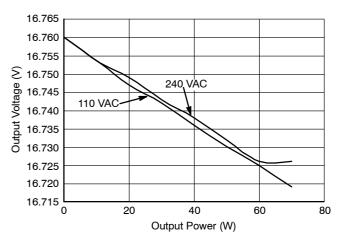


Figure 16. Load Regulation at Two Different Input Voltages

Efficiency

We have designed two boards, one using the auxiliary winding for best standby performance, and another one with the Dynamic Self-Supply (DSS) left normally working. Because of the auxiliary winding, it has been necessary to further clamp the drain voltage in order to improve the primary overload detection. It is not necessary with the DSS and therefore the RCD drain clamp network can be less aggressive, thus slightly improving the efficiency. Board 2 also features a 6 A MOSFET compared to a 3 A MOSFET on board 1.

Board 1, aux. winding: Vin = 110 VAC, η = 79%

Vin = 240 VAC, η = 83.5%

Board 2, DSS: Vin = 110 VAC, η = 83.4%

Vin = 240 VAC, η = 84.8%

TEST PROCEDURE

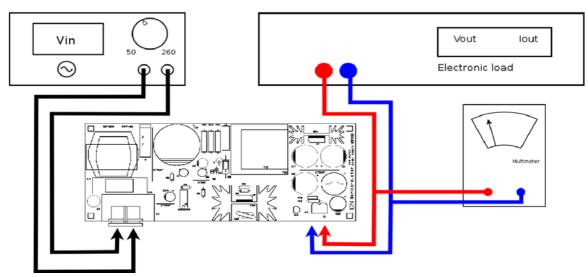


Figure 17. Test Setup

Required Equipment

- ac power supply, 85 265 Vac, 1 A
- Electronic load, 19 V up to 4 A
- Yokogawa power meter WT210
- Multimeter

Test Procedure

- 1. Connect the test setup as shown above.
- 2. Apply an input voltage, 85 Vac < Vin < 265 Vac, 50 or 60 Hz
- 3. Connect electronic load on output connector.

Table 1. DESIRED RESULTS

	Measurements Conditions		Results	Comments	
1	Output Voltage	Vin = 100 Vac no load	Vout = 19 V ±5%		
2	Input consumption at high line and no load	Vin = 230 Vac no load	Pin = 0.09 W ±15% Vout = 19 V	No load measurement can fluctuate, run WT210 in average mode	
3	Output voltage at high line and full load	Vin = 230 Vac Iload = 3.6 A	Vout = 19 V ±5%		
4	Output voltage at low line and full load	Vin = 85 Vac Iload = 3.6 A	Vout = 19 V ±5%		

Appendix A, Bill of Material

All resistors are 5%~1/4~W~SMD~1206 unless otherwise noted.

All SMD capacitors are 1206 SMD 16 V types unless otherwise noted.

All through-hole electrolytic capacitors are radial types unless otherwise noted.

Manufacturer references are given for specific components only.

Table 2. BILL OF MATERIALS

Desig- nator	QTY	Description	Value	Toler- ance	Foot- print	Manufacturer	Manufacturer Part Number	Substi- tution Allowed	RoHS Com- pliant
R1,R7	2	Chip Resistor	18 kΩ, 0.25 W	5%	1206	Vishay	CRCW120618KJN	Yes	Yes
R2	1	Fusible Resistor	2.2 Ω, 5W	10%	Axial	Vishay	CP00052R2JE14	Yes	Yes
R1A,B	2	Power Leaded Resistor	39 kΩ, 3.0 W	2%	Axial	Vishay	PR030203902G	Yes	Yes
R3*	1	Chip Resistor	1.0 MΩ, 0.25 W	5%	1206	Vishay	CRCW12061M0JN	Yes	Yes
R4	1	Chip Resistor	220 Ω, 0.25 W	5%	1206	Vishay	CRCW1206220RJN	Yes	Yes
R5,R8, R20	3	Chip Resistor	10 kΩ, 0.25 W	5%	1206	Vishay	CRCW120610KJN	Yes	Yes
R6, R11	2	Chip Resistor	1.0 kΩ, 0.25 W	5%	1206	Vishay	CRCW12061K0JN	Yes	Yes
R7A, B, C	3	Chip Resistor	1.0 Ω 1.0 W	5%	1218	Vishay	CRCW12181R0JN	Yes	Yes
R9	1	Leaded Resistor	47 Ω, 1/3 W	1%	Axial	Vishay	CCF5047RFKE36	Yes	Yes
R10	1	Chip Resistor	12 kΩ, 0.25 W	5%	1206	Vishay	CRCW120612KJN	Yes	Yes
R12	1	Leaded Resistor	27 kΩ, 1/3 W	5%	Axial	Vishay	CCF5027KJKE36	Yes	Yes
R13	1	Chip Resistor	1.5 kΩ	5%	1206	Vishay	CRCW12061K5JN	Yes	Yes
R18	1	Leaded Resistor	1.0 kΩ, 1/3 W	1%	Axial	Vishay	CCF501K0FKE36	Yes	Yes
R19*	0	Not Installed	-	-	-	-	-	-	-
R21	1	Chip Resistor	5.6 kΩ, 0.25 W	5%	1206	Vishay	CRCW12065K6JN	Yes	Yes
C1	1	Polyester Film Capacitor	100 nF, 400 V	10%	pitch: 15 mm	Vishay	2222-373-53104	Yes	Yes
C2	1	Class X2 Suppression Capacitor	470 nF / X2	10%	pitch: 27.5 mm	Vishay	F1772-447-20	Yes	Yes
СЗ	1	Aluminum Snap-In Capacitor	220 uF, 400 V	20%	Radial	Vishay	2222-157-46221	Yes	Yes
C4	1	Aluminum Capacitor	100 uF, 35 V	20%	Radial	Panasonic	ECA1VM101	Yes	Yes
C5 -7	3	Aluminum Capacitor	2200 uF, 35 V	20%	Radial	Vishay	2222-136-50222	Yes	Yes
C8*	0	Not Installed	-	-	-	-	-	-	-
C9	1	Ceramic Chip Capacitor	100 nF, 16 V	10%	1206	Vishay	VJ1206Y104KXJAT	Yes	Yes
C10, C23	2	Ceramic Chip Capacitor	1.0 nF, 16 V	10%	1206	Vishay	VJ1206Y102KXJAT	Yes	Yes
C11, C12	2	Ceramic Chip Capacitor	10 nF, 16 V	10%	1206	Vishay	VJ1206Y103KXJAT	Yes	Yes
C22	1	Electrolytic Capacitor	470 uF, 35 V	20%	Radial	Panasonic	ECA1VM471	Yes	Yes
C24	1	Electrolytic Capacitor	47 uF, 25 V	20%	Radial	Panasonic	ECEA1EN470U	Yes	Yes
C25	1	Ceramic AC Capacitor, Y1	2.2 nF, 500 V	20%	Disc	Vishay	WKP222MCMBFOK	Yes	Yes
B1	1	Diode Bridge	600 V-4.0 A	NA	NA	Vishay	KBU4J	Yes	Yes
D1	1	Schottky Rectifier	20 A, 100 V	NA	TO 220 3	ON Semiconductor	MBR20100CTG	No	Yes
D2-4	3	1N4148 Diode	75 V, 150 mA	NA	DO-35	Diodes Inc.	1N4148DICT-ND (Digi-Key)	Yes	Yes
D5A	1	Zener Diode	27 V, 500 mW	NA	SOD-122	ON Semiconductor	MMSZ27T1G	No	Yes

Table 2. BILL OF MATERIALS

Desig- nator	QTY	Description	Value	Toler- ance	Foot- print	Manufacturer	Manufacturer Part Number	Substi- tution Allowed	RoHS Com- pliant
D6	1	Schottky Barrier Diode	30 V, 200 mA	NA	DO-35	ST Microelectronics	497-2495-1-ND (Digi-Key)	Yes	Yes
D8	1	Ultrafast Rectifier	1A, 600 V	NA	Axial	ON Semiconductor	MUR160G	No	Yes
D9A	1	Zener Diode	15 V, 500 mW	NA	SOD-123	ON Semiconductor	MMSZ15T1G	No	Yes
IC1	1	Optocoupler	5300 Vrms	NA	SMD-4	Vishay	SFH6156-2	Yes	Yes
IC2	1	Programmable Precision Reference	2.5 - 36 V	2.2%	TO-92	ON Semiconductor	TL431ILPG	No	Yes
IC3	1	Flyback BWM Controller	NA	NA	PDIP	ON Semiconductor	NCP1200P60G	No	Yes
Q1	1	PNP Silicon Plastic Transistor	NA	NA	TO-92	ON Semiconductor	P2N2907AG	No	Yes
Q2	1	NPN Amplifier Transistor	NA	NA	TO-92	ON Semiconductor	P2N2222AG	No	Yes
L1	1	Inductor	10 uH, 5 A	10%	NA	Coilcraft	PCV-2-103-05	Yes	Yes
L2	1	Current-Compensated Double Choke	2 x 27 mH, 1.4 A	30%	NA	EPCOS	B82724-A2142-N1	Yes	Yes
L3	1	Molded Axial Inductor	47 uH, 110 mA	10%	Axial	Vishay	IM02EB470K	Yes	Yes
M1	1	Power MOSFET	500 V, 8 A	NA	TO-220N IS	Toshiba	2SK2543	Yes	Yes
T1	1	Flyback Transformer	0.7 mH	10%	NA	Coilcraft	Z9260-AL or Z9007-BL	No	Yes
Heat- sink 1	1	Heatsink SW (diode)	NA	NA	NA	Seifert- Electronic	KL194/38,1	Yes	Yes
Heat- sink 2	1	Heatsink SW (MOSFET)	NA	NA	NA	Seifert- Electronic	KL195/38,1	Yes	Yes

^{*}Not Installed

Appendix B, Transformer Manufacturers Eldor Corporation Headquarter

Via Plinio 10, 22030 Orsenigo (Como) Italia

Tel.: +39-031-636 111 Fax: +39-031 636 280

eldor@eldor.it www.eldor.it

ref.: 2074.5059A, no aux. winding, P = 70 W

Pulse Engineering

Site d'Orgelet Zone industrielle 39270 – ORGELET Tel.: 33 (0)3 84 35 04 04 Fax: 33 (0)3 84 25 46 41

http://www.pulseeng.com/ Email: vpelletier@pulseeng.com

ref.: PF0082, with auxiliary winding, P = 50 W ref.: PF0091, without auxiliary winding, P = 50 W

Coilcraft

1102 Silver Lake Road Cary, Illinois 60013 USA Tel: (847) 639–6400 Fax: (847) 639–1469

Email: info@coilcraft.com http://www.coilcraft.com

ref. : Z9260-A, with auxiliary winding, P = 70 W ref. : Z9007-B, without auxiliary winding, P = 70 W

Thomson Multimedia - Orega

Route de Noiron

B.P. 24

70101 GRAY Cedex – France Tel: 33 (0)3 84 64 54 26 Fax: 33 (0)3 84 65 18 45 www.thomsonmultimedia.com Email: Bouillotj@thmulti.com

Ref. : G7086-01, no aux. winding, P = 70 W

For Lower Volumes:

Atelier Special de Bobinage

125 cours Jean Jaurès

38130 ECHIROLLES – France Tel.: 33 (0)4 76 23 02 24

Fax: 33 (0)4 76 22 64 89 Email: asb@wanadoo.fr

Ref.: NCP1200-35 W-UM, no aux. winding,

RM10 P = 35 W

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