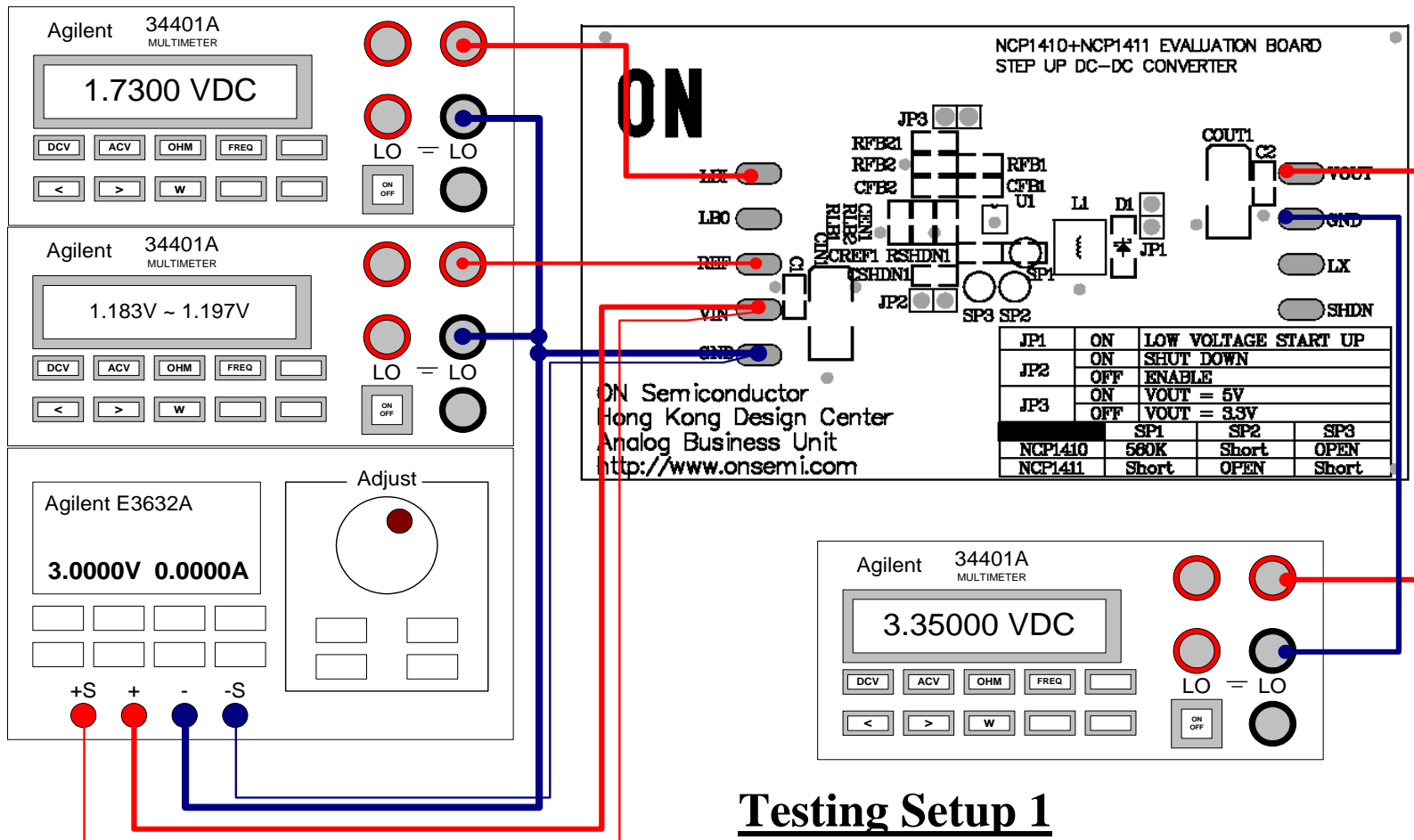


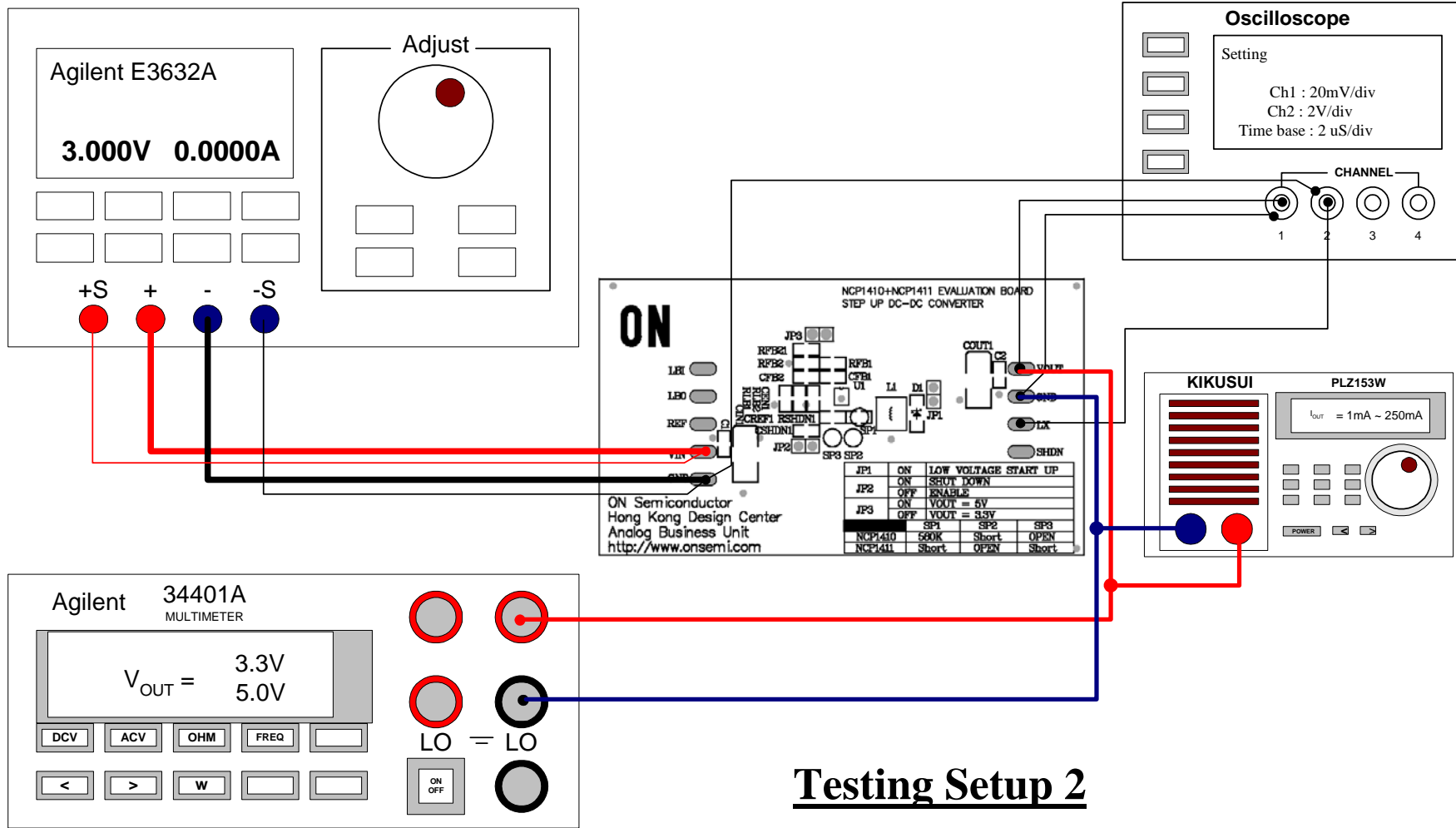
Test Procedure for the NCP1410 and NCP1411 Evaluation Board

ON Semiconductor®



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Testing Setup 2

Testing Procedure:

1. Make sure the device on the demo board is NCP1410 / NCP1411.
2. Connect the test setup as shown in Testing Setup 1.
3. **JP2** = OFF; [Enable the device].
4. Set the Power Supply to 3.0 V and apply across both V_{IN} and GND pins on demo board.
5. Verify the following,
 V_{LBI} (TP7) = 1.73 V
 V_{REF} (TP8) = 1.183 V ~ 1.197 V
 V_{OUT} (TP1) = 3.3 V [**JP3** OFF] / 5.0 V [**JP3** ON]
6. Connect the test setup as shown in Testing Setup 2.
7. Apply 200mA loading from the electronic load after power up the demo board.
8. Check I_{IN} , V_{OUT} , output ripple and L_X pin (TP4)

For NCP1410

[JP3 OFF]	$V_{OUT} = 3.3049 \text{ V to } 3.3448 \text{ V}$
$V_{OUT} = 3.3\text{V}$	$I_{IN} = 265 \text{ mA to } 268.2 \text{ mA}$
	$V_{RIPPLE} < 60 \text{ mV}_{P-P}$
[JP3 ON]	$V_{OUT} = 5.04 \text{ V to } 5.1 \text{ V}$
$V_{OUT} = 5.0\text{V}$	$I_{IN} = 452.5 \text{ mA to } 458 \text{ mA}$
	$V_{RIPPLE} < 80 \text{ mV}_{P-P}$

For NCP1411

[JP3 OFF]	$V_{OUT} = 3.3025 \text{ V to } 3.3424 \text{ V}$
$V_{OUT} = 3.3\text{V}$	$I_{IN} = 263.6 \text{ mA to } 266.8 \text{ mA}$
	$V_{RIPPLE} < 60 \text{ mV}_{P-P}$
[JP3 ON]	$V_{OUT} = 5.0197 \text{ V to } 5.079 \text{ V}$
$V_{OUT} = 5.0\text{V}$	$I_{IN} = 421.2 \text{ mA to } 426.27 \text{ mA}$
	$V_{RIPPLE} < 80 \text{ mV}_{P-P}$