

Test Procedure for the NCP2815GEVB Evaluation Board

Output Power :

ON Semiconductor

- 1- Set Vp = 1.8 V to power supply connector (J16).
- 2- Set an 16 Ω load (resistance) on the output connectors (J8 and J17).
- 3- With the function generator, set a single ended signal at 1 kHz and 0.5 Vrms input signal on the left and right inputs. Apply this signal J5 and J13 connectors.
 - a. On the NCP2815A, as R1 = R2 = R3 = R4 = 10k, OUTL_C and OUTR_C will see 0.5 Vrms. Place an oscilloscope probe on each output. You should get 0.5Vrms output signal with a "perfect sine wave". That is to say no clipping at the minima and maxima of the sine wave.
 - b. On the NCP2815B, the gain is internally set to -1.5 V/V, OUTL_C and OUTR_C will see 0.75 Vrms. Place an oscilloscope probe on each output. You should get 0.75Vrms output signal with a "perfect sine wave". That is to say no clipping at the minima and maxima of the sine wave

Quiescent current :

Check the quiescent current. Place an 16 Ω load on each output (J8, J17), no input signal. Vp set to 1.8V and J6 closed. You should measure around 1.6 mA.

