



Test Procedure for the NCP2824 Evaluation Board

TEST PROCEDURE

A power supply set to 3.6 V and current limit set to at least 1.5 A must be connected to J15 connector to power the NCP2824EVB/D. Also to compensate for parasitic inductance of wires between the power supply and the evaluation board it is highly recommended to connect a 470 μ F electrolytic capacitor to bypass J11 terminal. Like this the device can be evaluated under powering condition very similar that battery power supplies.

These tests are provided in order to guarantee a good assembly of the NCP2843 on its dedicated board, it does not consist in parametric test which is already done at chip level.

1. Shutdown Test

Switches setup for shutdown test:

Symbol	Switch Descriptions
J1	Must be connected to ground (low side)

All other switches must be kept floating.

Tests:

1. Set the switches in the configuration
2. Power the board with a 3.6V power supply limited at 1.5A and bypassed by a 470 μ F electrolytic capacitor.
3. Measure the current on the power supply (must be inferior to 1 μ A)

2. Wake Up Test

Switches setup for wire mode test:

Symbol	Switch Descriptions
J1	Must be connected to VDD (high side)

All other switches must be kept floating.

Tests:

4. Set the switches in the configuration.
5. Power the board with a 3.6V power supply limited at 1.5A and bypassed by a 470 μ F electrolytic capacitor.
6. Measure DC Output voltage on J10 on J11. DC Voltage must be equal to 1.8V
7. Measure DC input voltage on J4-2 on J5-2. DC Voltage must be equal to 1.26V

**Summary:**

Test	Measurement	Results of successful test
Shutdown test	I Supply	$I < 1\mu\text{A}$
Wake up test	VOUTP, VOUTN J4-2, J5_2	VDC=1.8V VDC=1.26V