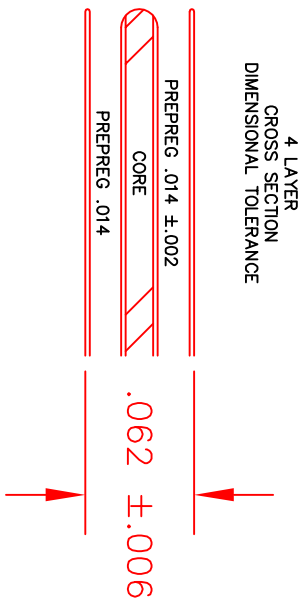
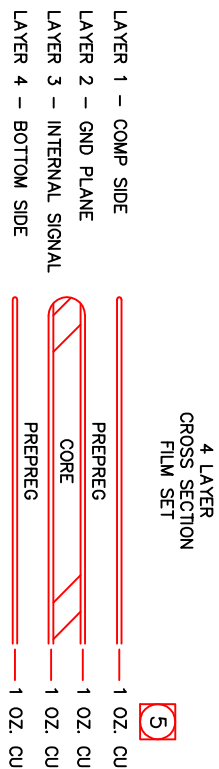


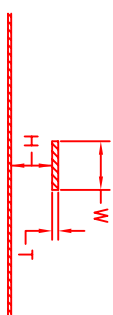
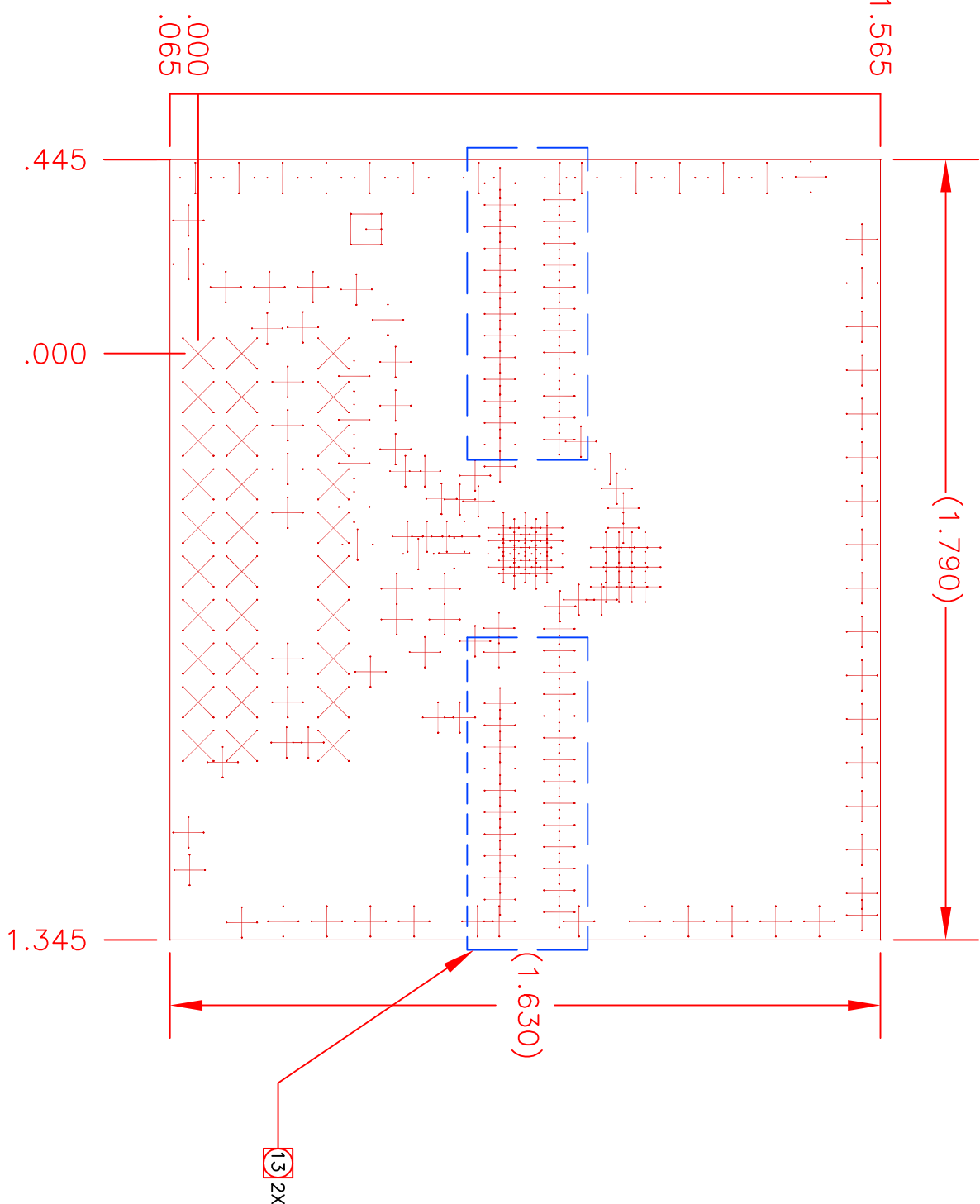
COMPONENT SIDE

NOTES UNLESS OTHERWISE SPECIFIED:

1. FABRICATE 1AW IPC-6012, CLASS 2, CURRENT REVISION.
2. BOARD SHALL MEET THE INSPECTION CRITERIA OF IPC-A-600 CLASS 2, CURRENT REVISION.
3. USE ARTWORK 1081051AW CURRENT REV.
4. MATERIAL: NELCO N4000-13, ROHS COMPLIANT. SEE LAYER STACK UP BELOW.
5. FINISHED WEIGHT OF ALL COPPER LAYERS SHALL BE 1 OZ. PER SQUARE FOOT NOMINAL.
6. DESMEAR HOLES AND VIAS.
7. FINISH: ELECTROLESS NI IMMERSION AU (ENIG). GOLD PLATING THICKNESS TO BE BETWEEN 3 – 8 MICRONS.
8. SILKSCREEN TOP SIDE ONLY WITH NONCONDUCTIVE EPOXY INK. COLOR SHALL BE A CONTRASTING INK WITH RESPECT TO SOLDERMASK COLOR. DISTORTION OF SILKSCREEN IS ACCEPTABLE OVER TRACES. REMOVE EPOXY INK FROM SOLDER LANDS.
9. APPLY SOLDERMASK OVER BARE COPPER (SBOBC) 1AW IPC-SM-840, TOP SIDE ONLY, USING LPI, COLOR CLEAR OR GREEN. SOLDERMASK OVER VIAS IS NOT ACCEPTABLE.
10. TANGENCY IS ACCEPTABLE.
11. REMOVAL OF NON-FUNCTIONAL PADS ON INTERNAL LAYERS IS ACCEPTABLE.
12. SUPPLIER LOGO AND LOT NUMBER/DATE CODE SHALL SHALL NOT BE SILKSCREENED ON BOARD.
13. CALCULATE IMPEDANCE USING MICROSTRIP MODEL. ADJUST RF TRACE WIDTH TO MEET 50 OHMS +/- 5%.
14. INCLUDE A 50 OHM TEST COUPON.



HOLE SCHEDULE (ALL PLATED THRU DIAMETERS ARE AFTER PLATING)			
SYM	DESCRIPTION	PLATED THRU	QTY
+	$\phi.010 +.003/- .010$ THRU	YES	185
X	$\phi.040 \pm .003$ THRU	YES	30
□	$\phi.055 \pm .003$ THRU	YES	1



13 CALCULATE IMPEDANCE
USING MICROSTRIP MODEL
CROSS SECTION

		ITEM NO	PART OR IDENT NO	CAGE CODE	NAME/CAPTURE OR DESCRIPTION		SPEC/STD
QTY PER DASH NO		PARTS LIST OR MATERIAL					
UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN INCHES.							
XX DEC ± .02 XXX DEC ± .005		TOLERANCES ON FINISH .63 RUS √ ± .2" Ø		ANGLES ± 0° 30'			
MATERIAL:		DR	K. KELSEN	3-9-11			
<div>4</div>		CRK					
		ENG C. BLUM		3-9-11			
		WFO					
FINAL PROTECTIVE FINISH:		CR					
<div>7</div>		TR					
		CONF NO.					
		SIZE	CAGE CODE		REV		
		D	14482	1081051PC		-	
SCALE		NONE	W/O NO.		SHEET	1	OF 1

PRINTED CIRCUIT BOARD

TQM8M9077

THAINT

SEMICONDUCTOR

SAN JOSE, CALIFORNIA