

Dear Xilinx Customer,

Thank you for selecting the Xilinx® Virtex-4<sup>™</sup> ML410 Embedded Development Platform. Based on the Xilinx XC4VFX60-11FFG1152CES4S Platform FPGA, the ML410 enables you to rapidly create and evaluate both hardware and software designs. The included 512 MB CompactFlash memory card contains a set of ACE files to run board diagnostics, as well as to demonstrate the operation of various operating systems such as Linux and VxWorks.

Your ML410-P Development Platform includes:

- ML410 board (ATX form factor)
- 256 MB DDR2 DIMM
- 64 MB DDR component memory
- System ACE CF controller
- 512 MB CompactFlash card
- Dual onboard 10/100/1000 Ethernet PHYs
- 4 PCI slots ( 3.3V and 5V)
- LCD character display and cable
- Dual FPGA serial port connections
- Personality module interface

- VGA graphics interface
- Standard JTAG connectivity
- ALi Super I/O
  - Parallel Port
    - 2 USB ports
    - 2 IDE connectors
    - GPIO
    - SMBus Interface
    - AC97 Audio CODEC
    - PS/2 keyboard and mouse ports
- ATX power supply

You can boot up and begin exploring the ML410 Embedded Development Platform by launching the applications on the included CompactFlash card. Follow the instructions in the readme.txt file or the tutorials and documentation at <u>http://www.xilinx.com/ml410</u>. Also, before using any Universal PCI add-in cards see the important instructions included in this box.

Xilinx Integrated Software Environment (ISE), Xilinx Platform Studio (XPS), and a Parallel Cable IV (PC4) JTAG cable are *not* included with the ML410, but are highly recommended for creating a complete hardware and software development environment. These items are available for purchase from Xilinx.

For more information on Xilinx tools, software, and cores:

- <u>http://www.xilinx.com/ise</u> for ISE, the industry-leading programmable logic design tool that lowers total system cost and delivers the fastest time-to-market for logic design.
- <u>http://www.xilinx.com/edk</u> for Xilinx Platform Studio (XPS), the embedded systems design tool for constructing system-level solutions encompassing processors, integrated hardware intellectual property (IP) cores, and board support packages.
- <u>http://www.xilinx.com/ipcenter</u> for access to the latest verified and optimized IP cores, reference designs, and design services for Xilinx FPGAs.

Sincerely,

Advanced Product Marketing

## www.BDTIC.com/XILINX