XILINX®

Overview

The purpose of this notification is to inform Xilinx customer of removal of Xilinx support for *Parity Checking* for PowerPC®405 in all Virtex®-4 FX FPGAs for the "XC" Commercial "C", Industrial "I" and Aerospace/Defense "XQ" devices. There is no change to the form, fit, function, or reliability with this notice.

Description

Xilinx is removing support for *Parity Checking* for PowerPC405 in all Virtex-4 FX FPGAs and updating the following documents to reflect this change:

UG011 - PowerPC Processor Reference Guide
UG018 - PowerPC 405 Processor Block Reference Guide

This change shows minimal impact for most Xilinx customers since *Parity Checking* is disabled by default. Customers must use custom code to actively enable this feature for use.
This feature is already under errata through answer record 20658.

Products Affected

This change includes all Virtex-4 FX FPGA products (XC and XQ), speed (-10, -11, -12 speed) and temperatures grade. The standard part numbers affected by this change are listed in Table 1:

Table 1: **Commercial "XC" Products Affected**

Xilinx Product	Xilinx Product
XC4VFX100-FF1152	XC4VFX20-FF672
XC4VFX100-FF1517	XC4VFX20-FFG672
XC4VFX100-FFG1152	XC4VFX40-FF1152
XC4VFX100-FFG1517	XC4VFX40-FF672
XC4VFX12-FF668	XC4VFX40-FFG1152
XC4VFX12-FFG668	XC4VFX40-FFG672
XC4VFX12SF363	XC4VFX60-FF1152
XC4VFX12SFG363	XC4VFX60-FF672
XC4VFX140-FF1517	XC4VFX60-FFG1152
XC4VFX140-FF1760	XC4VFX60-FFG672
XC4VFX140-FFG1517	

The products affected include all standard part numbers and specification control document (SCD) versions of the standard part numbers listed in Table 1.

Table 2: **Aerospace and Defense "XQ" Products Affected**

Xilinx Product	Xilinx Product
XQ4VFX100-FF1152	XQ4VFX20-FF672
XQ4VFX100-FF1517	XQ4VFX20-FFG672
XQ4VFX100-FFG1152	XQ4VFX40-FF1152
XQ4VFX100-FFG1517	XQ4VFX40-FF672
XQ4VFX12-FF668	XQ4VFX40-FFG1152
XQ4VFX12-FFG668	XQ4VFX40-FFG672
XQ4VFX12SF363	XQ4VFX60-FF1152
XQ4VFX12SFG363	XQ4VFX60-FF672
XQ4VFX140-FF1517	XQ4VFX60-FFG1152
XQ4VFX140-FF1760	XQ4VFX60-FFG672
XQ4VFX140-FFG1517	

Key Dates and Ordering Information

This PCN is active upon release.

Response

No response is required. For additional information or questions, please contact Xilinx Technical Support.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (http://www.xilinx.com/support). Register today and personalize your "MyAlerts" area to include Customer Notifications. Xilinx MySupport provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Xilinx Answer Record 18683.

Additional Documentation

Answer Record 20658:

http://www.xilinx.com/support/answers/20658.htm.

Errata Summary:

ftp://ftp.xilinx.com/pub/documentation/misc/ppc405f6v5_2_0.pdf.

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions
1/11/2010	1.0	Initial release.

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