MT-034 TUTORIAL

Current Feedback (CFB) Op Amps

INTRODUCTION

In this tutorial the two basic op amp topologies—voltage feedback (VFB) and current feedback (CFB)—are discussed in more detail, and the differences are pointed out. The basic voltage feedback op amp along with gain equations is repeated here in Figure 1.

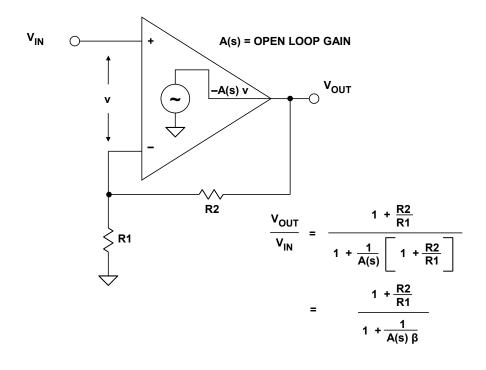


Figure 1: Voltage Feedback Op Amp With Feedback Network Connected

It is important to note that the error signal developed because of the feedback network and the finite open-loop gain A(s) is in fact a small voltage, v.

CURRENT FEEDBACK AMPLIFIER BASICS

The basic *current feedback* amplifier topology is shown in Figure 2. Notice that within the model, a unity gain buffer connects the non-inverting input to the inverting input. In the ideal case, the output impedance of this buffer is zero ($R_0 = 0$), and the error signal is a small current, i, which flows into the inverting input. The error current, i, is mirrored into a high impedance, T(s), and the voltage developed across T(s) is equal to T(s)·i. (The quantity T(s) is generally referred to as the *open-loop transimpedance gain*.)

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This voltage is then buffered, and is connected to the op amp output. If R_0 is assumed to be zero, it is easy to derive the expression for the closed-loop gain, V_{OUT}/V_{IN} , in terms of the R1-R2 feedback network and the *open-loop* transimpedance gain, T(s). The equation can also be derived quite easily for a finite R_0 , and Figure 3 gives both expressions.

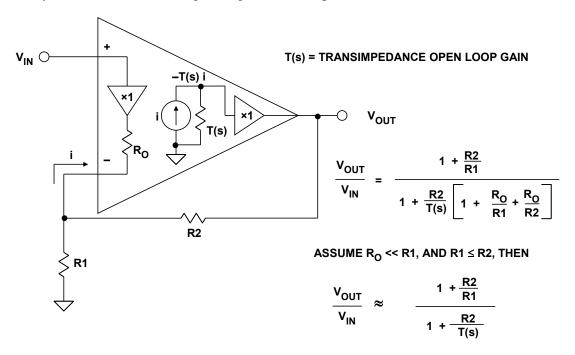


Figure 2: Current Feedback (CFB) Op Amp Topology

At this point it should be noted that current feedback op amps are often called *transimpedance* op amps, because the *open-loop* transfer function is in fact an impedance as described above. However, the term *transimpedance amplifier* is often more correctly applied to more general circuits such as current-to-voltage (I/V) converters, where either CFB or VFB op amps can be used. Therefore, some caution is warranted when the term *transimpedance* is encountered in a given application. On the other hand, the term *current feedback op amp* is rarely confused and is the preferred nomenclature when referring to op amp topology.

From this simple model, several important CFB op amp characteristics can be deduced.

- Unlike VFB op amps, CFB op amps *do not have balanced inputs*. Instead, the non-inverting input is high impedance, and the inverting input is low impedance.
- The open-loop gain of CFB op amps is measured in units of Ω (transimpedance gain) rather than V/V as for VFB op amps.
- For a fixed value feedback resistor R2, the closed-loop gain of a CFB can be varied by changing R1, without significantly affecting the closed-loop bandwidth. This can be seen by examining the simplified equation in Figure 3. The denominator determines the overall frequency response; and if R2 is constant, then R1 of the numerator can be changed (thereby

changing the gain) without affecting the denominator—hence the bandwidth remains relatively constant.

The CFB topology is primarily used where the ultimate in high speed and low distortion is required. The fundamental concept is based on the fact that in bipolar transistor circuits currents can be switched faster than voltages, all other things being equal.

Figure 3 shows a simplified schematic of an early IC CFB op amp, the <u>AD846</u>—introduced by Analog Devices in 1988. Notice that full advantage is taken of the complementary bipolar (CB) process which provides well matched high f_t PNP and NPN transistors.

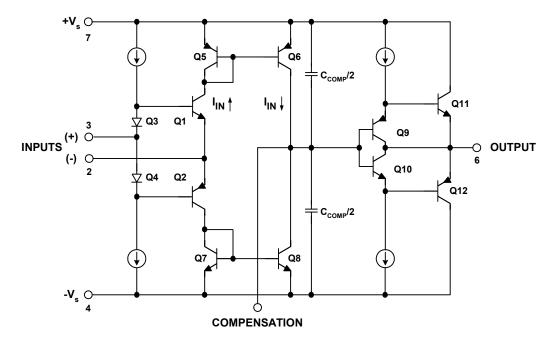


Figure 3: AD846 Current Feedback Op Amp (1988)

Transistors Q1-Q2 buffer the non-inverting input (pin 3) and drive the inverting input (pin 2). Q5-Q6 and Q7-Q8 act as current mirrors that drive the high impedance node. The C_{COMP} capacitor provides the dominant pole compensation; and Q9, Q10, Q11, and Q12 comprise the output buffer. In order to take full advantage of the CFB architecture, a high speed complementary bipolar (CB) IC process is required. With modern IC processes, this is readily achievable, allowing direct coupling in the signal path of the amplifier.

DIFFERENCES BETWEEN CFB AND VFB OP AMPS

One primary difference between the CFB and VFB amps is that the CFB amplifier does not have a constant gain-bandwidth product. While there is a small change in bandwidth with gain, it is much less than the 6 dB/octave we see with a VFB op amp. This is shown in Figure 4. As previously mentioned, the bandwidth of a CFB op amp is proportional to the feedback resistor. For every CFB op amp there is a recommended value of feedback resistor for maximum bandwidth. If you increase the value of the resistor beyond this value, you reduce the bandwidth.

If you use a lower value of resistor below this value, the phase margin is reduced, and the amplifier may become unstable.

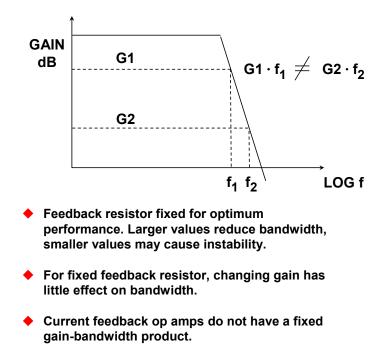


Figure 4: Current Feedback Amplifier Frequency Response

Gain is manipulated in a CFB op amp application by choosing the correct feedback resistor for the device (R2), and then selecting the bottom resistor (R1) to yield the desired closed loop gain. The gain relationship of R2 and R1 is identical to the case of a VFB op amp.

The optimum feedback resistor may be different for different operational conditions. For instance, the value will change for different package types because of the variation in parasitics. Figure 5 shows the optimum feedback resistor for the <u>AD8001</u> op amp for various gains for the PDIP, SOIC, and SOT-23 packages.

Component	AD8001AN (PDIP) Gain				AD8001AR (SOIC) Gain					AD8001ART (SOT-23-5) Gain					
	-1	+1	+2	+10	+100	-1	+1	+2	+10	+100	-1	+1	+2	+10	+100
$R_{\rm F}(\Omega)$	649	1050	750	470	1000	604	953	681	470	1000	845	1000	768	470	1000
$R_{G}(\Omega)$	649		750	51	10	604		681	51	10	845		768	51	10
R_0 (Nominal) (Ω)	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9	49.9
$R_{S}(\Omega)$	0					0					0				
R_T (Nominal) (Ω)	54.9	49.9	49.9	49.9	49.9	54.9	49.9	49.9	49.9	49.9	54.9	49.9	49.9	49.9	49.9
Small Signal BW (MHz)	340	880	460	260	20	370	710	440	260	20	240	795	380	260	20
0.1 dB Flatness (MHz)	105	70	105			130	100	120			110	300	145		

Figure 5: AD8001 Optimum Feedback Resistor vs. Package

Also, a CFB amplifier should not have a capacitor in the feedback loop. If a capacitor is used in the feedback loop, it reduces the net feedback impedance at higher frequencies, which will cause

the op amp to oscillate. Stray capacitance on the inverting input will cause a similar effect and should be minimized by etching away the ground plane around this terminal.

A common error in using a current feedback op amp is to connect the inverting input directly to the output in an attempt to build a unity gain voltage follower (buffer). This circuit will oscillate because the equivalent feedback resistor value is zero. The follower circuit can be stabilized by simply connecting the inverting input to the output using the recommended feedback resistor value.

Another difference between the VFB and CFB amplifiers is that the inverting input impedance of the CFB amp is low (typically 50 Ω to 100 Ω), while the non-inverting input impedance is high (typically several hundred k Ω). Therefore, the inputs of the CFB amp are not balanced, as is the case with VFB amps.

Slew rate performance is also enhanced by the CFB topology. The current that is available to charge and discharge the internal compensation capacitor is available on demand. It is not limited to a fixed value as is often the case in VFB topologies. With a step input, the current is increased (current-on-demand) until the feedback loop is stabilized. The basic current feedback amplifier has no fundamental slew-rate limit. Limits only come about from parasitic internal capacitances, and many strides have been made to reduce their effects.

The combination of higher bandwidths and slew rate allows CFB devices to have good distortion performance, while doing so at a lower power.

The distortion of an amplifier is affected by the open loop distortion of the amplifier and the loop gain of the closed-loop circuit. The amount of open-loop distortion contributed by a CFB amplifier is small due to the basic symmetry of the internal topology. High bandwidth is the other main contributor to low distortion. In most configurations, a CFB amplifier has a greater bandwidth than its VFB counterpart. So at a given signal frequency, the faster part has greater loop-gain and therefore lower distortion. There are, however, certain voltage feedback structures (often referred to as "quad core" or "H-Bridge") which approach the performance levels of CFB and provide "current on demand" when designed on similar processes (Reference 2, Chapter 1, Section 1-6).

SUMMARY: CURRENT FEEDBACK (CFB) VS. VOLTAGE FEEDBACK (VFB)

The application advantages of current feedback and voltage feedback differ. In many applications, the differences between CFB and VFB are not readily apparent. Today's CFB and VFB amplifiers have comparable performance, but there are certain unique advantages associated with each topology. Voltage feedback allows freedom of choice of the feedback resistor (or network) at the expense of sacrificing bandwidth for gain. Current feedback maintains high bandwidth over a wide range of gains at the cost of limiting the choices in the feedback impedance.

In general, VFB amplifiers offer:

- Lower Noise
- Better dc Performance
- Feedback Component Freedom

while CFB amplifiers offer:

- Faster Slew Rates
- Lower Distortion
- Feedback Component Restrictions

REFERENCES:

- Hank Zumbahlen, *Basic Linear Design*, Analog Devices, 2006, ISBN: 0-915550-28-1. Also available as <u>Linear Circuit Design Handbook</u>, Elsevier-Newnes, 2008, ISBN-10: 0750687037, ISBN-13: 978-0750687034. Chapter 1.
- Walter G. Jung, <u>Op Amp Applications</u>, Analog Devices, 2002, ISBN 0-916550-26-5, Also available as <u>Op</u> <u>Amp Applications Handbook</u>, Elsevier/Newnes, 2005, ISBN 0-7506-7844-5. Chapter 1.

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