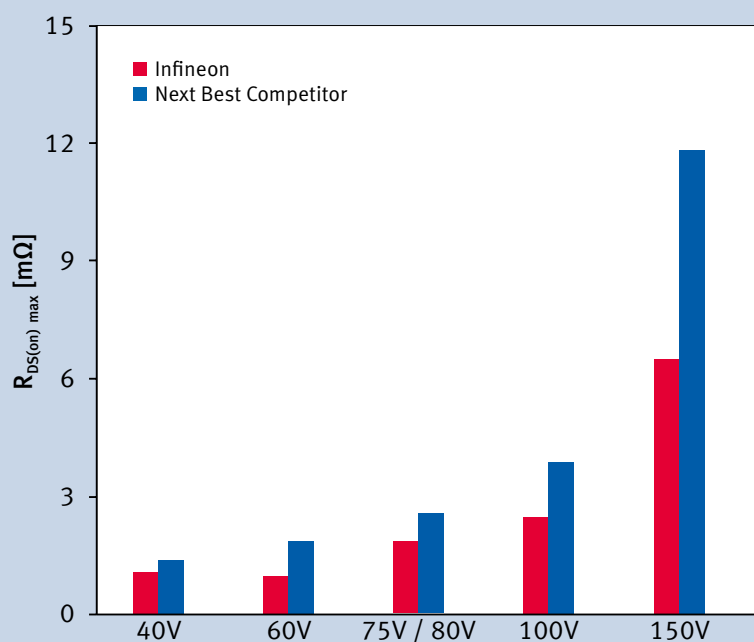




# Turn the Benefits of OptiMOS™ in D<sup>2</sup>PAK 7pin to Your Advantage

Infineon's OptiMOS™ family in D<sup>2</sup>PAK is optimized for high power applications and delivers an ideal combination of the industry's lowest  $R_{DS(on)}$  and the high currents required. The very low  $R_{DS(on)}$  values in D<sup>2</sup>PAK 7pin drastically reduce power losses, lead to less heat in the system and ease thermal management. The D<sup>2</sup>PAK 7pin solves the challenge of hot spot at source in an elegant way by having five pins instead of only one.



## Features

- Industry's lowest  $R_{DS(on)}$
- High current capability
- Very low package parasitics

## Benefits

- System cost improvement
- Highest system reliability due to no hot spot
- Less cooling required

## Applications

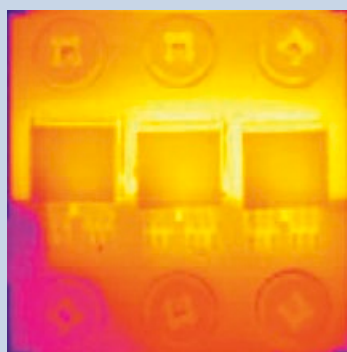
- Forklifts
- eCar
- eScooter
- Golf car
- Neighborhood car
- Power switches
- Battery management
- eFuse

# Turn the Benefits of OptiMOS™ in D<sup>2</sup>PAK 7pin to Your Advantage

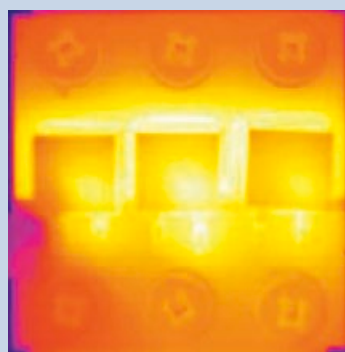
## OptiMOS™ in D<sup>2</sup>PAK 7pin enables more power in your design

D<sup>2</sup>PAK 7pin enables less design effort in high current applications where power losses and thermal management play an essential role.

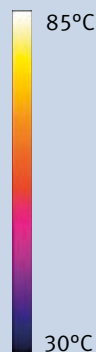
## No hot spot in high power applications by using D<sup>2</sup>PAK 7pin




D<sup>2</sup>PAK 7pin



Standard D<sup>2</sup>PAK



## Portfolio in D<sup>2</sup>PAK 7pin

Package	Type	V <sub>DS</sub> [V]	R <sub>DS(on)</sub> max @ V <sub>gs</sub> =10V [mΩ]
 <p>D<sup>2</sup>PAK 7pin</p>	IPB009N03L G	30	0.95
	IPB011N04L G	40	1.1
	IPB011N04N G	40	1.1
	IPB020N04N G	40	2.0
	IPB010N06N	60	1.0
	IPB016N06L3 G	60	1.6
	IPB017N06N3 G	60	1.7
	IPB023N06N3 G	60	2.3
	IPB034N06N3 G	60	3.4
	IPB019N08N3 G	80	1.9
	IPB030N08N3 G	80	3.0
	IPB025N10N3 G	100	2.5
	IPB039N10N3 G	100	3.9
	IPB036N12N3 G	120	3.6
	IPB065N15N3 G	150	6.5

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Date: 12 / 2011

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